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Institute Profile

Vidyalankar - A Sanskrit word which reveres 'Vidya' (Knowledge) as a precious 'Alankar' (Ornament), the essence being that knowledge is the true ornament of a progressive mind. A trusted and prestigious education specialist, the Vidyalankar Group of Educational Institutes continues to spread the radiance and the bright sparkle of knowledge and education far and wide.

Vidyalankar Institute of Technology is an Engineering Degree and Management College, approved by AICTE and affiliated to the University of Mumbai. Incepted in 1999 and awarded an 'A' Grade by the Govt. of Maharashtra, the institute generates quality engineers and managers with sound academic records and offers excellent placement opportunities. Biomedical and Electronics Engineering Departments are accredited by National Board of Accreditation.

Inspired by the philosophy of "Student Delight", every year VIT welcomes and nurtures around 750 engineering talents at the undergraduate level in Electronics, Electronics and Telecommunication, Information Technology, Computer and Biomedical domains, around 120 Management students and makes them industry-ready. Around 50 graduate engineers hone their skills, expand their knowledge base and orient themselves to R&D while achieving post-graduate qualifications. The institute also has a Ph.D. centre in Computer Engineering.

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Selected Papers will be published in

- 1. AET Journal of Applied Engineering and Technologies ISSN: 2278 1722
- 2. IJCA International Journal of Computer Application
- 3. IJCIT International Journal of Computer and Information Technology
- 4. IJAITE- International Journal of Advanced Innovative Technology in Engineering
- 5. IJIIT International Journal of Integrative Sciences, Innovation and Technology
- 6. BMESI- Journal of Biomedical Engineering Society of India

CONFERENCE VENUE

VIDYALANKAR INSTITUTE OF TECHNOLOGY, VIDYALANKAR MARG, WADALA (EAST), MUMBAI – 400 037

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- 1. Automation, Instrumentation, **Medical Instrumentation and Robotics**
- 2. Signal, Speech and Image Processing
- 3. MEMS Technology & VLSI Design
- 4. Network and Communication Systems, IoT
- 5. Embedded Systems & Control Systems
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IMPORTANT DATES

Commencement of Online Paper July 1, 2016 Submission

Last Date of Online Paper Submission Sept.13, 2016

Notification of Acceptance & Oct. 15, 2016 Commencement of Registration

Submission of Camera Ready Paper Oct. 31, 2016

Last Date of Registration Nov. 15, 2016

Dec. 22 - 23, 2016 Conference Dates

REGISTRATION FEES

Authors	National Participants	International Participants			
Academicians	Rs. 5000/-	USD 200			
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Industry Personnel	Rs. 7000/-	USD 300			
Non-Authors and	on-Authors and Co-Authors				
Academicians	Rs. 2500/-	USD 150			
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