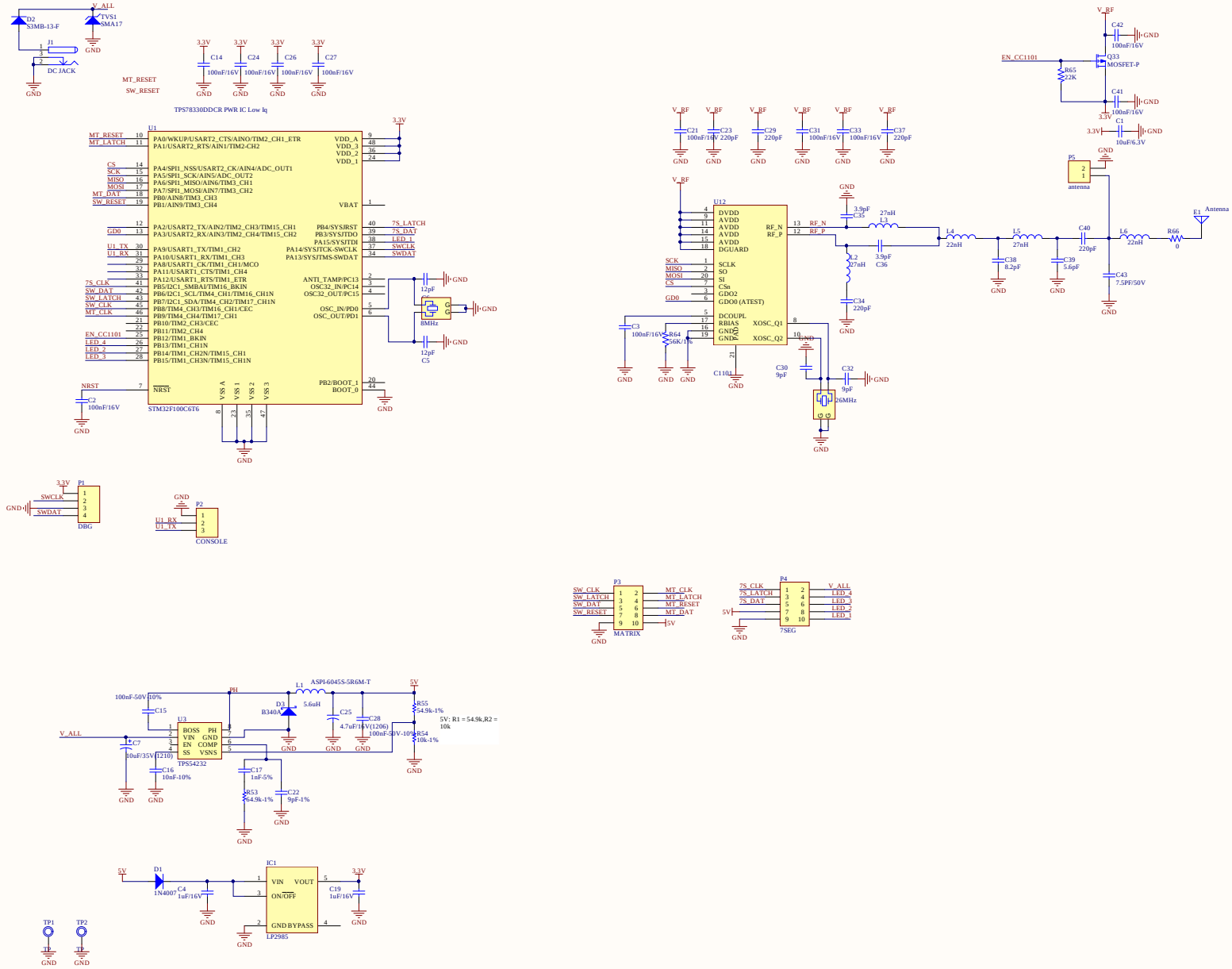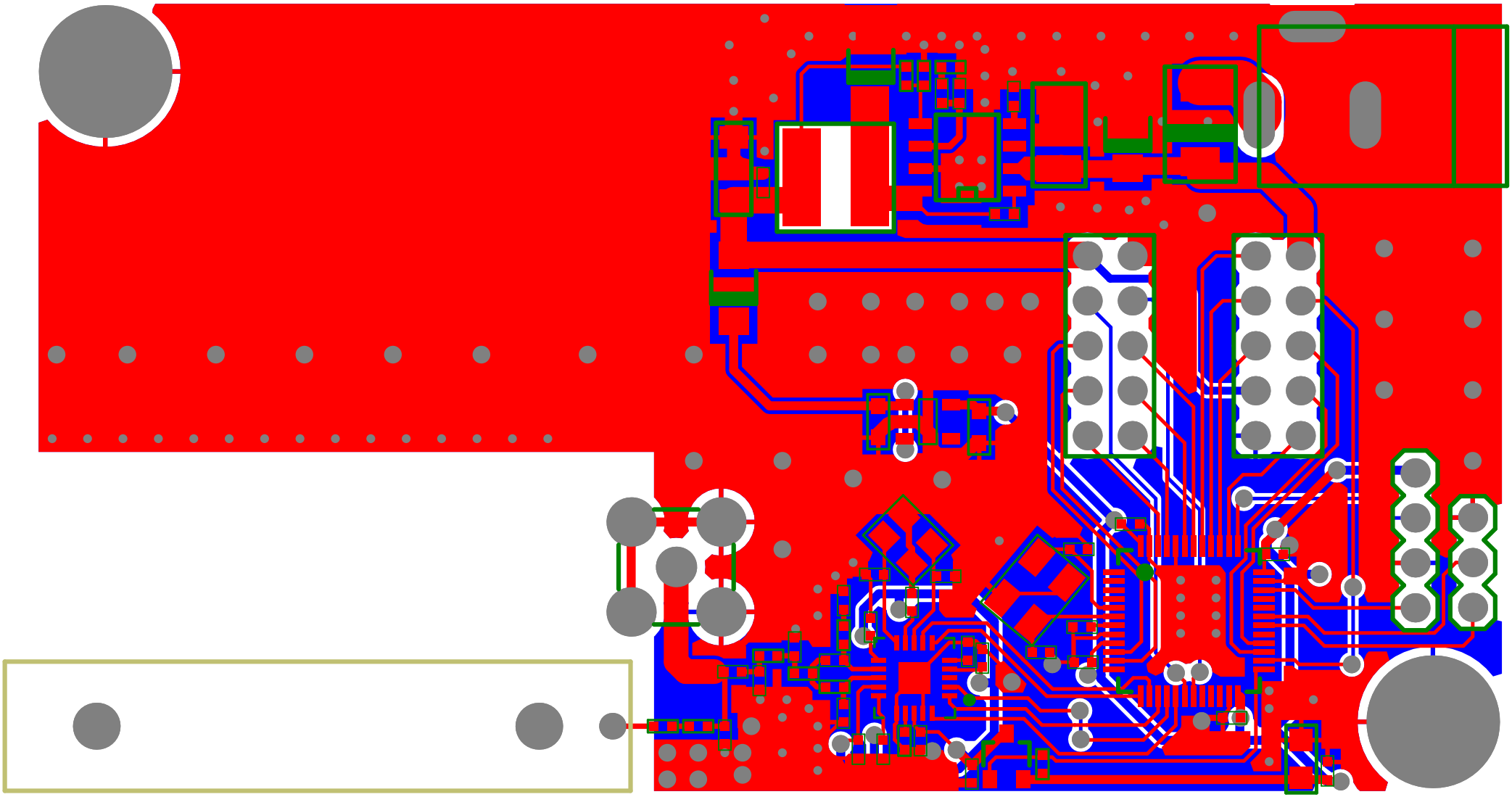
D2
S3MB-13-F
V_ALL
TVS1
SMA17
J1
DC JACK
GND
MT_RESET
SW_RESET
3.3V
C14
C24
C26
C27
100nF/16V
TPS78330DDCR PWR IC Low Iq
U1
MT_RESET
MT_LATCH
CS
SCK
MISO
MOSI
MT_DAT
SW_RESET
GD0
U1_TX
U1_RX
7S_CLK
SW_DAT
SW_LATCH
SW_CLK
MT_CLK
EN_CC1101
LED_4
LED_2
LED_3
NRST
PA0/WKUP/USART2_CTS/AINO/TIM2_CH1_ETR
PA1/USART2_RTS/AIN1/TIM2-CH2
PA4/SPI1_NSS/USART2_CK/AIN4/ADC_OUT1
PA5/SPI1_SCK/AIN5/ADC_OUT2
PA6/SPI1_MISO/AIN6/TIM3_CH1
PA7/SPI1_MOSI/AIN7/TIM3_CH2
PB0/AIN8/TIM3_CH3
PB1/AIN9/TIM3_CH4
PA2/USART2_TX/AIN2/TIM2_CH3/TIM15_CH1
PA3/USART2_RX/AIN3/TIM2_CH4/TIM15_CH2
PA9/USART1_TX/TIM1_CH2
PA10/USART1_RX/TIM1_CH3
PA8/USART1_CK/TIM1_CH1/MCO
PA11/USART1_CTS/TIM1_CH4
PA12/USART1_RTS/TIM1_ETR
PB5/I2C1_SMBAI/TIM16_BKIN
PB6/I2C1_SCL/TIM4_CH1/TIM16_CH1N
PB7/I2C1_SDA/TIM4_CH2/TIM17_CH1N
PB8/TIM4_CH3/TIM16_CH1/CEC
PB9/TIM4_CH4/TIM17_CH1
PB10/TIM2_CH3/CEC
PB11/TIM2_CH4
PB12/TIM1_BKIN
PB13/TIM1_CH1N
PB14/TIM1_CH2N/TIM15_CH1
PB15/TIM1_CH3N/TIM15_CH1N
VDD_A
VDD_3
VDD_2
VDD_1
VBAT
PB4/SYSJRST
PB3/SYSJTDO
PA15/SYSJTDI
PA14/SYSJTCK-SWCLK
PA13/SYSJTMS-SWDAT
ANT1_TAMP/PC13
OSC32_IN/PC14
OSC32_OUT/PC15
OSC_IN/PD0
OSC_OUT/PD1
PB2/BOOT_1
BOOT_0
7S_LATCH
7S_DAT
LED_1
SWCLK
SWDAT
STM32F100C6T6
VSS A
VSS 1
VSS 2
VSS 3
C2
100nF/16V
12pF
8MHz
C5
P1
DBG
P2
CONSOLE
P3
MATRIX
P4
7SEG
5V
V_RF
C42
C41
C1
10uF/6.3V
EN_CC1101
Q33
MOSFET-P
R65
22K
P5
antenna
E1
Antenna
U12
DVDD
AVDD
DGUARD
SCLK
SO
SI
CSn
GDO2
GDO0 (ATEST)
DCOUPL
RBIAS
GND2
GND1
RF_N
RF_P
XOSC_Q1
XOSC_Q2
C21
C23
C29
C31
C33
C37
220pF
C35
3.9pF
L3
27nH
L2
27nH
C36
3.9pF
C34
220pF
L4
22nH
L5
27nH
C38
8.2pF
C39
5.6pF
C40
220pF
L6
22nH
R66
0
C43
7.5PF/50V
C3
100nF/16V
R64
56K/1%
C110
C30
9pF
C32
9pF
26MHz
L1
ASPI-6045S-5R6M-T
5.6uH
D3
B340A
C25
4.7uF/16V(1206)
C28
100nF-50V-10%
R55
54.9k-1%
R54
10k-1%
5V: R1 = 54.9k,R2 = 10k
100nF-50V-10%
C15
U3
BOOS
VIN
EN
SS
PH
GND
COMP
VSNS
TPS54232
C7
10uF/35V(1210)
C16
10nF-10%
C17
1nF-5%
R53
64.9k-1%
C22
9pF-1%
D1
1N4007
C4
1uF/16V
IC1
VIN
VOUT
ON/OFF
GND
BYPASS
LP2985
C19
1uF/16V
TP1
TP2
TP
Title
Size
A2
Number
Revision
Date: 29/03/2016
Sheet of
File: D:\IKYL\MAIN_CALL_BUT.SchDoc
Drawn By: