

# Unit - II 2.2 KVL, KCL and Problems

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Toolchest -

Clacuit Analysis Syllabus

UNIT – II 14 Periods

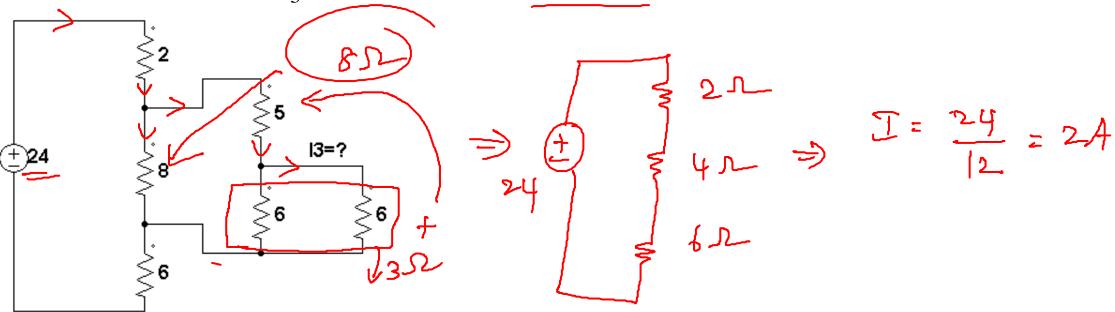
**DC Circuit Analysis:** Voltage source and current sources, ideal and practical, Kirchhoff's laws and applications to network solutions using mesh analysis, - Simplifications of networks using series- parallel, Star/Delta transformation, DC circuits-Current-voltage relations of electric network by mathematical equations to analyse the network (Superposition theorem, Thevenin's theorem, Maximum Power Transfer theorem), Transient analysis of R-L, R-C and R-L-C Circuits.

**AC Steady-state Analysis:** AC waveform definitions - Form factor - Peak factor - study of R-L - R-C -RLC series circuit - R-L-C parallel circuit - phasor representation in polar and rectangular form - concept of impedance - admittance - active - reactive - apparent and complex power - power factor, Resonance in R-L-C circuits - 3 phase balanced AC Circuits

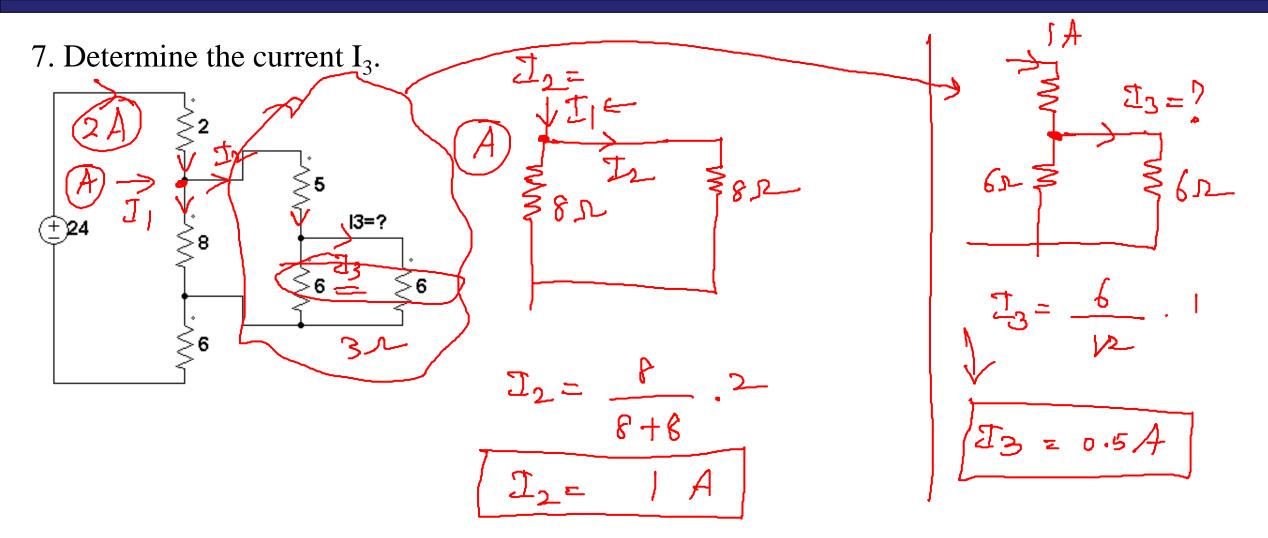


Determine the current  $I_3$ .



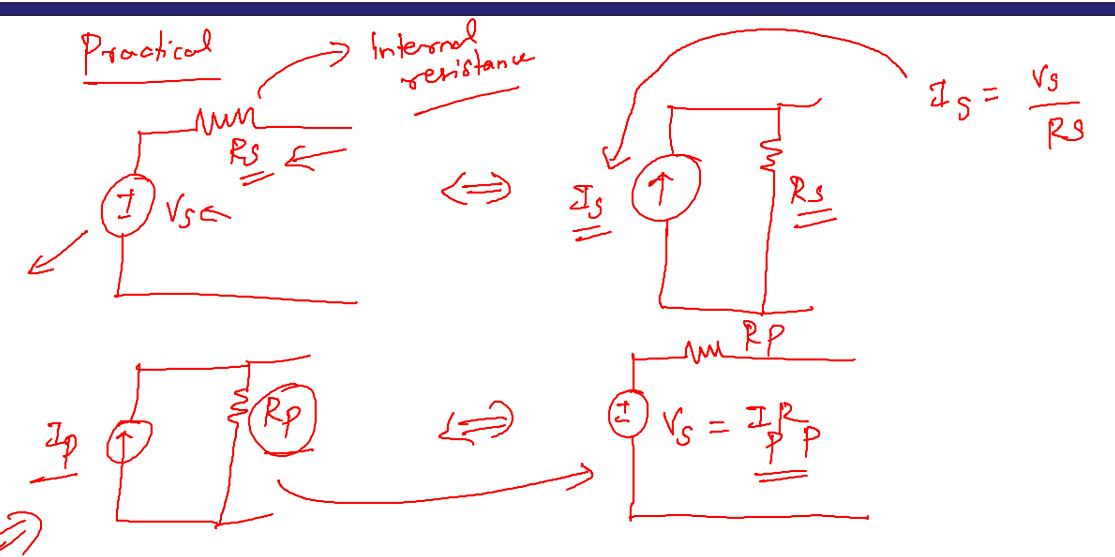








## **Source Transformation**



Two coils connected in series have a resistance of 18  $\Omega$  and when connected in parallel of 4  $\Omega$ .

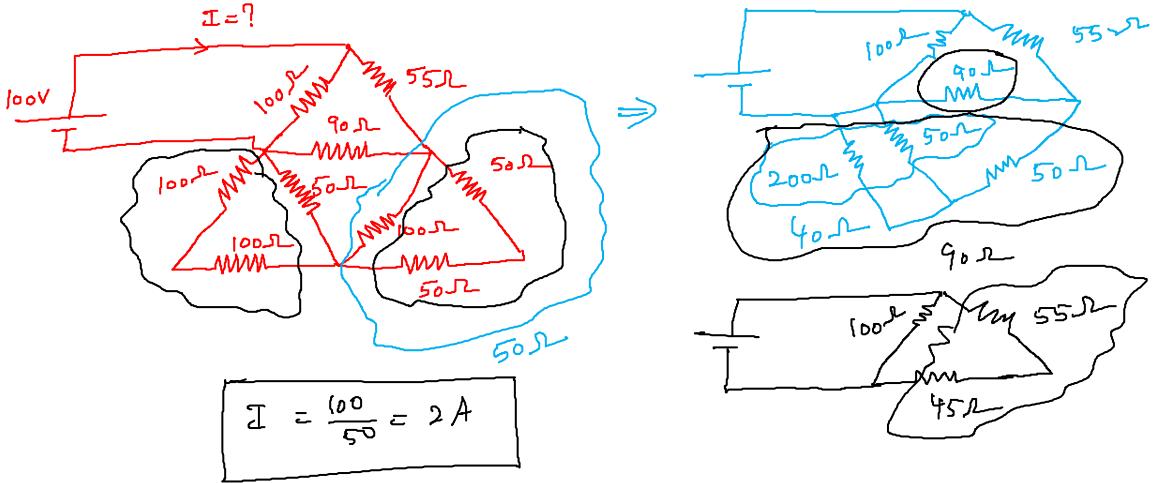
Find the value of resistance of the two coils.

$$R_1 + R_2 = 18$$
  
 $R_1 // R_2 = 4$ 

$$R_1 = 121$$
 $R_2 = 65$ 

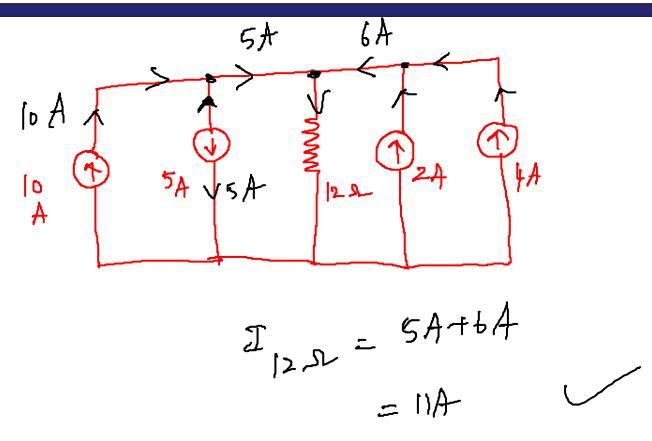


Determine the total current taken from the source.



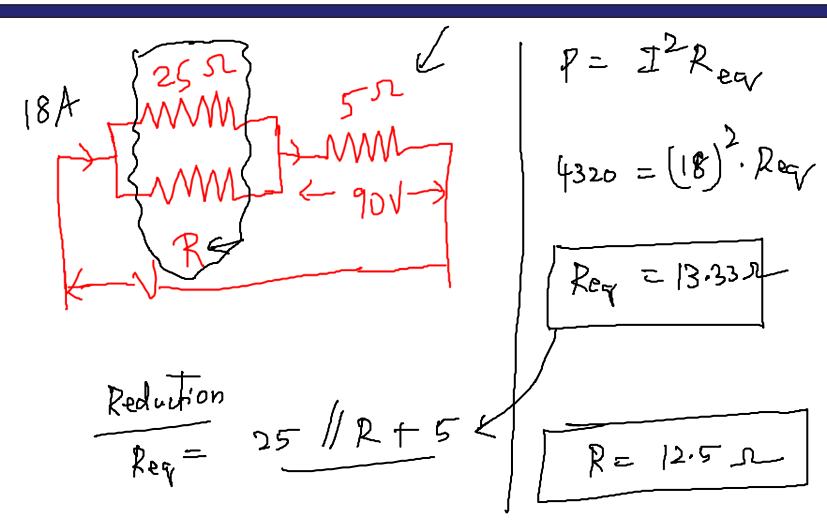


## **Current through 12 ohm resistor?**





## Find the R and V if the total power consumed is 4320 W

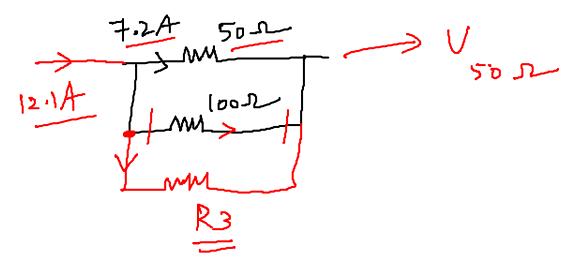


**Given Source Current is 18A** 



Exercise

A 50  $\Omega$  resistor is in parallel with a 100  $\Omega$  resistor. The current at 50  $\Omega$  resistor is 7.2 A. What is the value of third resistor to be added in parallel to make the total current as 12.1 A.





## Kirchhoff's Laws

#### KCL:

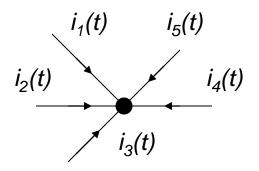
"The sum of the currents flowing towards a junction is equal to the sum of current flowing away from it".

#### KVL:

Kirchhoffs Voltage Law or **KVL**, states that "in any closed loop network, the total voltage around the loop is equal to the sum of all the voltage drops within the same loop" which is also equal to zero. In other words the algebraic sum of all voltages within the loop must be equal to zero



## KCL (Kirchhoff's Current Law)



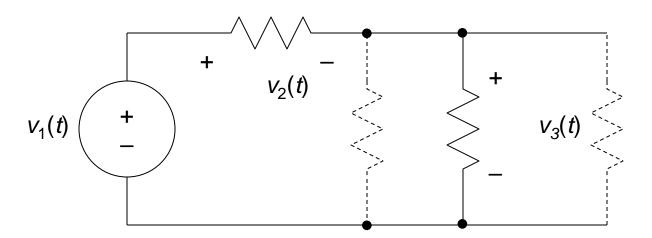
The sum of currents entering the node is zero:

$$\sum_{j=1}^{n} i_j(t) = 0$$

Analogy: mass flow at pipe junction



## **KVL** (Kirchhoff's Voltage Law)



The sum of voltages around a loop is zero:

$$\sum_{j=1}^{n} v_j(t) = 0$$

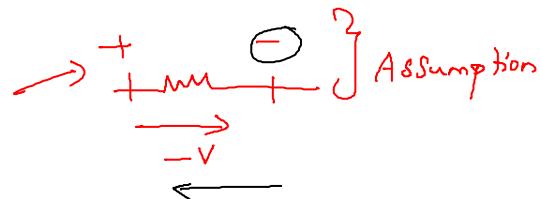
• Analogy: pressure drop thru pipe loop



## **KVL Polarity**

A loop is any closed path through a circuit in which no node is

encountered more than once



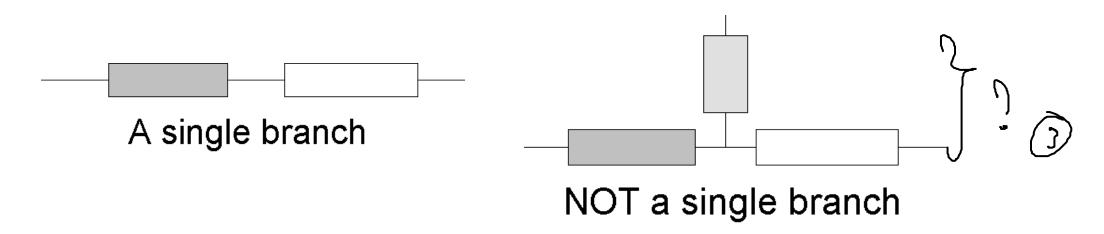
- Voltage Polarity Convention
  - A voltage encountered + to is negative
  - A voltage encountered to + is positive



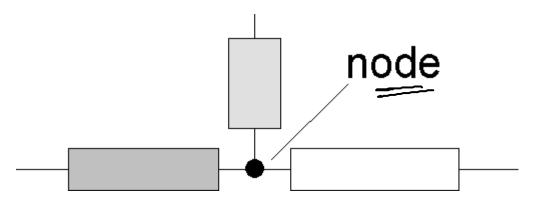


## **BRANCHES AND NODES**

Branch: elements connected end-to-end, nothing coming off in between (in series)

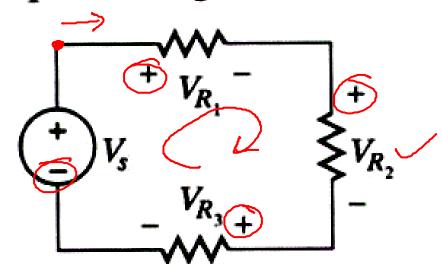


Node: place where elements are joined—entire wire

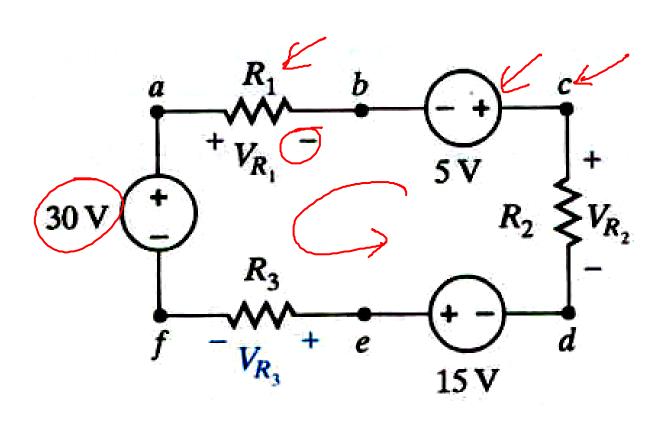


# KVL, KCL

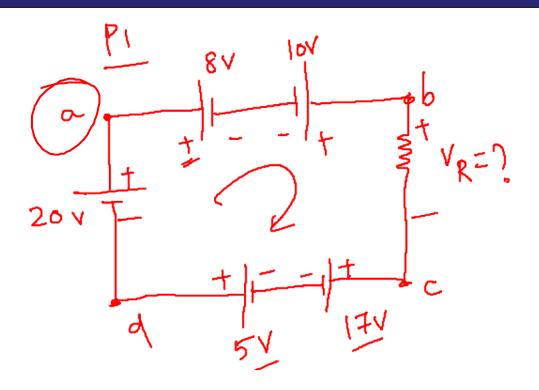
# Write the KVL equation for the following loop, traveling clockwise:



$$-V_{R_1}$$
 -  $V_{R_2}$  -  $V_{R_3}$  +  $V_5$  = 0

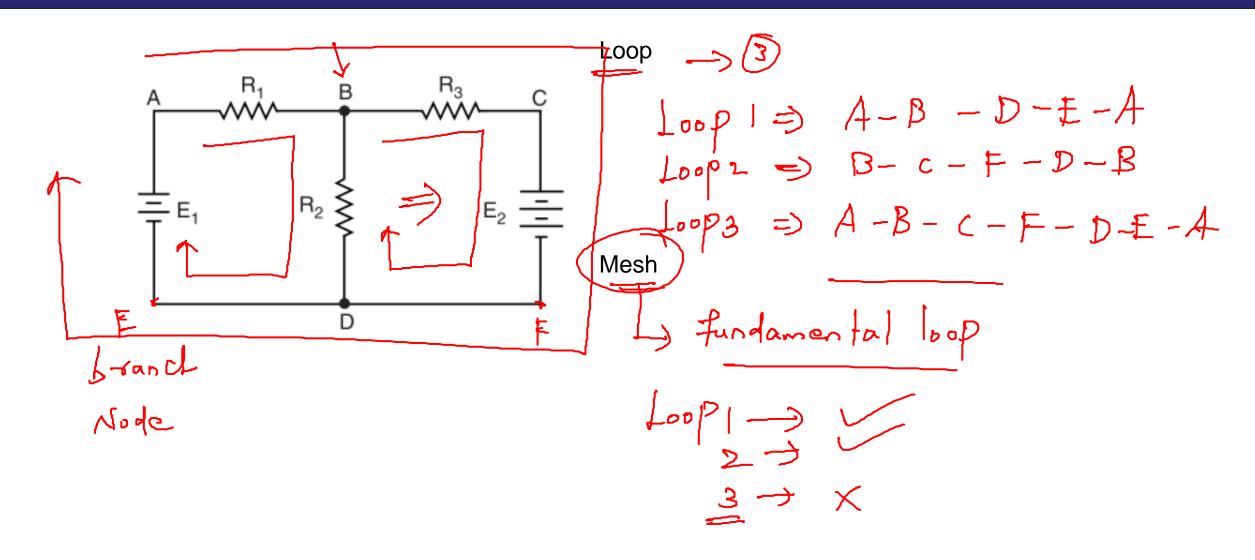








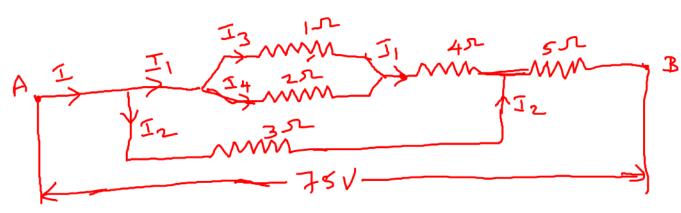
## **Network Terminologies**



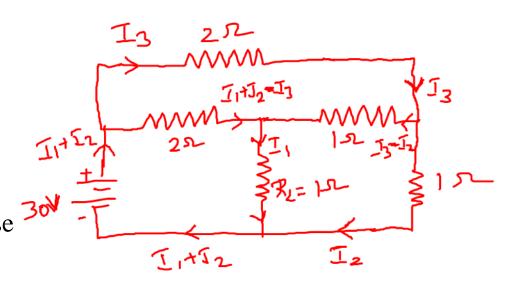


## **Practice Problem**

• Determine the effective resistance of the seriesparallel combination shown in the figure. Also, find the current, voltage and power dissipated in each of the resistor in the given circuit.



• Find the load current in the given circuit (Use KVL).





## **Summary**