1. **Using a timer clock source of 8 MHz, calculate PSC and ARR values to get a 60 Hz interrupt.**

* This is tricky because precisely 60 Hz is impossible with our system; instead, think about the process and minimize the error. Many combinations of PSC and ARR values work—not just one!

1. **Look through the Table 13 "STM32F072x8/xB pin definitions" in the chip datasheet and list all pins that can have the timer 3 capture/compare channel 1 alternate function.**

* If the pin is included on the LQFP64 package that we are using, list the alternate function number that you would use to select it.

1. **List your measured value of the timer UEV interrupt period from first experiment.**
2. **Describe what happened to the measured duty-cycle as the CCRx value increased in PWM mode 1.**
3. **Describe what happened to the measured duty-cycle as the CCRx value increased in PWM mode 2.**
4. **Include at least one logic analyzer screenshot of a PWM capture.**
5. **What PWM mode is shown in figure 3.6 of the lab manual (PWM mode 1 or 2)?**