

SENSOR NETWORKS LAB (PR WS14/15)

Lab 4: Advanced UDP communication

Sanjeet Raj PANDEY [313631]
Sanjay Santhosh KUMAR
Muhammad Tauseef KHAN [346301]
Rajibul Alam KHAN

Supervisor:
Vlado HANDZISKI
Tim BORMANN



Telecommunication Networks Group
Technical University Berlin

Exercise 4.3: *Memory Layout* Give an example for a 1-byte, 2-byte and 4-byte alignment. Use the following sequence: 16 bit, 16 bit, 8 bit, 32 bit and 16 bit.

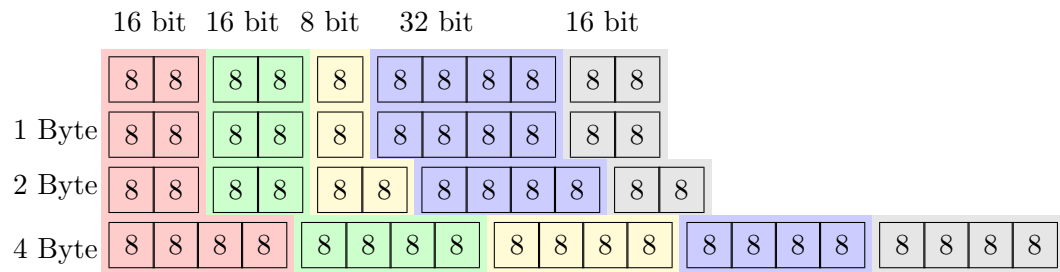


Figure 1: Alignment in General way for 16,16,8,32,16 bits arrangement

As we can see from diagram how alignment can get in problem if not kept consistent and memory must be mapped correctly. In diagram above is shown memory arrangement needed but endianness will effect while saving on these allocated memory . It could be most significant bit first or least significant first depending on architecture .