

No. 110526178

**Bachelor of Technology
(Electrical & Electronics)**

Examination: Dec-2019

Semester 3

Name of Student : SINGH ARJUN JAGDISH

Father's/Husband's Name : JAGDISH Enrollment No : 1911L0106697

Mother's Name : UMA Roll No : 19116518

Subject Code	Subjects	External Marks		Internal Marks		Total Marks OBTD	Result/ Remark
		MAX	OBTD	MAX	OBTD		
BT55E301	Mathematics - III	80	59	40	27	86	Pass
BT55E302	Numerical Analysis	80	46	40	34	80	Pass
BT55E303	Basic Electronics	80	50	40	24	74	Pass
BT55E304	Electric Circuits	80	54	40	27	81	Pass
BT55E305	Electrical Power Generation	80	53	40	22	75	Pass
BT55E306	Electrical Machines - I	80	56	40	29	85	Pass
BT55E307	Electrical Machines Lab - I	-	-	60	37	37	Pass
BT55E308	Basic Electronics Lab	-	-	60	32	32	Pass
BT55E309	Electric Circuits Lab	-	-	60	47	47	Pass
BT55E310	Programming with C Lab	-	-	60	42	42	Pass
BT55E311	Value Education	-	-	40	25	25	Pass
Grand Total		480	318	520	346	664	

Total Marks Obtained (In words) : Six Hundred Sixty Four

Result : Pass

Division : First

Date of Issue : 07/03/2020



Prepared By

Checked By

Controller of Examination

No. 110623875

**Bachelor of Technology
(Electrical & Electronics)
Examination: September-2020
Semester 4**

Name of Student : SINGH ARJUN JAGDISH

Father's/Husband's Name : JAGDISH Enrollment No : 1911L0106697

Mother's Name : UMA Roll No : 19116518

Subject Code	Subjects	External Marks		Internal Marks		Total Marks OBTD	Result/Remark
		MAX	OBTD	MAX	OBTD		
BT55E401	Network Analysis and Synthesis	80	55	40	25	80	Pass
BT55E402	Electromagnetic Theory	80	38	40	20	58	Pass
BT55E403	Electrical Measurements and Measuring Instruments	80	44	40	31	75	Pass
BT55E404	Electrical Power Systems	80	60	40	22	82	Pass
BT55E405	Analog Electronics	80	57	40	28	85	Pass
BT55E406	Digital Electronics and Logic Design	80	54	40	21	75	Pass
BT55E407	Electrical Measurements and Measuring Instruments Lab	-	-	60	36	36	Pass
BT55E408	Analog Electronics Lab	-	-	60	47	47	Pass
BT55E409	Digital Electronics and Logic Design Lab	-	-	60	38	38	Pass
BT55E410	Programming Lab (Mat Lab)	-	-	60	32	32	Pass
BT55E411	Health, Hygiene & Yoga	-	-	40	29	29	Pass
Grand Total		480	308	520	329	637	

Total Marks Obtained (In words) : Six Hundred Thirty Seven

Result : Pass

Division : First

Date of Issue : 09/12/2020



Prepared By

Checked By

Controller of Examination

No. 110724938

**Bachelor of Technology
(Electrical & Electronics)**

Examination: December-2020

Semester 5

Name of Student : SINGH ARJUN JAGDISH

Father's/Husband's Name : JAGDISH Enrollment No :1911L0106697

Mother's Name : UMA Roll No :20163861

Subject Code	Subjects	External Marks		Internal Marks		Total Marks OBTD	Result/ Remark
		MAX	OBTD	MAX	OBTD		
BT55E501	Microprocessor and Peripherals	80	43	40	25	68	Pass
BT55E502	Integrated Circuits and Applications	80	53	40	32	85	Pass
BT55E503	Linear Control Systems	80	55	40	33	88	Pass
BT55E504	Signals and Systems	80	60	40	21	81	Pass
BT55E505	Electrical Machines-II	80	62	40	35	97	Pass
BT55E506	Communication Systems	80	38	40	29	67	Pass
BT55E507	Electrical Machines - II Lab	-	-	60	26	26	Pass
BT55E508	Linear Control Systems Lab	-	-	60	49	49	Pass
BT55E509	Microprocessor and Peripherals Lab	-	-	60	47	47	Pass
BT55E510	Integrated Circuits and Applications Lab	-	-	60	34	34	Pass
BT55E511	Personality Development	-	-	20	17	17	Pass
BT55E512	Practical Training - Evaluation / Library	-	-	20	13	13	Pass
Grand Total		480	311	520	361	672	

Total Marks Obtained (In words) : Six Hundred Seventy Two

Result : Pass

Division : First

Date of Issue : 14/04/2021



Prepared By

Checked By

Controller of Examination

No. 110826161

**Bachelor of Technology
(Electrical & Electronics)**

Examination: June-2021

Semester 6

Name of Student : SINGH ARJUN JAGDISH

Father's/Husband's Name : JAGDISH Enrollment No : 1911L0106697

Mother's Name : UMA Roll No : 20163861

Subject Code	Subjects	External Marks		Internal Marks		Total Marks OBTD	Result/ Remark
		MAX	OBTD	MAX	OBTD		
BT55E601	Electrical Power System -II	80	65	40	26	91	Pass
BT55E602	Modern Instrumentation Techniques	80	39	40	20	59	Pass
BT55E603	Advanced Microprocessors and Peripherals	80	46	40	29	75	Pass
BT55E604	Power Electronics Devices and Circuits	80	59	40	35	94	Pass
BT55E605	Digital Signal Processing	80	51	40	21	72	Pass
BT55E606	Medical Electronics	80	63	40	29	92	Pass
BT55E607	Digital Signal Processing Lab	-	-	60	27	27	Pass
BT55E608	Power System Simulation Lab	-	-	60	37	37	Pass
BT55E609	Power Electronics Devices and Circuits Lab	-	-	60	31	31	Pass
BT55E610	Advanced Microprocessors and Peripherals Lab	-	-	60	43	43	Pass
BT55E611	Managerial Skills	-	-	40	23	23	Pass
Grand Total		480	323	520	321	644	

Total Marks Obtained (In words) : Six Hundred Forty Four

Result : Pass

Division : First

Date of Issue : 23/08/2021



Prepared By

Checked By

Controller of Examination

No. 110925888

**Bachelor of Technology
(Electrical & Electronics)**

Examination: December-2021

Semester 7

Name of Student : SINGH ARJUN JAGDISH

Father's/Husband's Name : JAGDISH Enrollment No : 1911L0106697

Mother's Name : UMA Roll No : 21162817

Subject Code	Subjects	External Marks		Internal Marks		Total Marks OBTD	Result/ Remark
		MAX	OBTD	MAX	OBTD		
BT55E701	Switchgear and Protection	80	64	40	26	90	Pass
BT55E702	Soft Computing and its Applications	80	50	40	35	85	Pass
BT55E703	Electrical Drives	80	62	40	29	91	Pass
BT55E704	Microcontrollers and Applications	80	47	40	32	79	Pass
BT55E705	Power Apparatus System	80	45	40	21	66	Pass
BT55E706	Switchgear & Protection Lab	-	-	60	34	34	Pass
BT55E707	Electric Drives Lab	-	-	60	42	42	Pass
BT55E708	Microcontrollers and Applications Lab	-	-	60	29	29	Pass
BT55E709	Minor Project	-	-	140	89	89	Pass
BT55E710	Innovative and Entrepreneurial Skills	-	-	40	22	22	Pass
BT55E711	Practical Training - Evaluation / Library	-	-	40	23	23	Pass
Grand Total		400	268	600	382	650	

Total Marks Obtained (In words) : Six Hundred Fifty

Result : Pass

Division : First

Date of Issue : 07/03/2022



Prepared By

Checked By

Controller of Examination

No. 111026736

**Bachelor of Technology
(Electrical & Electronics)**

Examination: June-2022

Semester 8

Name of Student : SINGH ARJUN JAGDISH

Father's/Husband's Name : JAGDISH Enrollment No : 1911L0106697

Mother's Name : UMA Roll No : 21162817

Subject Code	Subjects	External Marks		Internal Marks		Total Marks OBTD	Result/Remark
		MAX	OBTD	MAX	OBTD		
BT55E801	High Voltage Engineering	80	41	40	33	74	Pass
BT55E802	Management Concepts and Techniques	80	50	40	22	72	Pass
BT55E803	Computer Aided Power System	80	48	40	31	79	Pass
BT55E804	EHV AC and DC Transmission	80	65	40	30	95	Pass
BT55E805	Electrical Estimation and Costing	80	57	40	27	84	Pass
BT55E806	Computer Simulation Lab	-	-	60	49	49	Pass
BT55E807	Installation Maintenance & Testing of Electrical Equipment's Lab	-	-	60	39	39	Pass
BT55E808	High Voltage Engineering Lab	-	-	60	32	32	Pass
BT55E809	Major Project	-	-	180	124	124	Pass
BT55E810	Report Writing and Seminar	-	-	40	28	28	Pass
Grand Total				400	261	600	415
GRAND TOTAL				6000			676
RESULT							DIV
PASS							FIRST

Total Marks Obtained (In words) : Three Thousand Nine Hundred Forty Three

Date of Issue : 28/07/2022



Prepared By

Checked By

Controller of Examination

