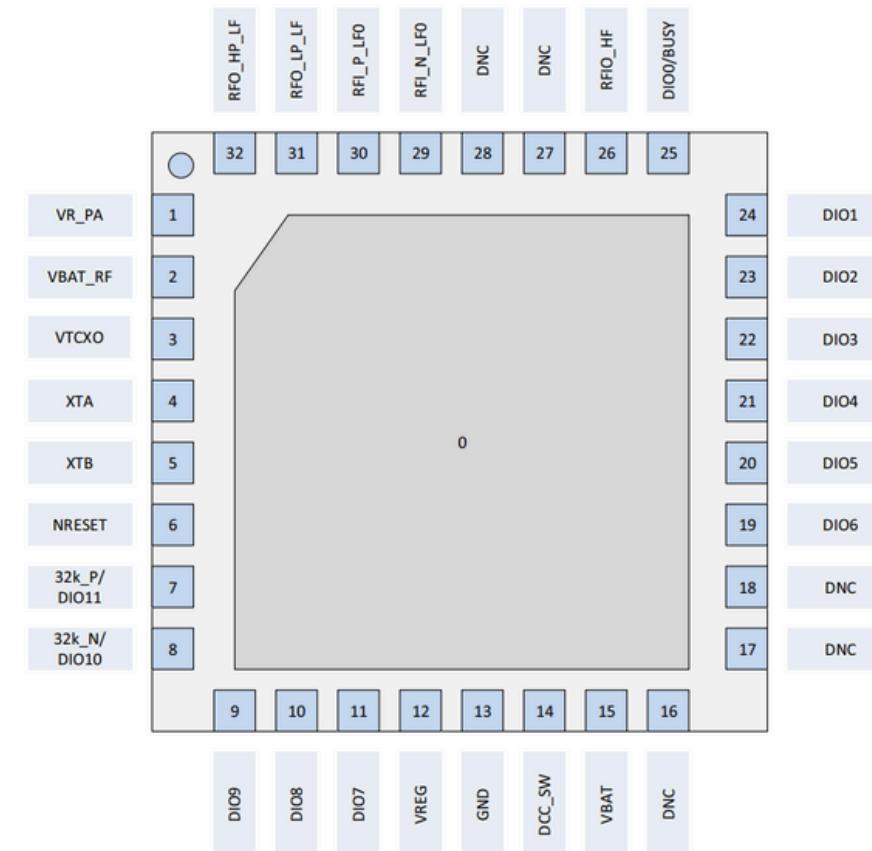


Figure 2-1: LR1121 Pinout



#define LR_MOSI 4	GPIO5 MISO
#define LR_MISO 5	GPIO4 MOSI
#define LR_SCK 6	GPIO6 SCK
#define LR_NSS 7	GPIO7 CS
#define LR_BUSY 3	GPIO3 BUSY
#define LR_NRST 2	GPIO1 IRQ
#define LR_IRQ 1	GPIO2 NRST
#define ARGB 8	GPIO8 ARGB
#define TX2 19	DIO6 PA_V1
#define RX2 18	DIO5 PA_V2

Table 4-1: LR1121 DIO Mapping

Pin	I/O Name	Function
6	NRESET	NRESET
7	32k_P/ DIO11	32k_P/ NC
8	32k_N/ DIO10	32k_N/ RFSW4
9	DIO9	IRQ
10	DIO8	RFSW3
11	DIO7	RFSW2
19	DIO6	RFSW1
20	DIO5	RFSW0
21	DIO4	SPI MISO
22	DIO3	SPI MOSI
23	DIO2	SPI SCK
24	DIO1	SPI NSS
25	DIO0/ BUSY	BUSY