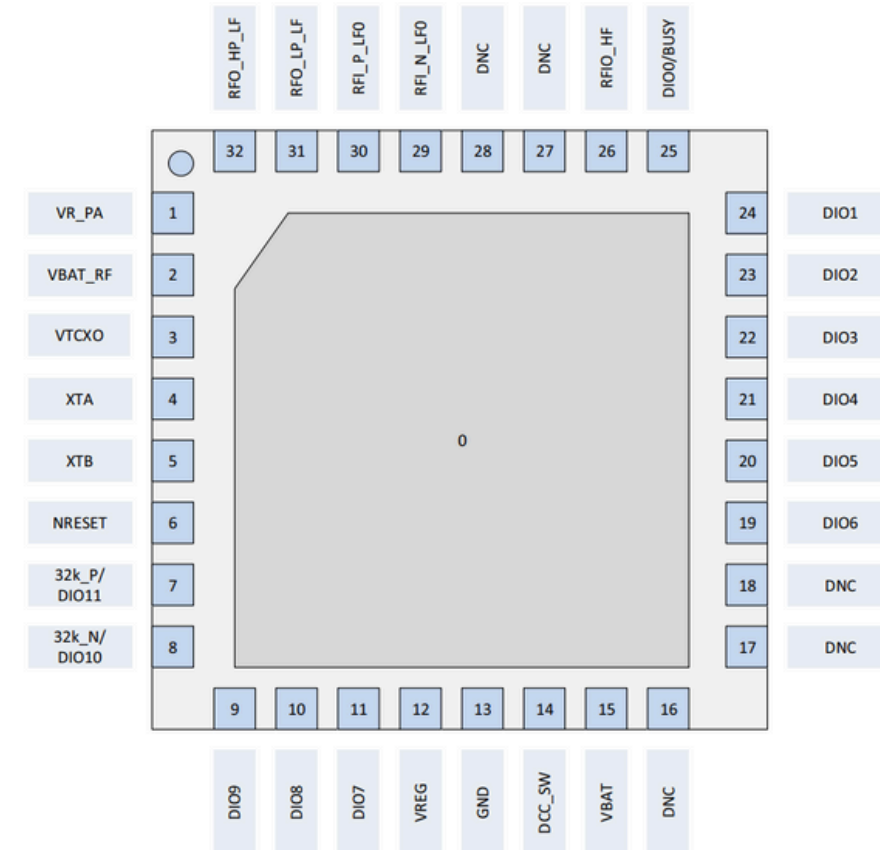


Figure 2-1: LR1121 Pinout



```
#define LR_MOSI 4
#define LR_MISO 5
#define LR_SCK 6
#define LR_NSS 7
#define LR_BUSY 3
#define LR_NRST 2
#define LR_IRQ 1
#define ARGB 8
#define TX2 19
#define RX2 18

GPIO5 MISO
GPIO4 MOSI
GPIO6 SCK
GPIO7 CS
GPIO3 BUSY
GPIO1 IRQ
GPIO2 NRST
GPIO8 ARGB
DIO6 PA_V1
DIO5 PA_V2
```

Table 4-1: LR1121 DIO Mapping

Pin	I/O Name	Function
6	NRESET	NRESET
7	32k_P/ DIO11	32k_P/ NC
8	32k_N/ DIO10	32k_N/ RFSW4
9	DIO9	IRQ
10	DIO8	RFSW3
11	DIO7	RFSW2
19	DIO6	RFSW1
20	DIO5	RFSW0
21	DIO4	SPI MISO
22	DIO3	SPI MOSI
23	DIO2	SPI SCK
24	DIO1	SPI NSS
25	DIO0/ BUSY	BUSY