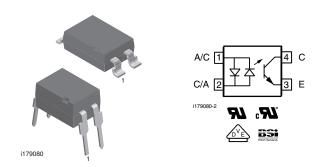


Vishay Semiconductors

Optocoupler, Phototransistor Output, AC Input

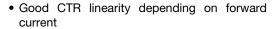


DESCRIPTION

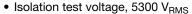
The SFH620A (DIP) and SFH6206 (SMD) feature a high current transfer ratio, low coupling capacitance and high isolation voltage. These couplers have a GaAs infrared diode emitter, which is optically coupled to a silicon planar phototransistor detector, and is incorporated in a plastic DIP-4 or SMD package.

The coupling devices are designed for signal transmission between two electrically separated circuits. The couplers are end-stackable with 2.54 mm lead spacing. Creepage and clearance distances of > 8 mm are achieved with option 6. This version complies with IEC 60950 (DIN VDE 0805) for reinforced insulation to an operation voltage of 400 V_{RMS} or DC.

FEATURES



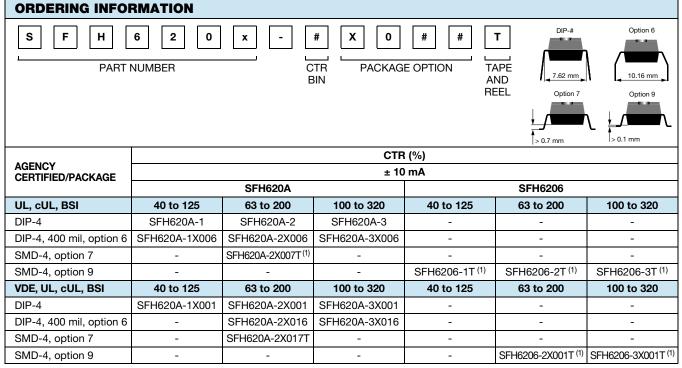




- High collector emitter voltage, V_{CEO} = 70 V
- Low saturation voltage
- Fast switching times
- · Low CTR degradation
- Temperature stable
- Low coupling capacitance
- End-stackable, 0.100" (2.54 mm) spacing
- High common-mode interference immunity
- Compliant to RoHS Directive to 2002/95/EC and in accordance WEEE 2002/96/EC

AGENCY APPROVALS

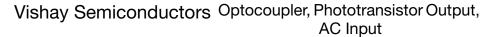
- UL1577, file no. E52744 system code H, double protection
- CSA 93751
- BSI IEC 60950; IEC 60065
- DIN EN 60747-5-2 (VDE 0884)/DIN EN 60747-5-5 (pending), avialable with option 1



Note

- Additional options may be possible, please contact sales office.
- (1) Also available in tubes; do not add T to end.

SFH620A, SFH6206





| ABSOLUTE MAXIMUM RATINGS (Tar | _{nb} = 25 °C, unless otherwise | specified) | | | | | |
|--|---|-------------------|--------------------|-----------|--|--|--|
| PARAMETER | TEST CONDITION | SYMBOL | VALUE | UNIT | | | |
| INPUT | | | | | | | |
| DC forward current | | I _F | ± 60 | mA | | | |
| Surge forward current | t _p ≤ 10 μs | I _{FSM} | ± 2.5 | Α | | | |
| Power dissipation | | P _{diss} | 100 | mW | | | |
| OUTPUT | | | | | | | |
| Collector emitter voltage | | V _{CE} | 70 | V | | | |
| Emitter collector voltage | | V _{EC} | 7 | V | | | |
| Collector current | | I _C | 50 | mA | | | |
| Collector current | t _p ≤ 1 μs | I _C | 100 | mA | | | |
| Power dissipation | | P _{diss} | 150 | mW | | | |
| COUPLER | | | | | | | |
| Isolation test voltage between emitter and detector | t = 1 s | V_{ISO} | 5300 | V_{RMS} | | | |
| Isolation voltage | | V _{IORM} | 890 | V_P | | | |
| Total power dissipation | | P _{tot} | 250 | mW | | | |
| Creepage distance | | | ≥ 7 | mm | | | |
| Clearance distance | | | ≥ 7 | mm | | | |
| Insulation thickness between emitter and detector | | | ≥ 4 | mm | | | |
| Comparative tracking index per DIN IEC112/ VDE 0303, part 1 | | СТІ | 175 | | | | |
| Isolation resistance | $V_{IO} = 500 \text{ V}, T_{amb} = 25 ^{\circ}\text{C}$ | R _{IO} | ≥ 10 ¹² | Ω | | | |
| ISOIATION RESISTANCE | V _{IO} = 500 V, T _{amb} = 100 °C | R _{IO} | ≥ 10 ¹¹ | Ω | | | |
| Storage temperature range | | T _{stg} | - 55 to + 150 | °C | | | |
| Ambient temperature range | | T _{amb} | - 55 to + 100 | °C | | | |
| Junction temperature | | Tj | 100 | °C | | | |
| Soldering temperature (1) | max. 10 s, dip soldering distance | T _{sld} | 260 | °C | | | |

Notes

Stresses in excess of the absolute maximum ratings can cause permanent damage to the device. Functional operation of the device is not implied at these or any other conditions in excess of those given in the operational sections of this document. Exposure to absolute maximum ratings for extended periods of the time can adversely affect reliability.

(1) Refer to reflow profile for soldering conditions for surface mounted devices (SMD). Refer to wave profile for soldering conditions for through hole devices (DIP).

| ELECTRICAL CHARACTERISTICS (T _{amb} = 25 °C, unless otherwise specified) | | | | | | | |
|--|---|-----------|--------------------|------|------|------|------|
| PARAMETER | TEST CONDITION | PART | SYMBOL | MIN. | TYP. | MAX. | UNIT |
| INPUT | | | | | | | |
| Forward voltage | $I_F = \pm 60 \text{ mA}$ | | V_{F} | | 1.25 | 1.65 | V |
| Capacitance | $V_R = 0 V, f = 1 MHz$ | | Co | | 50 | | pF |
| Thermal resistance | | | R _{thja} | | 750 | | K/W |
| OUTPUT | | | | | | | |
| Collector emitter capacitance | $V_{CE} = 5 \text{ V, f} = 1 \text{ MHz}$ | | C _{CE} | | 6.8 | | pF |
| Thermal resistance | | | R _{thja} | | 500 | | °C/W |
| COUPLER | | | | | | | |
| Collector emitter saturation voltage | $I_F = \pm 10 \text{ mA}, I_C = 2.5 \text{ mA}$ | | V _{CEsat} | | 0.25 | 0.4 | V |
| Coupling capacitance | | | C _C | | 0.2 | | pF |
| | | SFH620A-1 | I _{CEO} | | 2 | 50 | nA |
| | | SFH6206-1 | I _{CEO} | | 2 | 50 | nA |
| Collector emitter leakage current | V - 10 V | SFH620A-2 | I _{CEO} | | 2 | 50 | nA |
| | V _{CE} = 10 V | SFH6206-2 | I _{CEO} | | 2 | 50 | nA |
| | | SFH620A-3 | I _{CEO} | | 5 | 100 | nA |
| | | SFH6206-3 | I _{CEO} | | 5 | 100 | nA |

Note

Minimum and maximum values are testing requirements. Typical values are characteristics of the device and are the result of engineering
evaluation. Typical values are for information only and are not part of the testing requirements.
Still air, coupler soldered to PCB or base.



Optocoupler, Phototransistor Output, Vishay Semiconductors AC Input

| CURRENT TRANSFER R | CURRENT TRANSFER RATIO (T _{amb} = 25 °C, unless otherwise specified) | | | | | | | |
|--------------------------------|---|-----------|--------|------|------|------|------|--|
| PARAMETER | TEST CONDITION | PART | SYMBOL | MIN. | TYP. | MAX. | UNIT | |
| | | SFH620A-1 | CTR | 40 | | 125 | % | |
| | | SFH6206-1 | CTR | 40 | | 125 | % | |
| | V - 5 V I - + 10 mA | SFH620A-2 | CTR | 63 | | 200 | % | |
| | $V_{CE} = 5 \text{ V}, I_F = \pm 10 \text{ mA}$ | SFH6206-2 | CTR | 63 | | 200 | % | |
| | | SFH620A-3 | CTR | 100 | | 320 | % | |
| | | SFH6206-3 | CTR | 100 | | 320 | % | |
| I _C /I _F | | SFH620A-1 | CTR | 13 | 30 | | % | |
| | | SFH6206-1 | CTR | 13 | 30 | | % | |
| | \/ - 5 \/ - 1 m \ | SFH620A-2 | CTR | 22 | 45 | | % | |
| | $V_{CE} = 5 \text{ V}, I_F = \pm 1 \text{ mA}$ | SFH6206-2 | CTR | 22 | 45 | | % | |
| | | SFH620A-3 | CTR | 34 | 70 | | % | |
| | | SFH6206-3 | CTR | 34 | 70 | | % | |

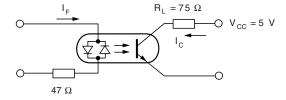


Fig. 1 - Switching Times Linear Operation (without Saturation)

| SWITCHING CHARACTERISTICS (T _{amb} = 25 °C, unless otherwise specified) | | | | | | |
|---|---|------------------|------|------|------|------|
| PARAMETER | TEST CONDITION | SYMBOL | MIN. | TYP. | MAX. | UNIT |
| Turn-on time | $R_L = 75~\Omega,~I_F = \pm~10~mA,~V_{CC} = 5~V$ | t _{on} | | 3 | | μs |
| Rise time | $R_L = 75 \Omega$, $I_F = \pm 10$ mA, $V_{CC} = 5 V$ | t _r | | 2 | | μs |
| Turn-off time | $R_L = 75 \Omega$, $I_F = \pm 10$ mA, $V_{CC} = 5 V$ | t _{off} | | 2.3 | | μs |
| Fall time | $R_L = 75~\Omega,~I_F = \pm~10~mA,~V_{CC} = 5~V$ | t _f | | 2 | | μs |
| Cut-off frequency | $R_L = 75~\Omega,~I_F = \pm~10~mA,~V_{CC} = 5~V$ | t _{ctr} | | 250 | | kHz |

| SAFETY AND INSULATION RATINGS | | | | | | | |
|--|------------------------|--------|--------|-----------|------|------|--|
| PARAMETER | TEST CONDITION | SYMBOL | MIN. | TYP. | MAX. | UNIT | |
| Climatic classification (according to IEC 68 part 1) | | | | 55/100/21 | | | |
| Comparative tracking index | | CTI | 175 | | 399 | | |
| V _{IOTM} | | | 10 000 | | | V | |
| V _{IORM} | | | 890 | | | V | |
| P _{SO} | | | | | 400 | mW | |
| I _{SI} | | | | | 275 | mA | |
| T _{SI} | | | | | 175 | °C | |
| Creepage distance | Standard DIP-4 | | 7 | | | mm | |
| Clearance distance | Standard DIP-4 | | 7 | | | mm | |
| Creepage distance | 400 mil DIP-4 | | 8 | | | mm | |
| Clearance distance | 400 mil DIP-4 | | 8 | | | mm | |
| Insulation thickness, reinforced rated | per IEC 60950 2.10.5.1 | | 0.4 | | | mm | |

Note

As per IEC 60747-5-5, § 7.4.3.8.1, this optocoupler is suitable for "safe electrical insulation" only within the safety ratings. Compliance with
the safety ratings shall be ensured by means of protective circuits.

Vishay Semiconductors Optocoupler, Phototransistor Output, AC Input



TYPICAL CHARACTERISTICS (T_{amb} = 25 °C, unless otherwise specified)

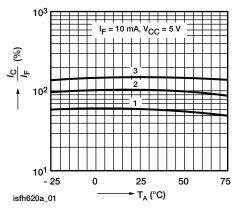


Fig. 2 - Current Transfer Ratio (CTR) vs. Temperature

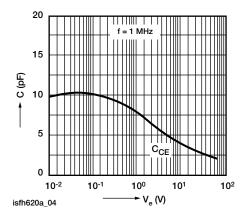


Fig. 5 - Transistor Capacitance (Typ.) vs. Collector Emitter Voltage

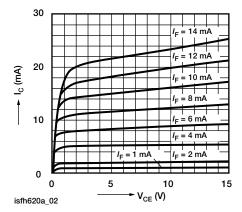


Fig. 3 - Output Characteristics (Typ.) Collector Current vs. Collector Emitter Voltage

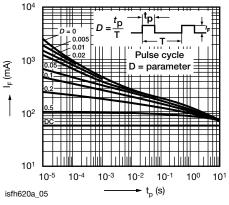


Fig. 6 - Permissible Pulse Handling Capability Forward Current vs. Pulse Width

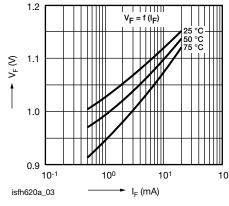


Fig. 4 - Diode Forward Voltage (Typ.) vs. Forward Current

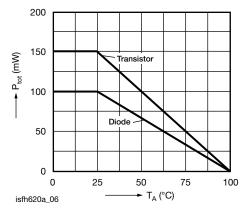


Fig. 7 - Permissible Power Dissipation vs. Ambient Temperature



Optocoupler, Phototransistor Output, Vishay Semiconductors AC Input

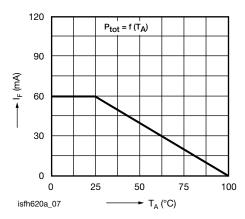
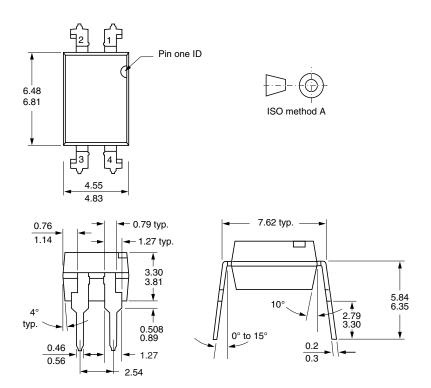


Fig. 8 - Permissible Diode Forward Current vs. Ambient Temperature

PACKAGE DIMENSIONS in millimeters

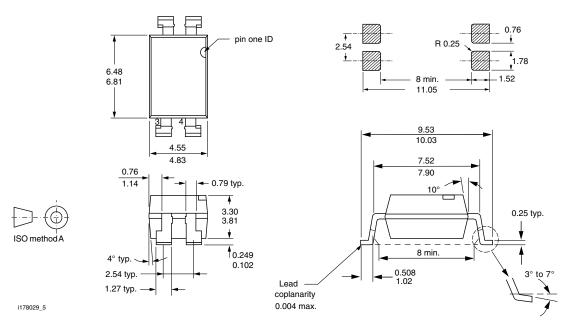


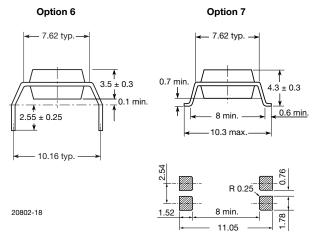
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Vishay Semiconductors Optocoupler, Phototransistor Output, AC Input

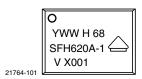


PACKAGE DIMENSIONS in millimeters





PACKAGE MARKING (example)



Notes

- Only options 1 and 7 are reflected in the package marking.
- The VDE Logo is only marked on option1 parts.
- Tape and reel suffix (T) is not part of the package marking.



Legal Disclaimer Notice

Vishay

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