

ECE3003- Microcontroller and its Applications

Lab Slot: L37+L38

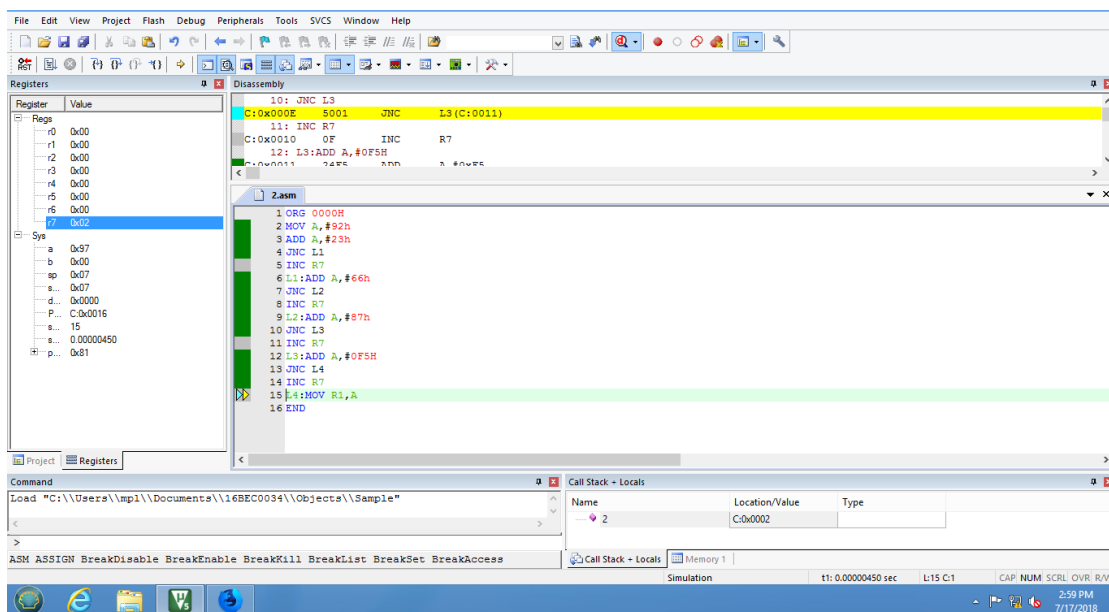
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Faculty: Prof. Chitra P

Program 1

Write and assemble a program to add the following data and then use the simulator to examine the CY flag. 92H, 23H, 66H, 87H, F5H

Output:



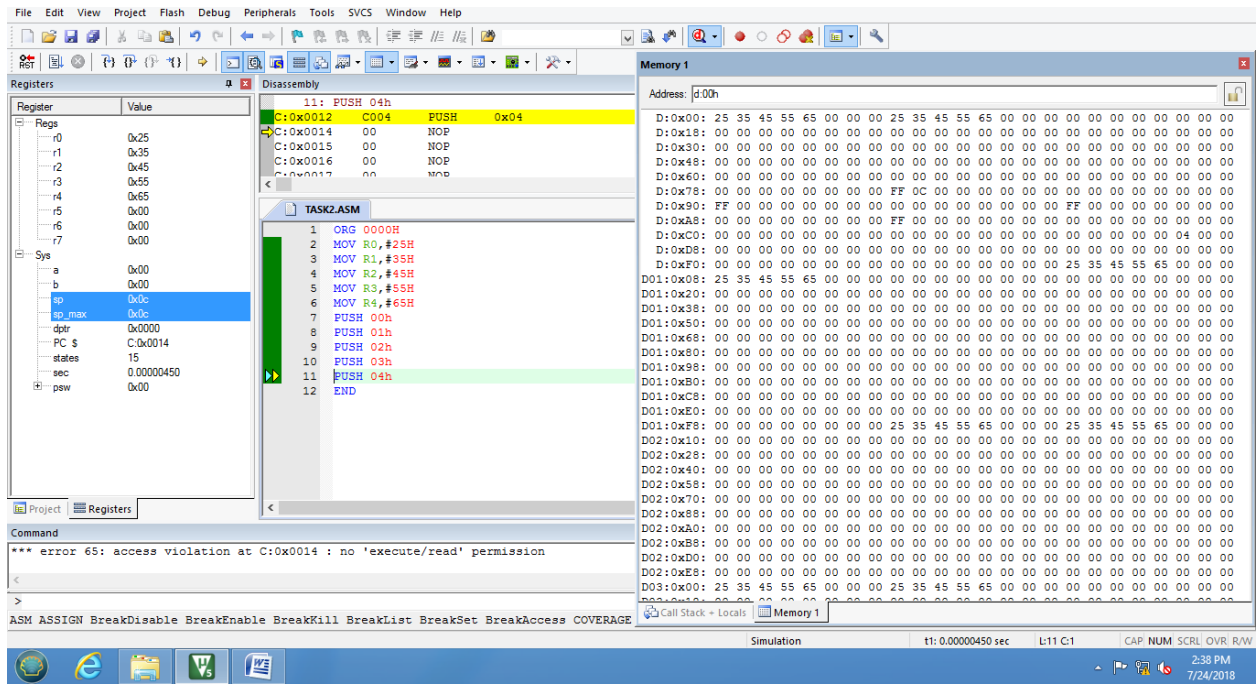
Code:

```
ORG 0000H
MOV A,#92h
ADD A,#23h
JNC L1
INC R7
L1:ADD A,#66h
JNC L2
INC R7
L2:ADD A,#87h
JNC L3
INC R7
L3:ADD A,#0F5H
JNC L4
INC R7
L4:MOV R1,A
END
```

Program 2

Write and assemble a program to load values into each of registers R0 - R4 and then push each of these registers onto the stack. Single-step the program, and examine the stack and the SP register after the execution of each instruction.

Output:



Code:

```
ORG 0000H
MOV R0,#25H
MOV R1,#35H
MOV R2,#45H
MOV R3,#55H
MOV R4,#65H
PUSH 00h
PUSH 01h
PUSH 02h
PUSH 03h
PUSH 04h
END
```

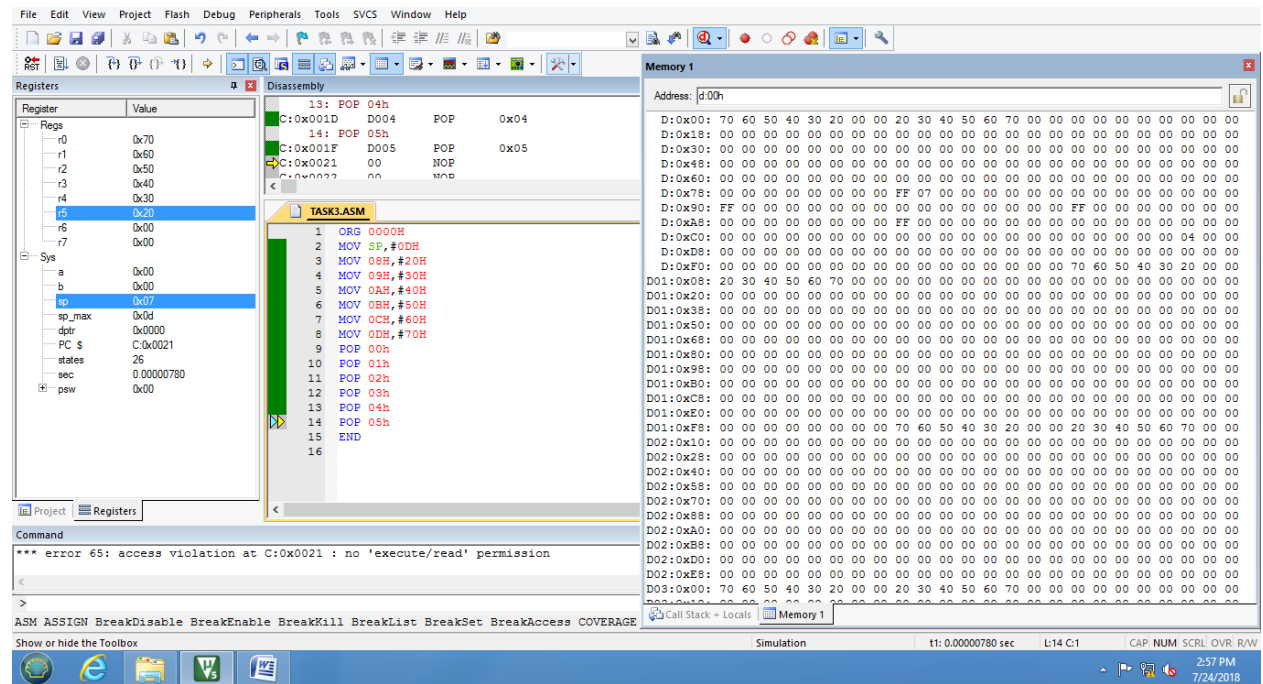
Program 3

Write and assemble a program to:

- (a) Set SP = 0D,
- (b) Put a different value in each of RAM locations 0D, 0C, 0B, 0A, 09, and 08,
- (c) POP each stack location into registers R0 - R4.

Use the simulator to single-step and examine the registers, the stack, and the stack pointer.

Output:



Code:

```
ORG 0000H
MOV SP,#0DH
MOV 08H,#20H
MOV 09H,#30H
MOV 0AH,#40H
MOV 0BH,#50H
MOV 0CH,#60H
MOV 0DH,#70H
POP 00h
POP 01h
POP 02h
POP 03h
POP 04h
POP 05h
END
```

Program 4:

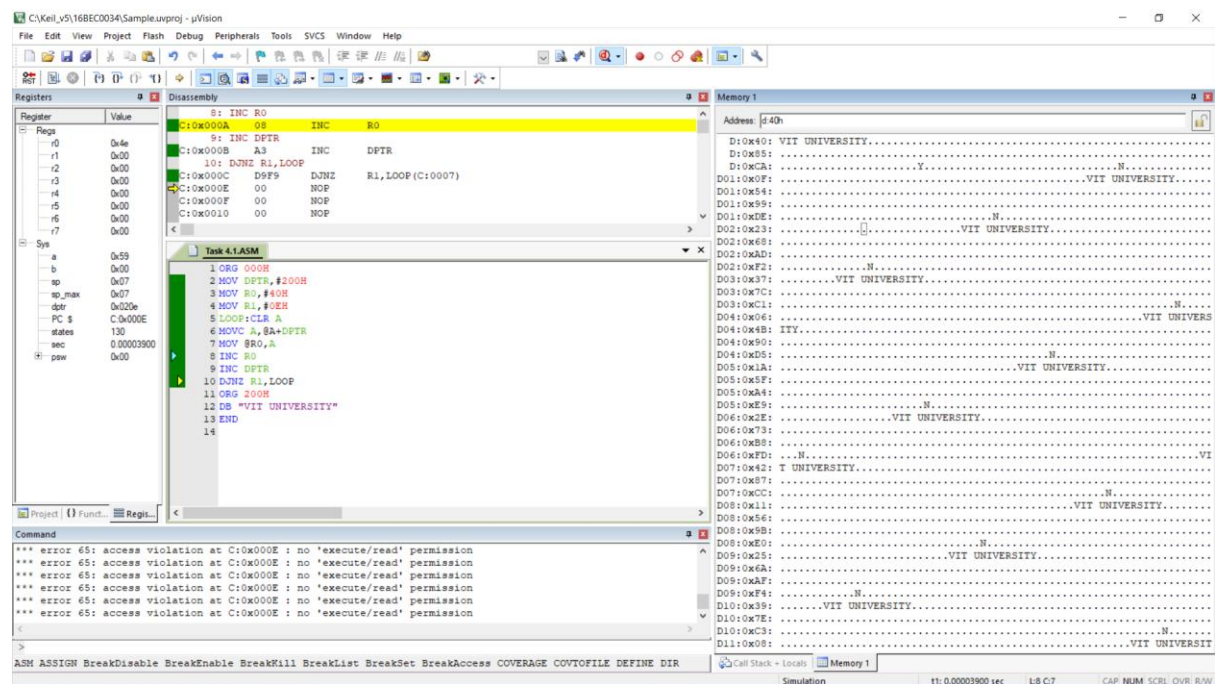
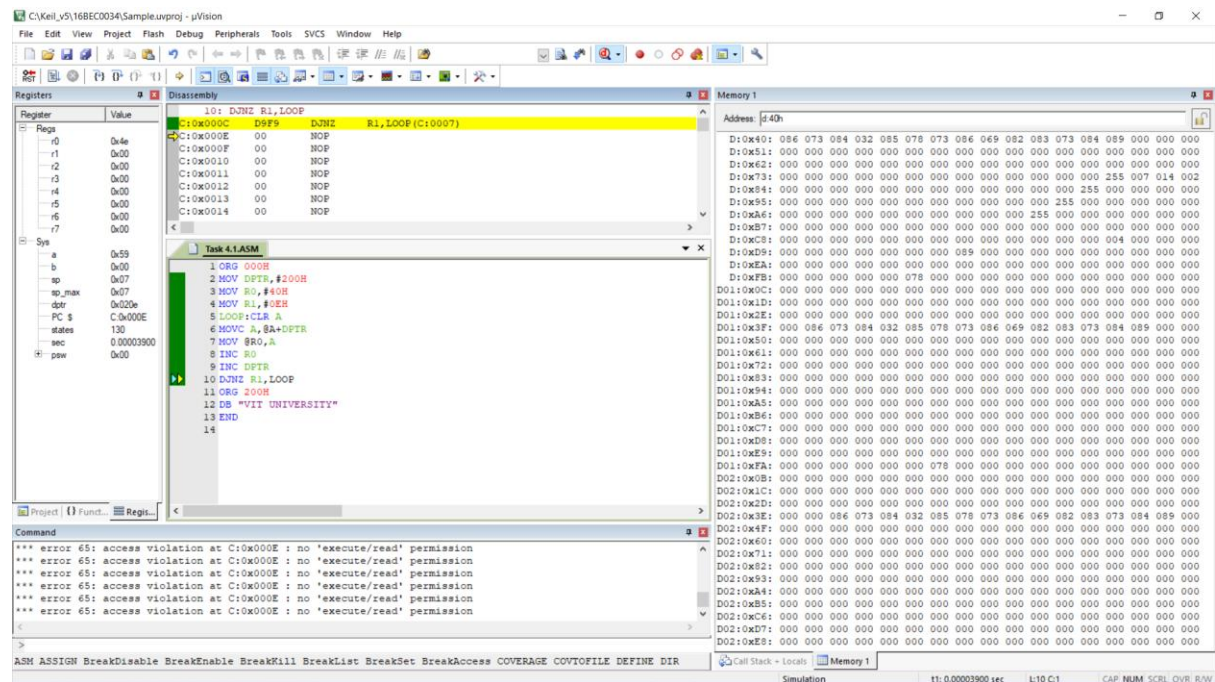
Write a program to transfer a string of data from code space starting at address 200H to RAM locations inside the CPU starting at 40H. The data is as shown below:

MYDATA: DB "VIT University"

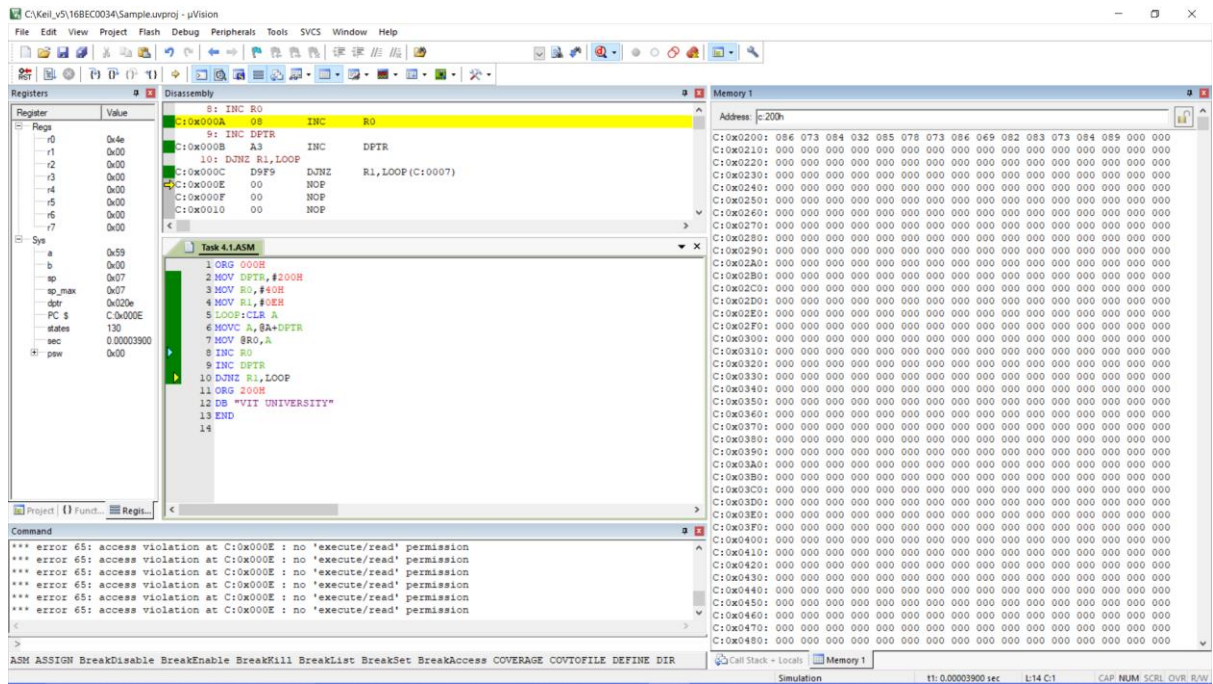
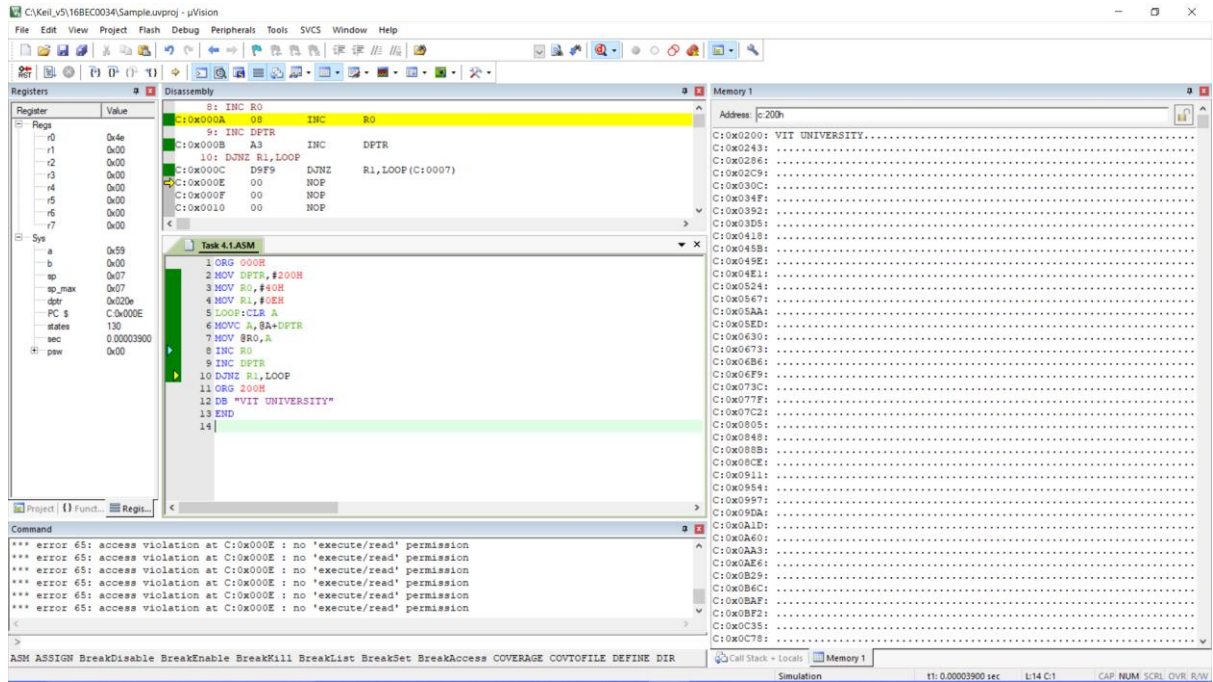
Using the simulator, single-step through the program and examine the data transfer and registers.

Output:

1)



*String Input



Code:

```
ORG 000H
MOV DPTR,#200H
MOV R0,#40H
MOV R1,#0EH
LOOP:CLR A
MOVC A,@A+DPTR
MOV @R0,A
INC R0
INC DPTR
DJNZ R1,LOOP
ORG 200H
DB "VIT UNIVERSITY"
END
```

Add the following subroutine to the program 4, single-step through the subroutine and examine the RAM locations. After data has been transferred from ROM space into RAM, the subroutine should copy the data from RAM locations starting at 40H to RAM locations starting at 60H.

Output:

The screenshot displays the Visual Studio IDE with the Disassembly window open. The Disassembly window shows the assembly instructions for the MOV instruction, with the address 0x0009 highlighted. The Registers window shows the values of various registers, including R0, R1, R2, R3, R4, R5, R6, R7, and the system registers. The TASKS-ASM window shows the assembly code for the program, including the MOV instruction. The Command window shows the command 'Load \"C:\\Users\\mp1\\Documents\\16BEC0034\\Objects\\Sample\"'.

Registers

Register	Value
r0	0x4e
r1	0x6e
r2	0x00
r3	0x00
r4	0x00
r5	0x00
r6	0x00
r7	0x00
a	0x59
b	0x00
sp_max	0x07
dptr	0x020e
PC	0x001B
states	265
sec	0.00007950
paw	0x00

Disassembly

7: MOV @R0,A
C:0x0009 F6 MOV @R0,A

Memory 1

Address: 0x50h

D:0x60:	56 49 54 20 55 4E 49 56 45 52 53 49 54 59 00 00 00 00 00 00 00 00 00 00
D:0x76:	00 00 00 00 00 00 00 00 00 00 FF 07 0E 02 00 00 00 00 00 00 00 00 00
D:0x8C:	00 00
D:0xA2:	00 00
D:0xB8:	00 00
D:0xCE:	00 00 00 00 00 00 00 00 04 00 00 00 00 00 00 00 00 00 59 00 00 00
D:0xE4:	00 00
D:0xFA:	00 00 00 00 00 00 00 00 4E 6E 00 00 00 00 00 00 00 00 00 00 00 00
D:0x10:	00 00
D:0x26:	00 00
D:0x3C:	00 00 00 00 56 49 54 20 55 4E 49 56 45 52 53 49 54 59 00 00 00 00 00
D:0x52:	00 00 00 00 00 00 00 00 00 00 00 00 00 00 56 49 54 20 55 4E 49 56
D:0x68:	45 52 53 49 54 59 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00
D:0x7E:	00 00
D:0x94:	00 00
D:0xAA:	00 00
D:0xC0:	00 00
D:0xD6:	00 00
D:0xEC:	00 4E 6E
D:0x02:	00 00

TASKS-ASM

```

1  ORG 000H
2  MOV DPTR,#200H
3  MOV R0,#40H
4  MOV R1,#0EH
5  LOOP:CLR A
6  MOV A,@A+DPTR
7  MOV @R0,A
8  INC R0
9  INC DPTR
10 DJNZ R1,LOOP
11 MOV R0,#40H
12 MOV R1,#60H
13 MOV R3,#0EH
14 LOOP2:CLR A
15 MOV A,@R0
16 MOV @R1,A
17 INC R0
18 INC R1
19 DJNZ R3,LOOP2
20 HERE:SNMP HERE
21 ORG 200H
22 DB "VIT UNIVERSITY"
23 END

```

Command

Load "C:\\Users\\mp1\\Documents\\16BEC0034\\Objects\\Sample"

Simulation t1: 0.00007950 sec L7: C10 CAP NUM SCRL OVR: R/W 2:42 PM 7/31/2018

The screenshot displays the Keil uVision IDE interface with the following components:

- Registers Window:** Shows the status of various registers. The PC register is at 0x001B. The status of other registers like R0, R1, R2, etc., is also visible.
- Assembly View:** The main window showing the assembly code. The source code is:


```

      7: MOV @R0,A
      C:0x0009 F6 MOV @R0,A
      8: TM R0
      9: TM R0
      
```
- Memory Window:** Shows the memory address 0x50h and the corresponding assembly instructions:


```

      D:0x60: VIT UNIVERSITY.....Y.....
      D:0xA4: .....Nn.....
      D:0xE8: .....Nn.....
      D01:0x2C: .....VIT UNIVERSITY.....VIT UNIVERSITY..
      D01:0x70: .....
      D01:0xB4: .....
      D01:0xF8: .....Nn.....
      D02:0x3C: .....VIT UNIVERSITY.....VIT UNIVERSITY..
      D02:0x80: .....
      D02:0xC4: .....Nn.....
      D03:0x08: .....VIT UNIVERSITY.....VIT UNIVERSITY..
      D03:0x4C: TY.....VIT UNIVERSITY.....
      D03:0x90: .....
      D03:0xD4: .....Nn.....
      D04:0x18: .....VIT UNIVERSITY.....
      D04:0x5C: .....VIT UNIVERSITY.....
      D04:0xA0: .....
      D04:0xE4: .....Nn.....
      D05:0x28: .....VIT UNIVERSITY.....VIT UNIVERSITY..
      D05:0x6C: TY.....
      
```
- TASKS.ASM Window:** Shows the assembly code for the 'VIT UNIVERSITY' program:


```

      1 ORG 000H
      2 MOV DPTR,#200H
      3 MOV R0,#40H
      4 MOV R1,#0EH
      5 LOOP:CLR A
      6 MOV A,8A+DPTR
      7 MOV @R0,A
      8 INC R0
      9 INC DPTR
      10 DJNZ R1,LOOP
      11 MOV R0,#40H
      12 MOV R1,#60H
      13 MOV R3,#0EH
      14 LOOP2:CLR A
      15 MOV A,@R0
      16 MOV @R1,A
      17 INC R0
      18 INC R1
      19 DJNZ R3,LOOP2
      20 HERE:JMP HERE
      21 ORG 200H
      22 DB "VIT UNIVERSITY"
      23 END
      
```
- Command Window:** Shows the load path for the program:


```

      Load "C:\\Users\\mpl\\Documents\\16BEC0034\\Objects\\Sample"
      
```
- Simulation Status:** The status bar at the bottom indicates the simulation is running, with a time of 0.00007950 sec and a level of L7 C:10.

*String Input

Registers

Register	Value
r0	0x4e
r1	0x6e
r2	0x00
r3	0x00
r4	0x00
r5	0x00
r6	0x00
r7	0x00
SP	0x07
PC	0x0018
PSW	0x00

Disassembly

```
7: MOV @R0,A
C:0x0009 F6 MOV @R0,A
```

Memory 2

Address: 0x200h

```
C:0x0200: VIT UNIVERSITY...
C:0x0242:
C:0x0284:
C:0x02C6:
C:0x0308:
C:0x034A:
C:0x038C:
C:0x03CE:
C:0x0410:
C:0x0452:
C:0x0494:
C:0x04D6:
C:0x0518:
C:0x055A:
C:0x059C:
C:0x05DE:
C:0x0620:
C:0x0662:
C:0x06A4:
C:0x06E6:
```

TASKS.ASM

```
1 ORG 000H
2 MOV DPTR,#200H
3 MOV R0,#40H
4 MOV R1,#0EH
5 LOOP: CLR A
6 MOV C,A,@A+DPTR
7 MOV @R0,A
8 INC R0
9 INC DPTR
10 DJNZ R1,LOOP
11 MOV R0,#40H
12 MOV R1,#60H
13 MOV R3,#0EH
14 LOOP2: CLR A
15 MOV A,R0
16 MOV @R1,A
17 INC R0
18 INC R1
19 DJNZ R3,LOOP2
20 HERE: SJMP HERE
21 ORG 200H
22 DB "VIT UNIVERSITY"
23 END
```

Command

Load "C:\\Users\\mp1\\Documents\\16BEC0034\\Objects\\Sample"

ASM ASSIGN BreakDisable BreakEnable BreakKill BreakList BreakSet BreakAccess COVERAGE COVTOFILE DEFINE DIR Display Enter EVALUATE EXIT FUNC Go INCLUDE KILL LogicAnalyze

Simulation t1: 0.00007950 sec L7 C:10 CAP NUM SCRL OVR RAW

2:43 PM 7/31/2018

Registers

Register	Value
r0	0x4e
r1	0x6e
r2	0x00
r3	0x00
r4	0x00
r5	0x00
r6	0x00
r7	0x00
SP	0x07
PC	0x0018
PSW	0x00

Disassembly

```
7: MOV @R0,A
C:0x0009 F6 MOV @R0,A
```

Memory 2

Address: 0x200h

```
C:0x0200: 56 49 54 20 55 4E 49 56 45 52 53 49 54 59 00 00 00 00 00 00 00 00
C:0x0216: 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00
C:0x022C: 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00
C:0x0242: 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00
C:0x0258: 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00
C:0x026E: 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00
C:0x0284: 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00
C:0x029A: 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00
C:0x02B0: 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00
C:0x02C6: 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00
C:0x02DC: 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00
C:0x02F2: 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00
C:0x0308: 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00
C:0x031E: 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00
C:0x0334: 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00
C:0x034A: 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00
C:0x0360: 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00
C:0x0376: 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00
C:0x038C: 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00
C:0x03A2: 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00
```

TASKS.ASM

```
1 ORG 000H
2 MOV DPTR,#200H
3 MOV R0,#40H
4 MOV R1,#0EH
5 LOOP: CLR A
6 MOV C,A,@A+DPTR
7 MOV @R0,A
8 INC R0
9 INC DPTR
10 DJNZ R1,LOOP
11 MOV R0,#40H
12 MOV R1,#60H
13 MOV R3,#0EH
14 LOOP2: CLR A
15 MOV A,R0
16 MOV @R1,A
17 INC R0
18 INC R1
19 DJNZ R3,LOOP2
20 HERE: SJMP HERE
21 ORG 200H
22 DB "VIT UNIVERSITY"
23 END
```

Command

Load "C:\\Users\\mp1\\Documents\\16BEC0034\\Objects\\Sample"

ASM ASSIGN BreakDisable BreakEnable BreakKill BreakList BreakSet BreakAccess COVERAGE COVTOFILE DEFINE DIR Display Enter EVALUATE EXIT FUNC Go INCLUDE KILL LogicAnalyze

Simulation t1: 0.00007950 sec L7 C:10 CAP NUM SCRL OVR RAW

2:44 PM 7/31/2018

Code:

```
ORG 000H
MOV DPTR,#200H
MOV R0,#40H
MOV R1,#0EH
LOOP:CLR A
MOVC A,@A+DPTR
MOV @R0,A
INC R0
INC DPTR
DJNZ R1,LOOP
MOV R0,#40H
MOV R1,#60H
MOV R3,#0EH
LOOP2:CLR A
MOV A,@R0
MOV @R1,A
INC R0
INC R1
DJNZ R3,LOOP2
HERE:SJMP HERE
ORG 200H
DB "VIT UNIVERSITY"
END
```

TASK 1

Program 6:

Write a program to calculate y where $y = x^2 + 2x + 9$. x is between 0 and 9 and the look-up table for x² is located at the address (code space) of 200H.

Register R0 has the x, and at the end of the program R2 should have y.

Use the simulator to change the x value and single-step through the program, examining the registers as you go.

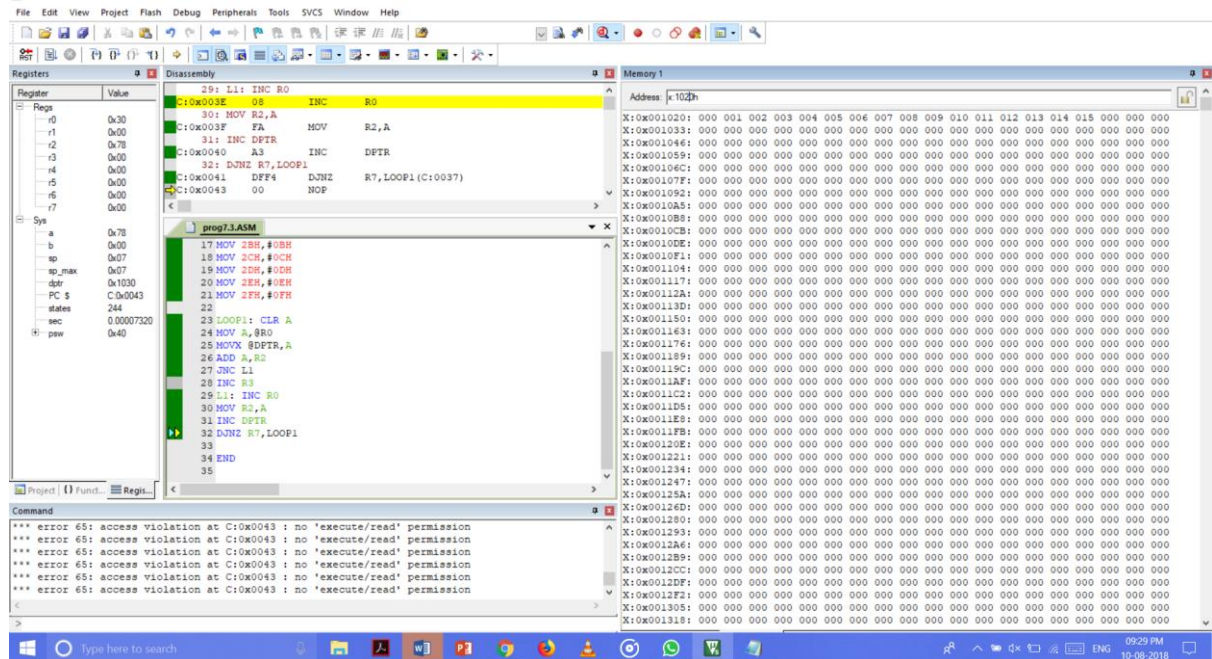
Code:

```
ORG 000H
    MOV DPTR,#200H
    MOV A,#30H
    MOV R1,A
    MOV R0,A
    ADDC A,R1
    MOV R1,A
    MOV A,R0
    MOVC A,@A+DPTR
    ADDC A,#09H
    ADDC A,R1
    MOV R2,A
HERE: SJMP HERE
    ORG 200H
    DB 00H,01H,04H,09H,10H,19H,24H,31H,40H,51H
END
```

Program 7:

Transfer the block of data from 20h to 30h to external location 1020h to 1030h. and find the sum of data block given by you.

Output:



Code:

```
ORG 0000H
MOV R7, #10H
MOV DPTR, #1020H
MOV R0, #20H

MOV 20H, #00H
MOV 21H, #01H
MOV 22H, #02H
MOV 23H, #03H
MOV 24H, #04H
MOV 25H, #05H
MOV 26H, #06H
MOV 27H, #07H
MOV 28H, #08H
MOV 29H, #09H
MOV 2AH, #0AH
MOV 2BH, #0BH
MOV 2CH, #0CH
```

```
MOV 2DH, #0DH
MOV 2EH, #0EH
MOV 2FH, #0FH
```

```
LOOP1: CLR A
MOV A, @R0
MOVX @DPTR, A
ADD A, R2
JNC L1
INC R3
L1: INC R0
MOV R2, A
INC DPTR
DJNZ R7, LOOP1
```

```
END
```