THIAGARAJAR COLLEGE OF ENGINEERING



(An Autonomous Institution Affiliated to Anna University)

MADURAI- 625 015, TAMILNADU, INDIA



GRADE CARD

B.E. DEGREE EXAMINATION

SL.NO:204093-C

CANDIDATE NAME REG.NO. / ROLL NO.

: SANTHOSH S

DATE OF PUBLICATION

: 16-05-2023 : APRIL-2023

: 19D079 / 62825 : ELECTRONICS AND COMMUNI

EXAM YEAR

BRANCH

: ELECTRONICS AND COMMUNICATION ENGINEERING										
CODE	NAME	les	loo	V/0.5						_
18CH190		1	GR S	YOP N19	CODE	NAME	+-	GR	-	1
18CHB30	CHEMISTRY	3	C	N19		SPEECH SIGNAL PROCESSING	1	Α	N21	I
18EG140	ENGLISH	2	c	N19	18EC510	DATA COMMUNICATION NETWORKS	3	В	N21	I
18EG170	ENGLISH LABORATORY	1	c	N19	18EC530	ANTENNAS AND WAVE PROPAGATION	3	В	N21	١
18ES150	ENGINEERING EXPLORATION	3	s	N19	18EC560	The state of the s	3	Α	N21	١
18ME160	ENGINEERING GRAPHICS	ľ	С	N19	18EC570		3	^	N21	١
18PH180	PHYSICS LABORATORY	1	В	N19	18EC580	DATA COMMUNICATION NETWORKING LABORATORY ANALOG AND DIGITAL COMMUNICATIONS LABORATORY	11	Α.	N21	l
18PHB20	PHYSICS	3	A	N19	200 000	REAL TIME EMBEDDED SYSTEMS	1	A	N21	١
18CHAA0	ENVIRONMENTAL SCIENCE	0	c	A20	18ES590		3	В	N21	١
18EC220	NETWORK THEORY	3	A	A20	18EC1F0	EMBEDDED SYSTEM DESIGN	3	A	N21	١
18EC230	ELECTRONIC DEVICES	3	A	A20	18EC610	ACCOUNTING AND FINANCE	1	В	A22	l
18EC240	SEMICONDUCTOR PHYSICS	3	В	A20	18EC620		3	A	A22	١
18EC260	DIGITAL SYSTEM DESIGN	3	A	A20	18EC630		3	^	A22	١
18EC270	CIRCUITS AND DEVICES LABORATORY	1	A	A20	2 2 2 2	DIGITAL COMMUNICATION SYSTEM DESIGN	2	A	A22	١
18EC280	ELECTRONICS WORKSHOP	1	s	A20	18EC670		2	C	A22	١
18ES290	LATERAL THINKING	1	s	A20	18ECEB0		3	S	A22	ı
18MA110		4	С	A20	18ECPF0	FPGA BASED DIGITAL SYSTEM DESIGN	3	В	A22 A22	I
18MA210	THE STAND CHUMANT DIFFERENTIAL EQUATIONS	3	Α	A20	18ECPZ0	1 0/4	3	s	A22	١
18EC310	TO E TO S AND LINEAR ALGEBRA	3	В	N20	18ES690	3.33	3	s	A22	I
18EC320	RF PASSIVE DEVICES AND CIRCUITS	3	В	N20	21MTMA0		ľ	s	A22	١
18EC330	ELECTRONIC CIRCUITS	3	Α	N20	18EC710		L	A	N22	l
18EC340	SIGNALS AND SYSTEMS	3	В	N20	18ECPD0	WIRELESS COMMUNICATION SYSTEMS	3	В	N22	١
18EC350	MICROPROCESSORS AND MICROCONTROLLERS	3	Α	N20	18ECPH0		3	В	N22	١
18EC360	PROGRAMMING FOR PROBLEM SOLVING	3	В	N20	18ECRC0	MULTIMEDIA COMPRESSION TECHNIQUES	4	В	N22	١
18EC370	MICROPROCESSOR AND MICROCONTROLLER LABORATORY	1	s	N20	18ECRD0		4	c	N22	١
18EC380	ELECTRONIC CIRCUITS LABORATORY	1	s	N20	19ES790	PROFESSIONAL READINESS FOR INNOVATION EMPLOYABILITY AND ENTREPRENEURSHIP	3	s	N22	١
18ES390	DESIGN THINKING	2	s	N20	18EC810	PROJECT	9	s	A23	ı
100	CONSTITUTION OF INDIA	0	s	A21	*18INSC0	INTERNSHIP	3	s	A23	I
18CHFB0	MATERIALS FOR ENGINEERS	3	Α	A21	••••	*******END OF STATEMENT******			**	l
18EC410	OPTIMIZATION AND NUMERICAL METHODS	3	s	A21	IA III					I
18EC420	RF ACTIVE CIRCUITS	3	s	A21	4				>-	ı
18EC430	CMOS VLSI SYSTEMS	3	s	A21	13					١
18EC440	SIGNAL PROCESSING	3	Α	A21						١
18EC470	RF CIRCUITS LABORATORY	1	s	A21	-					١
18EC480	SIGNAL PROCESSING LABORATORY	1	s	A21	131	5 · ·				١
18EC490	PROJECT MANAGEMENT	3	S	A21	22	1				١
18EG460	PROFESSIONAL COMMUNICATION	2	s	A21	- 43					١
*18CHAC0		0	В	N21	7.8	200	1			١
18CSGE0	APPLICATIONS OF VIRTUAL REALITY	3	Α	N21	1048	s un eganh				١

MEDIUM OF INSTRUCTION: ENGLISH * Not Considered for CGPA

Semester	ODD.Jr	EVEN		EVEN				EVEN
•	(N19)	(A20)	(N20)	(A21)	(N21)	(A22)	(N22)	(A23)
GPA.	6.59	8.59	8.64	9.73	8.63	9.00	8.17	10.00
Credits Registered	22	22	22	22	24	25	18	12
Credits Earned	18	22	22	22	24	25	18	12
Total Credits Earned				16	60			
CGPA				8.		120 (125)		
Class Obtained		First Class				Bee.		

CONDITIONS

1. Any alteration or overwriting make this statement of marks invalid

CONTROLLER OF EXAMINATIONS

2. Any discrepancy in the above entries must be brought to the notice of the CONTROLLER OF EXAMINATIONS immediately

Date:02-06-2023

Note: CR-Credit, GR-Grade, YOP-Year Of Passing Note: Crectedings Control of the Creation of t