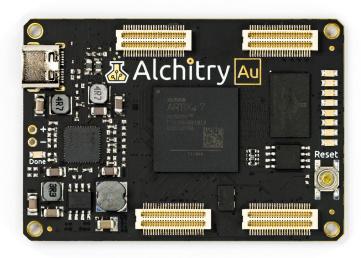


## **Product Brief**



## Overview

The Alchitry Au is our most powerful FPGA development board to date! It features a Xilinx Artix 7 FPGA which is supported by Vivado, Xilinx's latest design tools.

This board is a fantastic starting point into the world of FPGAs and the heart of your next project.

## **Features**

Xilinx Artix 7 XC7A35T-1C **FPGA** 

Memory 256MB DDR3 connected via a 16bit interface (approx. 10Gbit/s)

10 102 pins at 3.3V

20 of these can be switched to 1.8V for LVDS

9 differential analog inputs (1 dedicated, 8 shared with digital IO)

**Board Peripherals** 8 general purpose LEDs

> 1 general purpose button (typically used as a reset) 1 status LED to show when the FPGA is configured 100MHz clock (FPGA can synthesize new frequencies)

Serial interface over USB C port via FT2232H (12M Baud max)

JTAG programming over USB C port via FT2232H **Programming** 

**Power Options** 5V through USB C port, 0.1" holes, or SMT headers

Power Regulation 3.3V @ 3.5A - used for on-board peripherals and IO 1.8V @ 1.2A - used by FPGA VAUX and LVDS IO

1.8V @ 200mA - used for analog circuits

1.35V @ 1.2A - used by DDR3

1V @ 2.2A - used by FPGA internal logic

**Dimensions** 65mm x 45mm

Operating Temperature 0-60°C

## Physical Dimensions

