

# FMCA<sup>D</sup><sup>16</sup>

3-6 October,  
2016



## 16th International Conference on Formal Methods in Computer Aided Design

### Invited Speakers

Christos  
Papadimitriou  
(UC Berkeley)

George  
Varghese  
(MSR)



Synopsys Building B, Mountain View, California, USA

#### Program Committee

Pranav Ashar	(Real Intent)
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Ziyad Hanna	(Cadence Design System)
Fei He	(Tsinghua University)
Keijo Heljanko	(Aalto University)
Warren Hunt	(University of Texas Austin)
Himanshu Jain	(Synopsys)
Gerwin Klein	(NICTA and UNSW)
Daniel Kroening	(University of Oxford)
Shuvendu Lahiri	(Microsoft Research)
Rebekah Leslie-Hurd	(Intel)
Panagiotis Manolios	(Northeastern University)
Kenneth McMillan	(Microsoft Research)
John O'Leary	(Intel)
Lee Pike	(Galois, Inc.)
Ruzica Piskac	(Yale University (co-chair))
Ahmed Rezine	(Linköping University)
Sean Safarpour	(Synopsys)
Divyot Sethi	(CISCO)
Natasha Sharygina	(University of Lugano)
Sharon Shoham	(Tel Aviv)
Muralidhar Talupur	(FormalSim Inc (co-chair))
Michael Tautschnig	(Queen Mary University of London)
Tomas Vojnar	(Brno University of Technology)
Chao Wang	(Virginia Tech)
Eran Yahav	(Technion)
Florian Zuleger	(Technische Universität Wien)

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Ruzica Piskac	(Yale University)
Muralidhar Talupur	(FormalSim Inc.)
Helmut Veith	(TU Wien)

#### Publication Chair

Florian Zuleger	(TU Wien)
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Jens Katelaan	(TU Wien)

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