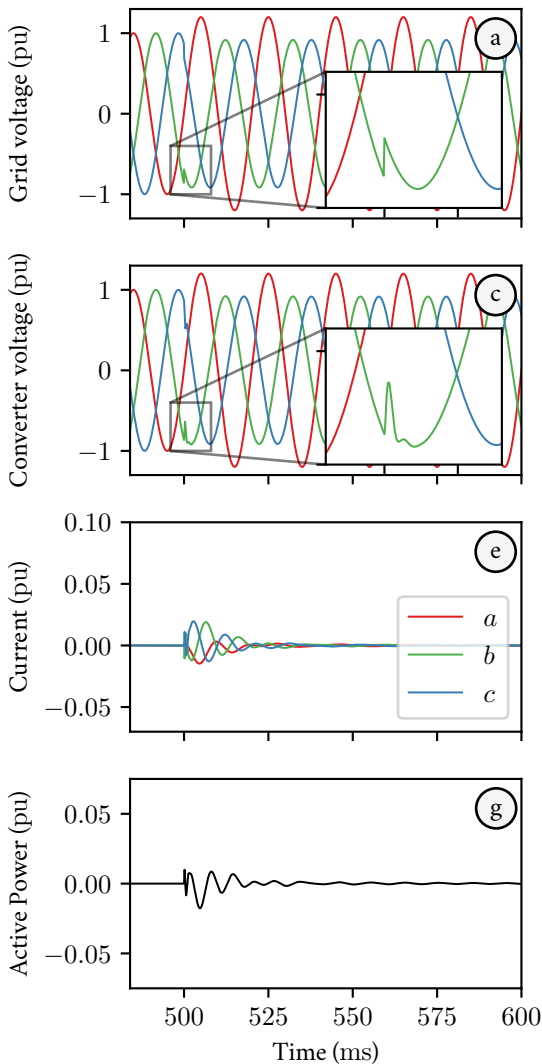


Notch-based dual controller



DDRRF-based dual controller

