EXPERIMENT-7

AIM: To study a 2 bit magnitude comparator and verify its truth table

HARDWARE / SOFTWARE APPARATUS : Power supply , bread board , connecting wires , respective IC

TRUTH TABLE:

INPUT				OUTPUT		
A1	A0	B1	B0	A<B	A=B	A>B
0	0	0	0	0	1	0
0	0	0	1	1	0	0
0	0	1	0	1	0	0
0	0	1	1	1	0	0
0	1	0	0	0	0	1
0	1	0	1	0	1	0
0	1	1	0	1	0	0
0	1	1	1	1	0	0
1	0	0	0	0	0	1
1	0	0	1	0	0	1
1	0	1	0	0	1	0
1	0	1	1	1	0	0
1	1	0	0	0	0	1
1	1	0	1	0	0	1
1	1	1	0	0	0	1
1	1	1	1	0	1	0

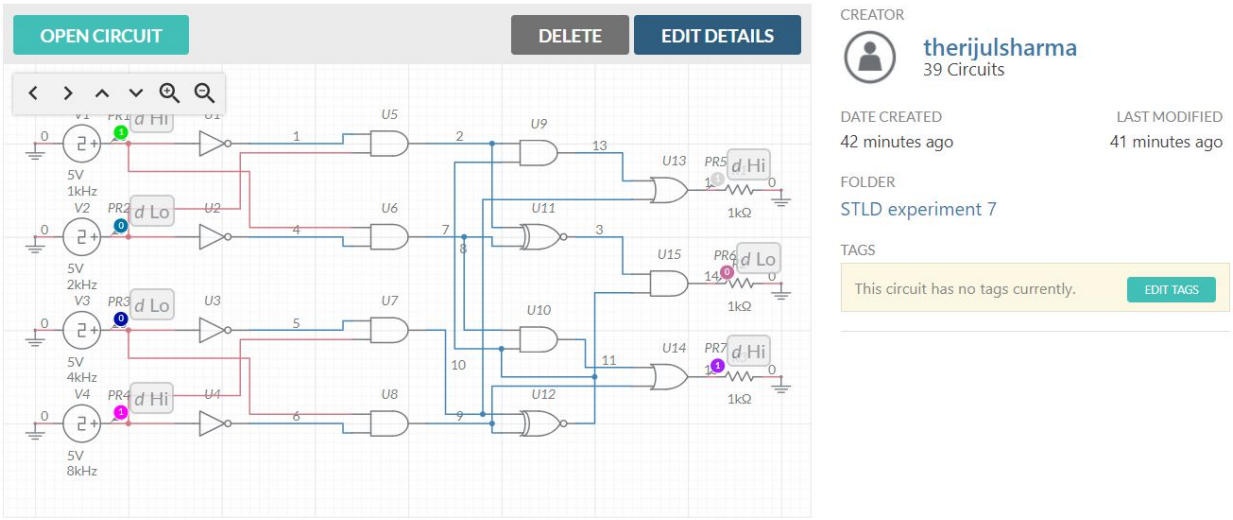
THEORY: A magnitude digital Comparator is a combinational circuit that compares two digital or binary numbers in order to find out whether one binary number is equal, less than or greater than the other binary number. We logically design a circuit for which we will have two inputs one for A and other for B and have three output terminals, one for A > B condition, one for A = B condition and one for A < B condition.

PROCEDURE (MULTISIM):

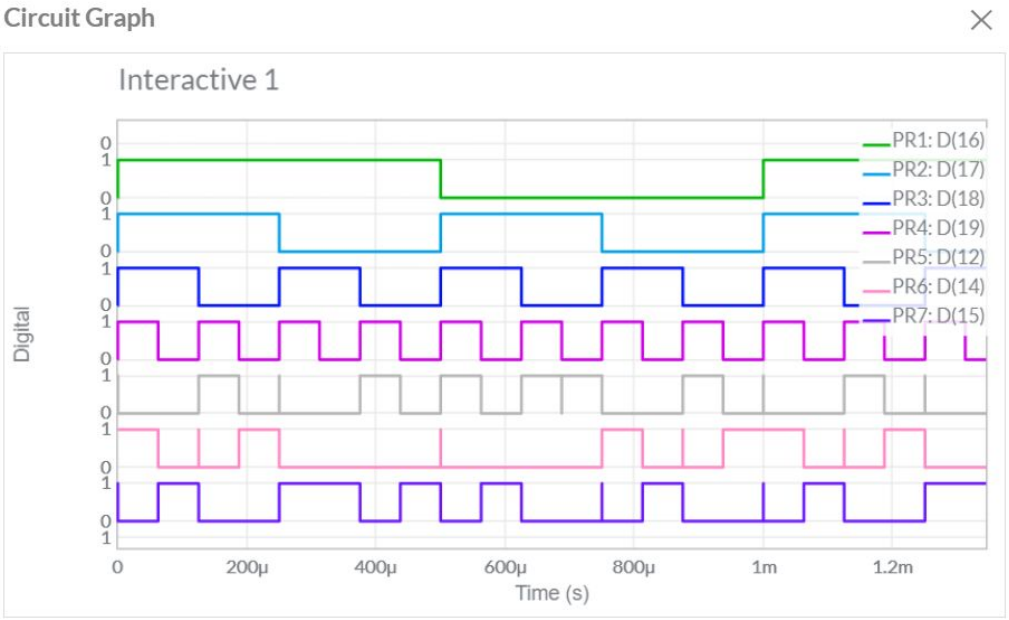
- Select the required gate symbol from the digital section of the tool bar on the left .
- Select a resistor from the same toolbar.

- Select the voltage sources and ground symbols from that toolbar.
- Ground both the voltage sources(clock) and then connect them to the input terminal of the gate.
- Connect the output terminal to 1kohm resistor and ground it.

CIRCUIT DIAGRAMS:



INPUT /OUTPUT WAVEFORMS:



PRECAUTIONS:

- Power supply should not exceed more than 5V.
- Connections should be tight.
- Components should be tested before the practical.