

modd	desc
00	No Displacement
01	8 bit Displacement
10	16 bit Displacement
11	R/M is a Register

opcode	mne m	name
000000	ADD	Register/Memory with Register
0000010	ADD	Immediate to accumulator
001010	SUB	Register/Memory and Register to either
01010	PUSH	Register
01011	POP	Register
100000	ADD	Immediate to register / memory
1000011	XCHG	Register Memory with Register
100010	MOV	Register/memory to /from register
10001111	POP	Register Memory
1010000	MOV	Memory to Accumulator
1010001	MOV	Accumulator to Memory
1011	MOV	Immediate to Register
1100011	MOV	Immediate to register / memory
11111111	PUSH	Register Memory

Reg_ad d	W1	W0
000	AX	AL
001	CX	CL
010	DX	DL
011	BX	BL
100	SP	AH
101	BP	CH
110	SI	DH
111	DI	BH

mod d	rm	i_name
00	000	BX+SI
00	001	BX+DI
00	010	BP+SI
00	011	BP+DI
00	100	SI
00	101	DI
00	110	NULL
00	111	BX
01	000	BX+SI
01	001	BX+DI
01	010	BP+SI
01	011	BP+DI
01	100	SI
01	101	DI
01	110	BP
01	111	BX