2022-02-16 Scau

Scan

Parallel Algorithm Design WS21/22

N. Kochendörfer, C. Alles, S. Proß

February 16, 2022

Scan Parallel Algorithm Design WS21/22

N. Kochendörfer, C. Alles, S. Proß

February 16, 2022

Table of Contents

- Scan Theory
- 2 Implementation
- Optimizations
- 4 Summary

Scan

Table of Contents

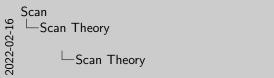
Table of Contents

Table of Contents

2022-02-16

Scan Theory

- Synonyms: prefix sum, cumulative sum or scan
- inclusive and exclusive version
- further specialization: segmented scan



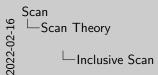
Scan Theory

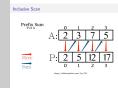
Synonyms: prefix sum, cumulative sum or scan
 inclusive and exclusive version
 further specialization: segmented scan

Inclusive Scan

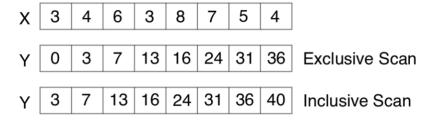
$\underset{P \text{ of } A}{\text{Prefix}} \text{Sum}$ Store 0 3

https://williamrjribeiro.com/?p=132





inclusive vs. exclusive scan



https://livebook.manning.com/book/parallel-and-high-performance-computing/chapter-5/v-11/

segmented variant

						input
1	0	0	1	0	1	flag bits
1	3	6	4	9	6	${\rm segmented}\;{\rm scan}\;+$

https://en.wikipedia.org/wiki/Segmented_scan

Scan

Scan Theory

Scan Theory

segmented variant



Implementation

STL Algorithm

STL provides:

- std::inclusive_scan
- std::exclusive_scan

Essentially equivalent to:

```
float sum = 0;
for(size_t i =0; i<N; i++)
{
    sum += input[i];
    output[i] = sum;
}</pre>
```

 \Rightarrow Sequential to a fault!

```
Scan
Implementation
Implementation
```

2022-02-16

```
Implementation
STA Agreem
STA product
and office of the control of
```

→ Sequential to a fault!

Implementation

Alternatives

Alternatives to STL:

- OpenMP: scan pragma
- TBB: parallel_scan function

Alternative Algorithms:

- Up-Down Sweeping Scan
- Tiled Scan

Scan
Implementation
Implementation

Attentions to STL

- Openhift uses preprint

- Timplementation

Attention to STL

- Openhift uses preprint

- Timplementation

Attention to STL

- Openhift uses preprint

- Timplementation

Attention to STL

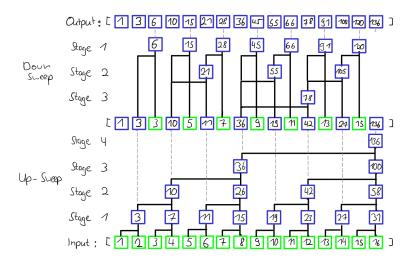
- Openhift uses preprint

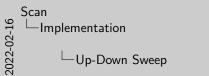
- Timplementation

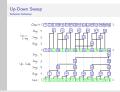
2022-02-16

Up-Down Sweep

Schema Inclusive







Up-Down Sweep

Dependency:

- Only between stages
- ⇒ Lots of parallelism

Downsides:

- Workload of 2N
- Communication!
- Workload stage dependent!

Scan

Implementation

Dependency:
- Only Internet Ages
- Less of parallelin

Demendency:
- Workload of 201
- Commissional
- Workload degree (opposedness)
- Workload degree (opposedness)

2022-02-16

Tiled Scan

Idea: Process input in independent chunks.

- Each chunk misses previous results
 - ⇒ Second pass over data.
- Workload: 2N

Our solution:

- Temporary vector for intermediate sums.
- Only one write to output.

Scan
—Implementation
—Tiled Scan

Tiled Scan

Idea: Process input in independent chunks.

• Each chunk misses previous results

• Second pass over data.

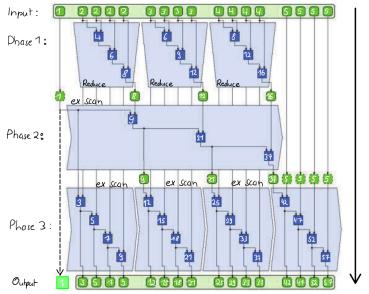
• Workload: 2N

Our solution:

Temporary vector for intermediate sums.
Only one write to output.

2022-02-16

Tiled Scan Schema



Scan Implementation

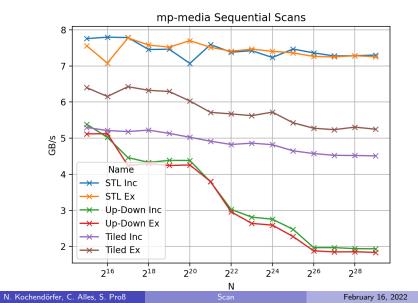
2022-02-16

Distr Phase From

Tiled Scan Schema

└─Tiled Scan Schema

Sequential Scan Results



Scan

Implementation

Sequential Scan Results

2022-02-16

13/31

Segmented Scan

Implementation

- Not present in STL!
- No reference implementations...

Solution: Wrapping the binary operation!

```
[binary_op](PairType left, PairType right){
    PairType new_right = right;
    if (not right.flag)
        new_right.value =
              binary_op(left.value, right.value);
    return new_right;
});
```

```
Scan
Implementation
Segmented Scan
```

2022-02-16

```
Segmented Scan
```

return new right:

Not present in STLI
 No reference implementations....

Solution: Wrapping the binary operation!

Segmented Scan

Solution

Works for:

- STL Scans
- Most inclusive scans

Challenge: Exclusive Scan

- Exclusive Segmented is complex
- Custom solution for each variant

Scan
Implementation
Segmented Scan

2022-02-16

Segmented Scan
Solveton

Works for:

9 ST1 Scans

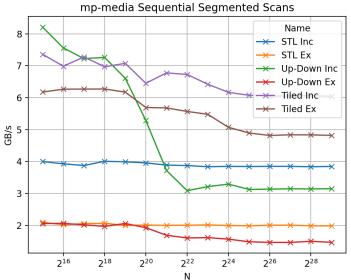
9 Most inclusive scans

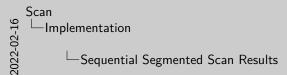
Challenge: Exclusive Scan

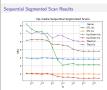
a Exclusive Segmented is complex.

Custom solution for sach variant.

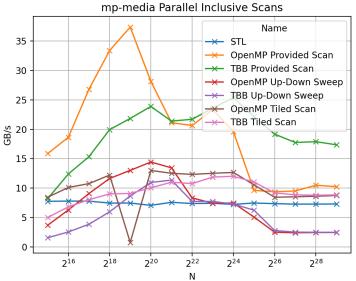
Sequential Segmented Scan Results



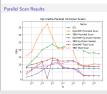




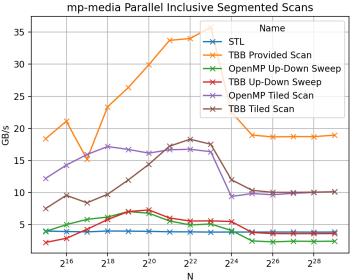
Parallel Scan Results







Parallel Segmented Scan Results







Intermediate Results

Ranking:

- Library provided implementations
- Tiled Scan
- Up-Down Sweeping Scan

Remarks:

- OpenMP >= TBB (if available)
- Up-Down Sweep is slow

Can we do better?

Scan
Optimizations
Intermediate Results

Intermediate Results

Ranking

- Library provided implementations
 Tiled Scan
- Up-Down Sweeping Scan

 Powerler:
- OpenMP >= TBB (if available)
 Up-Down Sweep is slow

Can we do better?

2022-02-16

Algorithmic Optimization

Initial Goal: functional correctness.

Algorithmic Optimizations:

- Loop-Fusion:
 - Up-Down Sweep
 - Exclusive Segmented Scan
- Limiting Memory Accesses
- General clean up

Performance gain \sim 1-5 GB/s!

Scan
Optimizations
Algorithmic Optimization

2022-02-16

Algorithmic Optimization

Initial Goal: functional correctness.

Algorithmic Optimizations:

Up-Down Sweep
 Esclusive Segmented Scan
 Limiting Memory Accesses
 General clean up

Performance gain ~ 1-5 GB/sl

Ensure that the data generated is local to the node:

```
std::vector<float> data(N);
#pragma omp parallel for schedule(static)
for (size_t i = 0; i < data.size(); i++)
data[i] = rand();
```

• The performance gain by using data local structures is likely to be small due to the warmup of Catch2

Scan Optimizations ☐ Data Locality

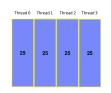
2022-02-

Data Locality

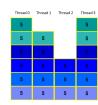
Ensure that the data generated is local to the node: #pragma omp parallel for schedule(static for (size_t i = 0; i < data.size(); i++)

. The performance gain by using data local structures is likely to be small due to the warmup of Catch2

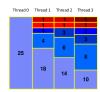
OpenMP Scheduling



(a) Static Scheduling



(b) Dynamic Scheduling



(c) Guided Scheduling

Fig. 1: Different Scheduling Strategies for 100 Iterations and 4 Threads

Scan Optimizations

2022-02-16

OpenMP Scheduling



Static: Round-robin

■ Dynamic: Constant Chunk Size

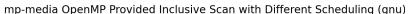
Guided: Variable Chunk Size

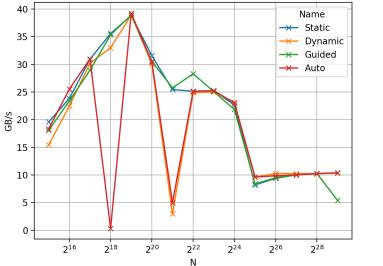
Auto: Compiler and/or Runtime System

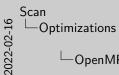
Cache Fusion if:

- 1. single parallel region executes all of the maps to be fused.
- 2. The loop for each map has the same bounds and chunk size.
- 3. Each loop uses the static scheduling mode, either as implied by the environment or explicitly specified.
- Export or during runtime

OpenMP Scheduling - Results MP-Media







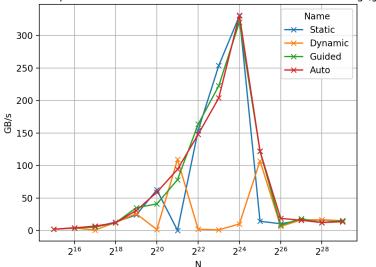
OpenMP Scheduling - Results MP-Media

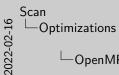


- Static and Guided perform best
- Expected due to overhead

OpenMP Scheduling - Results Ziti-Rome

ziti-rome OpenMP Provided Inclusive Scan with Different Scheduling (gnu)





OpenMP Scheduling - Results Ziti-Rome



- More severe difference
- Static least overhead
- Static Bound size and Distribution of Work
- => Static best

TBB Partitioning

TBB parallel constructs used:

- parallel_scan
- parallel_for

available partitioners:

- auto_partitioner
- affinity_partitioner
- simple_partitioner
- static_partitioner

Scan Optimizations ☐TBB Partitioning

2022-02-16

TBB Partitioning

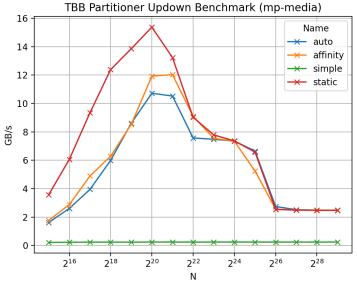
TBB parallel constructs used: a parallel_scan

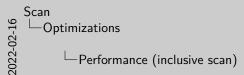
o parallel_for available partitioners

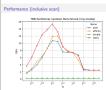
· auto_partitioner affinity_partitioner • static partitioner

simple_partitioner

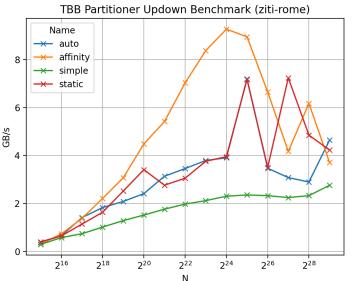
Performance (inclusive scan)

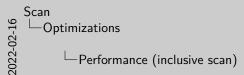






Performance (inclusive scan)







Vectorization

Requirements:

- No loop carried dependency
- Loop bounds
- No jumps in code

Realising it:

- #pragma omp simd
- Compiling with -O3
- Using Intel Icx Compiler

Scan

Optimizations

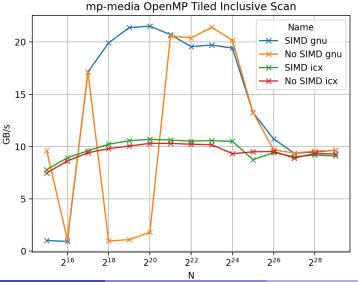
Responsests.

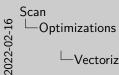
• The long carried dependency
• To be propose carried.
• To propose carried.
• Propose carried.
• Optimization

Realing is:
• # propose carry grant
• Compiler.
• Using listed to Compiler.

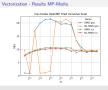
2022-02-16

Vectorization - Results MP-Media



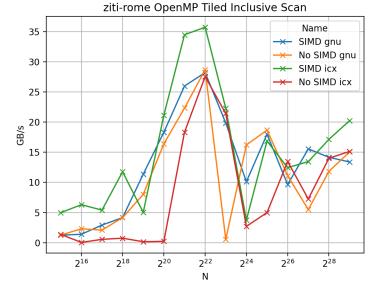


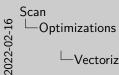
└─Vectorization - Results MP-Media



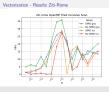
- Difference: Annotation of simd to loop
- Both compiled with -O3
- Possibly auto vectorization
- ICX much worse for MP-Media
- SIMD more stable

Vectorization - Results Ziti-Rome





└─Vectorization - Results Ziti-Rome



- Possibly auto vectorization
- ICX much better for ziti-rome
- SIMD better performance
- => SIMD

Library Provided Scans are fastest

Scan 91-70-2025 —Summary —Summary

Summary

Library Provided Scans are fastest

Summary

Summary

Library Provided Scans are fastest

Optimization done:

- Algorithmic
- Data Locality
- Scheduler & Partitioner
- Vectorization

We have

- 4 versions of Scan
- 3 different algorithms
- 2 parallelization libraries + sequential

 \Rightarrow 36 Versions to keep track of!

