

Group B

Attempt any SIX questions.

11. Explain the bus organization of 8085 microprocessor.
12. Explain the opcode fetch machine cycle for MVI A, 32H with timing diagram.
13. Explain 8085 instruction addressing mode with example.
14. Explain the memory hierarchy with diagram.
15. Explain the organization of Microprogrammed Control Unit.
16. Define control word. Explain the procedure for generating control word for specific

operation.

17. Define instruction pipeline. Explain the four-segment instruction pipeline with example.

Full Marks: 60 Pass Marks: 24 Time: 3 hours

[6*5=30]

18. Explain the functional block of 8085 microprocessor.
19. Explain the design and control logic of Accumulator.
20. Explain the different data transfer and manipulation instruction with example.

Group C

Attempt any TWO questions.

18. Explain the functional block of 8085 microprocessor.
19. Explain the design and control logic of Accumulator.
20. Explain the different data transfer and manipulation instruction with example.