## TRUTH TABLE Multiplexing Stage, FishVR Electronics

Microcontroller Outputs (C7)

8	7	5	6	1	2	3	4	SELECTED SECTOR	SELECTED CHANNEL	8	7	5	6	1	2	3	3 4	SELECTED SECTOR	SELECTED CHANNEL	8	7	5	6	1	2	3	4	SELECTED SECTOR	SELECTED CHANNEL
0	0	0	0	0	0	0	0	U1	i0	0	1	0	0	0	0	0	) (	U3	iO	0	0	1	0	0	0	0	0	U5	i0
0	0	0	0	1	0	0	0	U1	i1	0	1	0	0	1	0	0	) (	U3	i1	0	0	1	0	1	0	0	0	U5	i1
0	0	0	0	0	1	0	0	U1	i2	0	1	0	0	0	1	0	) (	U3	i2	0	0	1	0	0	1	0	0	U5	i2
0	0	0	0	1	1	0	0	U1	i3	0	1	0	0	1	1	0	) (	U3	i3	0	0	1	0	1	1	0	0	U5	i3
0	0	0	0	0	0	1	0	U1	i4	0	1	0	0	0	0	1	(	U3	i4	0	0	1	0	0	0	1	0	U5	i4
0	0	0	0	1	0	1	0	U1	i5	0	1	0	0	1	0	1	(	U3	i5	0	0	1	0	1	0	1	0	U5	i5
0	0	0	0	0	1	1	0	U1	i6	0	1	0	0	0	1	1	(	U3	i6	0	0	1	0	0	1	1	0	U5	i6
0	0	0	0	1	1	1	0	U1	i7	0	1	0	0	1	1	1	(	U3	i7	0	0	1	0	1	1	1	0	U5	i7
0	0	0	0	0	0	0	1	U1	i8	0	1	0	0	0	0	0	) 1	U3	i8	0	0	1	0	0	0	0	1	U5	i8
0	0	0	0	1	0	0	1	U1	i9	0	1	0	0	1	0	0	) 1	U3	i9	0	0	1	0	1	0	0	1	U5	i9
0	0	0	0	0	1	0	1	U1	i10	0	1	0	0	0	1	0	) 1	U3	i10	0	0	1	0	0	1	0	1	U5	i10
0	0	0	0	1	1	0	1	U1	i11	0	1	0	0	1	1	0	) 1	U3	i11	0	0	1	0	1	1	0	1	U5	i11
0	0	0	0	0	0	1	1	U1	i12	0	1	0	0	0	0	1	1	U3	i12	0	0	1	0	0	0	1	1	U5	i12
0	0	0	0	1	0	1	1	U1	i13	0	1	0	0	1	0	1	1	U3	i13	0	0	1	0	1	0	1	1	U5	i13
0	0	0	0	0	1	1	1	U1	i14	0	1	0	0	0	1	1	1	U3	i14	0	0	1	0	0	1	1	1	U5	i14
0	0	0	0	1	1	1	1	U1	i15	0	1	0	0	1	1	1	1	U3	i15	0	0	1	0	1	1	1	1	U5	i15
1	0	0	0	0	0	0	0	U2	i0	1	1	0	0	0	0	0	) (	U4	i0	1	0	1	0	0	0	0	0	U6	i0
1	0	0	0	1	0	0	0	U2	i1	1	1	0	0	1	0	0	) (	U4	i1	1	0	1	0	1	0	0	0	U6	i1
1	0	0	0	0	1	0	0	U2	i2	1	1	0	0	0	1	0	) (	U4	i2	1	0	1	0	0	1	0	0	U6	i2
1	0	0	0	1	1	0	0	U2	i3	1	1	0	0	1	1	0	) (	U4	i3	1	0	1	0	1	1	0	0	U6	i3
1	0	0	0	0	0	1	0	U2	i4	1	1	0	0	0	0	1	(	U4	i4	1	0	1	0	0	0	1	0	U6	i4
1	0	0	0	1	0	1	0	U2	i5	1	1	0	0	1	0	1	(	U4	i5	1	0	1	0	1	0	1	0	U6	i5
1	0	0	0	0	1	1	0	U2	i6	1	1	0	0	0	1	1	(	U4	i6	1	0	1	0	0	1	1	0	U6	i6
1	0	0	0	1	1	1	0	U2	i7	1	1	0	0	1	1	1	(	U4	i7	1	0	1	0	1	1	1	0	U6	i7
1	0	0	0	0	0	0	1	U2	i8	1	1	0	0	0	0	0	) 1	U4	i8	1	0	1	0	0	0	0	1	U6	i8
1	0	0	0	1	0	0	1	U2	i9	1	1	0	0	1	0	0	) 1	U4	i9	1	0	1	0	1	0	0	1	U6	i9
1	0	0	0	0	1	0	1	U2	i10	1	1	0	0	0	1	0	1	U4	i10	1	0	1	0	0	1	0	1	U6	i10
1	0	0	0	1	1	0	1	U2	i11	1	1	0	0	1	1	0	1	U4	i11	1	0	1	0	1	1	0	1	U6	i11
1	0	0	0	0	0	1	1	U2	i12	1	1	0	0	0	0	1	1	U4	i12	1	0	1	0	0	0	1	1	U6	i12
1	0	0	0	1	0	1	1	U2	i13	1	1	0	0	1	0	1	1	U4	i13	1	0	1	0	1	0	1	1	U6	i13
1	0	0	0	0	1	1	1	U2	i14	1	1	0	0	0	1	1	1	U4	i14	1	0	1	0	0	1	1	1	U6	i14
1	0	0	0	1	1	1	1	U2	i15	1	1	0	0	1	1	1	1	U4	i15	1	0	1	0	) 1	1	1	1	U6	i15

Selected channel output is Vsig. All other channels output GND. See **CD74HC4067** datasheet for electrical specifications.