

# Solutions - Midterm Exam

(February 14<sup>th</sup> @ 7:30 pm)

Presentation and clarity are very important! Show your procedure!

## PROBLEM 1 (20 PTS)

- Compute the result of the following operations. The operands are signed fixed-point numbers. The result must be a signed fixed point number. For the division, use  $x = 5$  fractional bits.

1.010001 + 1.011	1001.1101 - 1.011101	0.010101 + 01.11111
10.101 × 0.10011	1.011 × 1.0101	10.10010 ÷ 0.101

$$\begin{array}{c}
 c_8=1 \\ c_7=1 \\ c_6=0 \\ c_5=1 \\ c_4=0 \\ c_3=0 \\ c_2=0 \\ c_1=0 \\ c_0=0 \\
 \hline
 1 \ 1.0 \ 1 \ 0 \ 0 \ 0 \ 1 \ + \\
 1 \ 1.0 \ 1 \ 1 \ 0 \ 0 \ 0 \\
 \hline
 1 \ 0.1 \ 0 \ 1 \ 0 \ 0 \ 1
 \end{array}
 \quad
 \begin{array}{c}
 1 \ 0 \ 0 \ 1.1 \ 1 \ 0 \ 1 \ 0 \ 0 \ - \\
 1 \ 1 \ 1 \ 1.0 \ 1 \ 1 \ 1 \ 0 \ 1 \\
 \hline
 1 \ 0 \ 1 \ 0.0 \ 1 \ 0 \ 1 \ 1 \ 1
 \end{array}
 \quad
 \begin{array}{c}
 c_9=0 \\ c_8=0 \\ c_7=1 \\ c_6=1 \\ c_5=0 \\ c_4=0 \\ c_3=0 \\ c_2=0 \\ c_1=0 \\ c_0=0 \\
 \hline
 0 \ 0 \ 0 \ 0.1 \ 0 \ 0 \ 0 \ 1 \ 1 \\
 0 \ 0 \ 0 \ 0.1 \ 0 \ 0 \ 0 \ 1 \ 1 \\
 \hline
 0 \ 1 \ 0.0 \ 1 \ 0 \ 0 \ 1 \ 1
 \end{array}$$

$$\begin{array}{c}
 10.101 \times \\
 0.10011 \\
 \hline
 1 \ 0 \ 0 \ 1 \ 1 \\
 1 \ 0 \ 1 \ 1 \\
 \hline
 1 \ 0 \ 0 \ 1 \ 1 \\
 1 \ 0 \ 0 \ 1 \ 1 \\
 \hline
 0 \ 0 \ 0 \ 0 \ 0 \\
 1 \ 0 \ 0 \ 1 \ 1 \\
 \hline
 0 \ 1 \ 1 \ 0 \ 1 \ 0 \ 0 \ 0 \ 1 \\
 \hline
 0.1 \ 1 \ 0 \ 1 \ 0 \ 0 \ 0 \ 1 \\
 \hline
 1.0 \ 0 \ 1 \ 0 \ 1 \ 1 \ 1 \ 1
 \end{array}
 \quad
 \begin{array}{c}
 1.011 \times \\
 1.0101 \\
 \hline
 1 \ 0 \ 1 \ 1 \\
 1 \ 0 \ 1 \\
 \hline
 1 \ 0 \ 1 \ 1 \\
 0 \ 0 \ 0 \ 0 \\
 \hline
 1 \ 0 \ 1 \ 1 \\
 0 \ 0 \ 1 \ 1 \ 0 \ 1 \ 1 \ 1 \\
 \hline
 0.0 \ 1 \ 1 \ 0 \ 1 \ 1 \ 1
 \end{array}$$

- ✓  $\frac{10.10010}{0.101}$ : To unsigned (numerator) and then alignment,  $a = 4$ :  $\frac{01.0111}{0.101} = \frac{01.0111}{00.1010} = \frac{010111}{001010} \equiv \frac{10111}{1010}$

$$\begin{array}{r}
 0001001001 \\
 1010 \overline{) 1011100000} \\
 \underline{1010} \phantom{0000} \\
 1100 \phantom{0000} \\
 \underline{1010} \phantom{0000} \\
 10000 \\
 \underline{1010} \\
 110
 \end{array}$$

Append  $x = 5$  zeros:  $\frac{1011100000}{1010}$

Integer Division:

$$Q = 1001001, R = 110 \\
 \rightarrow Qf = 10.01001 \ (x = 5)$$

Final result (2C):  $\frac{01.01110}{1.011} = 2C(010.01001) = 101.10111$

## PROBLEM 2 (10 PTS)

- Represent these numbers in Fixed Point Arithmetic (signed numbers). Select the minimum number of bits in each case.

✓  $-16.375$

$+16.375 = 010000.011$

$\Rightarrow -16.375 = 101111.101$

✓  $32.3125$

$+32.3125 = 0100000.0101$

- Complete the table for the following fixed point formats (signed numbers): (6 pts.)

Integer bits	Fractional Bits	FX Format	Range	Resolution
6	3	[9 3]	$[-2^5, 2^5 - 2^{-3}]$	$2^{-3}$
8	5	[13 5]	$[-2^7, 2^7 - 2^{-5}]$	$2^{-5}$

## PROBLEM 3 (40 PTS)

- Calculate the result (provide the 32-bit result) of the following operations with 32-bit floating point numbers. Truncate the results when required. When doing fixed-point division, use 4 fractional bits. Show your procedure.

✓ C1500000 + 436A0000	✓ D0A90000 - CF480000	✓ 80400000 × 7AB80000	✓ FBB80000 ÷ 49400000
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- ✓  $X = \text{C1500000} + 436\text{A0000}$ :

C1500000: 1100 0001 0101 0000 1000 0000 0000 0000

$$e + \text{bias} = 10000010 = 130 \rightarrow e = 130 - 127 = 3$$

Significand = 1.101

$$\text{C1500000} = -1.101 \times 2^3$$

436A0000: 0100 0011 0110 1010 0000 0000 0000 0000

$$e + \text{bias} = 10000110 = 134 \rightarrow e = 134 - 127 = 7$$

Significand = 1.110101

$$436\text{A0000} = 1.110101 \times 2^7$$

$$X = -1.101 \times 2^3 + 1.110101 \times 2^7 = -\frac{1.101}{2^4} \times 2^7 + 1.110101 \times 2^7$$

$$X = (-0.0001101 + 1.110101) \times 2^7$$

To subtract these unsigned numbers, we first convert to 2C:

$$R = 01.110101 - 0.0001101 = 01.110101 + 1.1110011$$

The result in 2C is:  $R = 01.1011101$

For floating point, we need to convert to sign-and-magnitude:

$$\Rightarrow R(\text{SM}) = +1.1011101$$

\* You can also do unsigned subtraction:  $X = (1.110101 - 0.0001101) \times 2^7$

$$X = 1.1011101 \times 2^7, e + \text{bias} = 7 + 127 = 134 = 10000110$$

$$X = 0100 0011 0101 1101 0000 0000 0000 0000 = 435\text{D0000}$$

- ✓  $X = \text{D0A90000} - \text{CF480000}$ :

D0A90000: 1101 0000 1010 1001 0000 0000 0000 0000

$$e + \text{bias} = 10100001 = 161 \rightarrow e = 161 - 127 = 34$$

Significand = 1.0101001

$$\text{D0A90000} = -1.0101001 \times 2^{34}$$

CF480000: 1100 1111 0100 1000 0000 0000 0000 0000

$$e + \text{bias} = 10011110 = 158 \rightarrow e = 158 - 127 = 31$$

Significand = 1.1001

$$\text{CF480000} = -1.1001 \times 2^{31}$$

$$X = -1.0101001 \times 2^{34} + 1.1001 \times 2^{31} = -1.0101001 \times 2^{34} + \frac{1.1001}{2^3} \times 2^{34}$$

$$X = -(1.0101001 - 0.0011001) \times 2^{34} \text{ (unsigned subtraction)}$$

$$X = -1.001 \times 2^{34}, e + \text{bias} = 34 + 127 = 161 = 10100001$$

$$X = 1101 0000 1001 0000 0000 0000 0000 0000 = \text{D0900000}$$

- ✓  $X = 80400000 \times 7\text{AB80000}$ :

80400000: 1000 0000 0100 0000 0000 0000 0000 0000

$$e + \text{bias} = 00000000 = 0 \rightarrow \text{Denormal number} \rightarrow e = -126 \text{ Significand} = 0.1$$

$$80400000 = -0.1 \times 2^{-126}$$

7AB80000: 0111 1010 1011 1000 0000 0000 0000 0000

$$e + \text{bias} = 11110101 = 245 \rightarrow e = 245 - 127 = 118$$

Significand = 1.0111

$$7\text{AB80000} = 1.0111 \times 2^{118}$$

$$X = (-0.1 \times 2^{-126}) \times (1.0111 \times 2^{118}) = -0.10111 \times 2^{-8} = -1.0111 \times 2^{-9}$$

$$e + \text{bias} = -9 + 127 = 118 = 01110110$$

$$X = 1011 1011 0011 1000 0000 0000 0000 0000 = \text{BB380000}$$

- ✓  $X = \text{FBB80000} \div 49400000$ :

FBB80000: 1111 1011 1011 1000 0000 0000 0000 0000

$$e + \text{bias} = 11110111 = 247 \rightarrow e = 247 - 127 = 120$$

Significand = 1.0111

$$\text{FBB80000} = -1.0111 \times 2^{120}$$

49400000: 0100 1001 0100 0000 0000 0000 0000 0000

$$e + \text{bias} = 10010010 = 146 \rightarrow e = 146 - 127 = 19$$

Significand = 1.1

$$49400000 = 1.1 \times 2^{19}$$

$$\begin{array}{r} \begin{array}{cccccccc} c_9 & c_8 & c_7 & c_6 & c_5 & c_4 & c_3 & c_2 & c_1 & c_0 \\ 0 & 1.1 & 1 & 0 & 1 & 0 & 1 & 0 & 1 & 0 \end{array} + \\ \begin{array}{cccccccc} 1 & 1.1 & 1 & 1 & 0 & 0 & 1 & 1 & & \end{array} \\ \hline \begin{array}{cccccccc} & 0 & 1.1 & 0 & 1 & 1 & 1 & 0 & 1 & \end{array} \\ \\ \begin{array}{cccccccc} b_8 & b_7 & b_6 & b_5 & b_4 & b_3 & b_2 & b_1 & b_0 \\ 1.1 & 1 & 0 & 1 & 0 & 1 & 0 & 1 & 0 \end{array} - \\ \begin{array}{cccccccc} 0.0 & 0 & 0 & 1 & 1 & 0 & 1 & & \end{array} \\ \hline \begin{array}{cccccccc} 1.1 & 0 & 1 & 1 & 1 & 0 & 1 & & \end{array} \end{array}$$

$$\begin{array}{r} \begin{array}{cccccccc} b_8 & b_7 & b_6 & b_5 & b_4 & b_3 & b_2 & b_1 & b_0 \\ 1.0 & 1 & 0 & 1 & 0 & 0 & 1 & 1 & 0 \end{array} - \\ \begin{array}{cccccccc} 0.0 & 0 & 1 & 1 & 0 & 0 & 1 & & \end{array} \\ \hline \begin{array}{cccccccc} 1.0 & 0 & 1 & 0 & 0 & 0 & 0 & & \end{array} \end{array}$$

$$X = -\frac{1.0111 \times 2^{120}}{1.1 \times 2^{19}} = -\frac{1.0111}{1.1} \times 2^{101}$$

Alignment:

$$\frac{1.0111}{1.1} = \frac{1.0111}{1.1000} = \frac{10111}{11000}$$

Append  $x = 4$  zeros:  $\frac{101110000}{11000}$

Integer division  
 $Q = 1111 \rightarrow Qf = 0.1111$

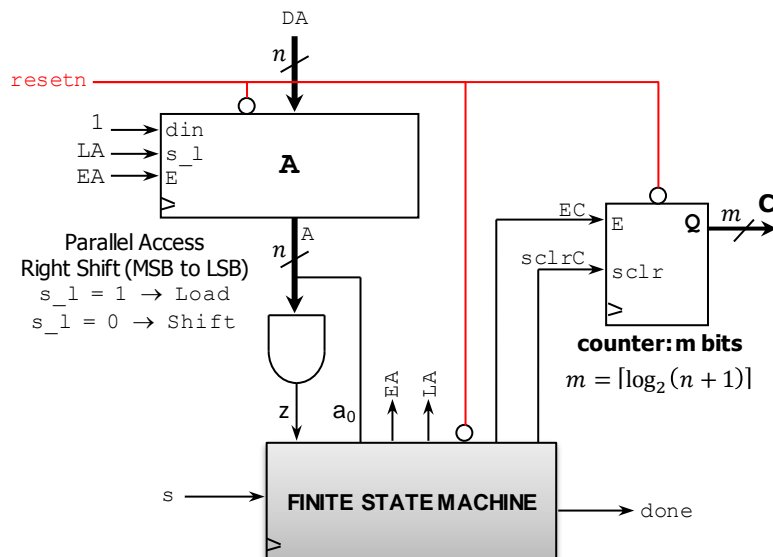
$$\begin{array}{r} 000001111 \\ 11000 \overline{) 101110000} \\ \underline{11000} \phantom{000} \\ 101100 \phantom{00} \\ \underline{11000} \phantom{00} \\ 101000 \phantom{0} \\ \underline{11000} \phantom{0} \\ 100000 \\ \underline{11000} \\ 1000 \end{array}$$

Thus:  $X = -0.1111 \times 2^{101} = -1.111 \times 2^{100}$   
 $e + \text{bias} = 100 + 127 = 227 = 11100011$

$X = 1111 \ 0001 \ 1111 \ 0000 \ 0000 \ 0000 \ 0000 \ 0000 = \text{F1F00000}$

#### PROBLEM 4 (30 PTS)

- "Counting 0's" Circuit: It counts the number of bits in register A that has the value of '0'.  
 The digital system is depicted below: FSM + Datapath. Example: For  $n = 8$ : if  $A = 00110110$ , then  $C = 0100$ .  
 ✓ m-bit counter:  $sclr$ . If  $E = sclr = 1$ , the count is initialized to zero. If  $E = 1, sclr = 0$ , the count is increased by 1.  
 ✓ Parallel access shift register: If  $E = 1: s\_l = 1 \rightarrow \text{Load}, s\_l = 0 \rightarrow \text{Shift}$ .
- Sketch the Finite State Machine diagram (in ASM form) given the algorithm (for  $n = 8, m = 4$ ). (18 pts.)  
 ✓ The process begins when  $s$  is asserted, at this moment we capture  $DA$  on register A. Then the process starts by shifting A one bit at a time. The process is concluded when  $A = 2^n - 1$ . The signal  $done$  is asserted when we finish counting.  
 ✓ Note: If  $A = 2^n - 1 \rightarrow z = 1$ , else  $z = 0$ . As A is being shifted, each time  $a_0 = 0$ , we need to increase the count C.
- Complete the timing diagram (next page) where  $n = 8, m = 4$ . (12 pts.)



#### ALGORITHM

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C ← 0
while A ≠ 11...1 ( $2^n - 1$ )
    if  $a_0 = 0$  then
        C ← C + 1
    end if
    right shift A
end while

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Finite State Machine:

