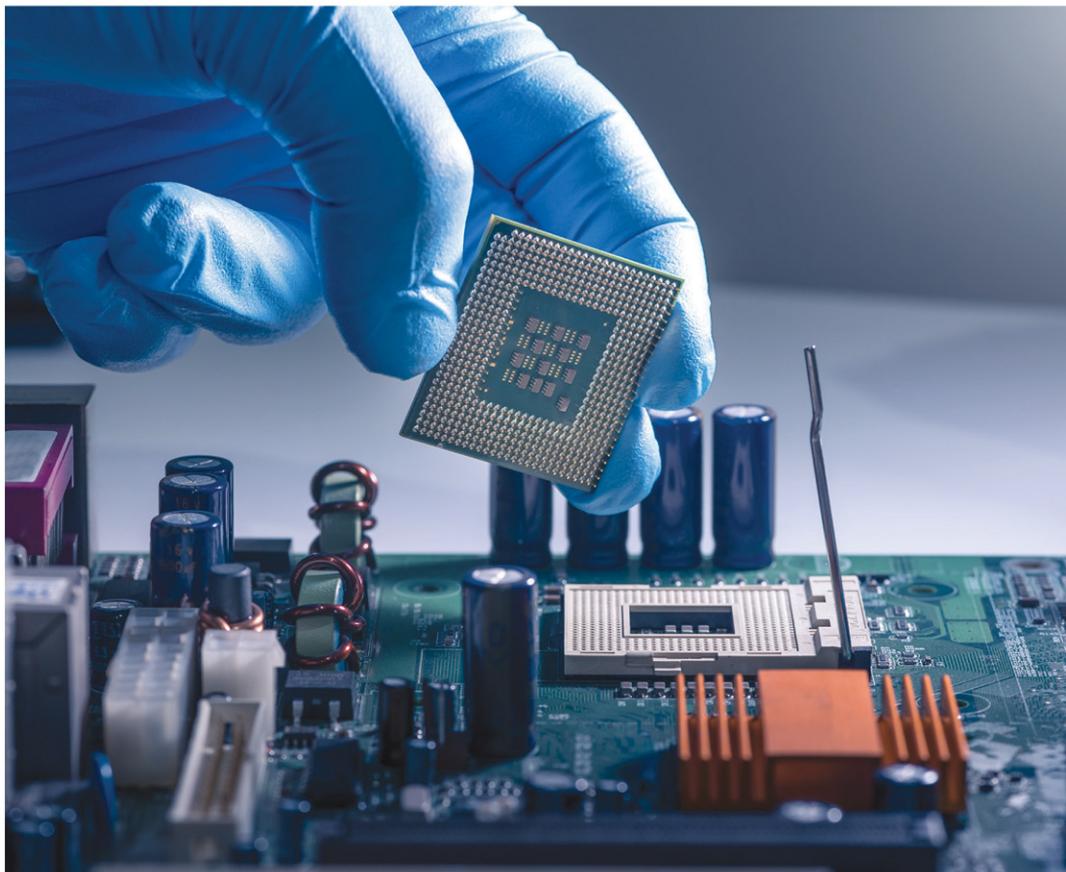


Advanced Technologies for Next Generation Integrated Circuits

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Chapter 5

Memristor Devices and Memristor-based Circuits

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There are four fundamental circuit variables: voltage, current, charge and magnetic flux. Until the year 1971, there were only three fundamental components: resistor, capacitor and the inductor. In that year 1971, Leon O. Chua proposed a new device named “Memristor” which relates charge and flux. At the time, due to lack of sophisticated fabrication facilities, the new device did not receive much attention until HP Labs successfully fabricated one in 2007. This fabrication of the new device has provided device research with a new perspective as the memristor exhibits a new hysteresis phenomenon named as “Pinched Hysteresis”. The memristor can remember the voltage that passed through it even when the supply is turned off. Hence the name Memory + Resistor, Memristor. After the device was fabricated successfully, research has been done extensively implementing the memristor in various applications which require reconfigurability. The memristor has been used from oscillators to neural networks and logic gates to security applications giving it a wide range of applications. This chapter presents the device description, characteristics and various applications of the memristor in analog and digital applications.

This chapter is organized as follows: Different types of memristors are presented in section 2. Fabrication principles of the memristor and how it works are presented in section 3. For simulation purposes, various models for memristors have been proposed. Such models are presented in Section 4. The electrical characteristics of the memristor are presented in section 5. Applications of memristors in analog and digital nanoelectronics are presented in section 6 and section 7, respectively. Summary and future directions are presented in section 8. Table 1 summarizes the notations and symbols used in the current chapter.

1 Introduction

1.1 Brief History of Memristor

There were only three fundamental circuit elements known in 1971: resistor, capacitor and inductor. In that year, Leon O. Chua presented in his article titled “Memristor – The Missing Circuit Element”, a device named memristor [18]. There are four fundamental circuit variables: voltage (v), current (i), charge (q) and magnetic flux (ϕ). Because there are four variables and three fundamental devices, Chua wanted to attain symmetry and theoretically presented the memristor. The relation between voltage and current is used by the resistor, voltage and charge by the capacitor and current and magnetic flux by the inductor. The memristor uses the relation between charge and magnetic flux [80], as shown in figure 1. It was demonstrated by Chua mathematically that the device he proposed would be able to provide a non-linear relationship between the flux and the charge. But even before Chua published his work, there had been some current-voltage behaviors observed that could not be explained. In 2015, a new research was published by Leon O. Chua and researchers at Hong Kong University which revealed that the first man-made memristor was actually developed in 1801 [49]. Humphry Davy conducted a carbon arc discharge experiment, which can generate light without the use of fire. The same experiment was repeated with a modern power supply and observed which revealed the fingerprint of the memristor.

References

1. Abdalla, H., Pickett, M.D.: SPICE Modeling of Memristors. In: Proceedings of the IEEE International Symposium on Circuits and Systems, pp. 1832–1835 (2011)
2. Adhikari, S.P., Sah, M.P., Kim, H., Chua, L.O.: Three Fingerprints of Memristor. *IEEE Transactions on Circuits and Systems I: Regular Papers* **60**(11), 3008–3021 (2013). DOI 10.1109/TCSI.2013.2256171
3. Agu, E., Mohanty, S., Kougianos, E., Gautam, M.: Simscape Design Flow for Memristor Based Programmable Oscillators. In: Proceedings of the 23rd ACM Great Lakes Symposium on VLSI (GLSVLSI), pp. 223–224 (2014)
4. Agu, E., Mohanty, S.P., Kougianos, E., Gautam, M.: Simscape Based Design Flow for Memristor Based Programmable Oscillators. In: Proceedings of the 23rd ACM/IEEE Great Lakes Symposium on VLSI, pp. 223–224 (2014)
5. Alharbi, A.G., Fouda, M.E., Chowdhury, M.H.: Memristor Emulator Based on Practical Current Controlled Model. In: IEEE 58th International Midwest Symposium on Circuits and Systems (MWSCAS), pp. 1–4 (2015). DOI 10.1109/MWSCAS.2015.7282109
6. Arbet, D., Brenkus, J., Majer, L., Stopjakova, V.: Oscillation-Based Built-In Self Test of Integrated Active Analog Filters. In: Applied Electronics (AE), 2011 International Conference on, pp. 1–4 (2011)
7. Atasoyu, M., Altun, M., Ozoguz, S., Roy, K.: Spin-torque Memristor Based Offset Cancellation Technique for Sense Amplifiers. In: 2017 14th International Conference on Synthesis, Modeling, Analysis and Simulation Methods and Applications to Circuit Design (SMACD), pp. 1–4 (2017). DOI 10.1109/SMACD.2017.7981595
8. Beck, A., Bednorz, J.G., Gerber, C., Rossel, C., Widmer, D.: Reproducible switching effect in thin oxide films for memory applications. *Applied Physics Letters* **77**(1), 139–141 (2000)
9. Bedair, S.M., Zavada, J.M., El-Masry, N.: Spintronic Memories to Revolutionize Data Storage. *IEEE Spectrum* (2010)
10. Bielek, D., Bielek, Z., Biolkova, V.: SPICE Modeling of Memristive, Memcapacitative and Meminductive Systems. In: Proceedings of the European Conference on Circuit Theory and Design, pp. 249–252 (2009)
11. Bielek, D., Biolkova, V., Kolka, Z.: Modified MIM Model of Titanium Dioxide Memristor for Reliable Simulations in SPICE. In: 14th International Conference on Synthesis, Modeling, Analysis and Simulation Methods and Applications to Circuit Design (SMACD), pp. 1–4 (2017). DOI 10.1109/SMACD.2017.7981564
12. Bielek, Z., Bielek, D., Biolkova, V.: SPICE Model of Memristor With Nonlinear Dopant Drift. *Radioengineering* **18**, 210–214 (2009)
13. Borghetti, J., Snider, G.S., Kuekes, P.J., Yang, J.J., Stewart, D.R., Williams, R.S.: Memristive Switches Enable Stateful Logic Operations via Material Implication. *Nature* **464**, 873 (2010). URL <http://dx.doi.org/10.1038/nature08940>
14. Buot, F.A., Rajagopal, A.K.: Binary Information Storage at Zero Bias in Quantum-Well Diodes. *Journal of Applied Physics* **76**(9), 5552–5560 (1994)
15. Chen, Y., Li, H., Wang, X.: Spintronic Devices: From Memory to Memristor. In: 2010 International Conference on Communications, Circuits and Systems (ICCCAS), pp. 811–816 (2010). DOI 10.1109/ICCCAS.2010.5581868
16. Chen, Y., Wang, X.: Compact Modeling and Corner Analysis of Spintronic Memristor. In: Nanoscale Architectures, 2009. NANOARCH '09. IEEE/ACM International Symposium on, pp. 7–12 (2009)
17. Cheng, K., Strukov, D.: 3D CMOS-Memristor Hybrid Circuits: Devices, Integration, Architecture, and Applications. In: Proceedings of the 2012 ACM international symposium on International Symposium on Physical Design, pp. 33–40. ACM (2012)
18. Chua, L.: Memristor-The Missing Circuit Element. *IEEE Transactions on Circuit Theory* **18**(5), 507–519 (1971)
19. da Costa, H.J.B., de Assis Brito Filho, F., de Araujo do Nascimento, P.I.: Memristor Behavioural Modeling and Simulations Using Verilog-AMS. In: IEEE 3rd Latin American Symposium on Circuits and Systems (LASCAS), pp. 1–4 (2012). DOI 10.1109/LASCAS.2012.6180334
20. Ebong, I.E., Mazumder, P.: CMOS and Memristor-Based Neural Network Design for Position Detection. *Proceedings of the IEEE* **100**(6), 2050–2060 (2012). DOI 10.1109/JPROC.2011.2173089
21. Engelberg, S., Hazout, H., Hirshowitz, J.: A Capacitive-Sensing Based Simple Serial Mouse. *Instrumentation & Measurement Magazine, IEEE* **13**(2), 32–36 (2010)
22. Erokhin, V.: Organic Memristors : Basic Principles. In: Proceedings of 2010 IEEE International Symposium on Circuits and Systems, pp. 5–8 (2010). DOI 10.1109/ISCAS.2010.5537145
23. Fan, D., Sharad, M., Roy, K.: Design and Synthesis of Ultralow Energy Spin-Memristor Threshold Logic. *IEEE Transactions on Nanotechnology* **13**(3), 574–583 (2014). DOI 10.1109/TNANO.2014.2312177
24. Farooq, U., Aslam, M.H.: Design and Implementation of Basic Building Blocks of FPGA using Memristor-Transistor Hybrid Approach. In: Fifth International Conference on the Innovative Computing Technology (INTECH 2015), pp. 142–147 (2015). DOI 10.1109/INTECH.2015.7173484
25. Fayyazi, A., Ansari, M., Kamal, M., Afzali-Kusha, A., Pedram, M.: An Ultra Low-Power Memristive Neuromorphic Circuit for Internet of Things Smart Sensors. *IEEE Internet of Things Journal* **PP**(99), 1–1 (2018). DOI 10.1109/JIOT.2018.2799948
26. Fei, W., Yu, H., Zhang, W., Yeo, K.S.: Design Exploration of Hybrid CMOS and Memristor Circuit by New Modified Nodal Analysis. *IEEE Transactions on Very Large Scale Integration (VLSI) Systems* **PP**(99) (2011). DOI 10.1109/TVLSI.2011.2136443
27. Gandhi, G., Aggarwal, V., Chua, L.O.: The First Radios Were Made Using Memristors! *IEEE Circuits and Systems Magazine* **13**(2), 8–16 (2013). DOI 10.1109/MCAS.2013.2256255
28. Garitselov, O., Mohanty, S.P., Kougianos, E.: A Comparative Study of Metamodels for Fast and Accurate Simulation of Nano-CMOS Circuits. *IEEE Transactions on Semiconductor Manufacturing* **25**(1), 26–36 (2012)
29. Gergel-Hackett, N., Hamadani, B., Dunlap, B., Suehle, J., Richter, C., Hacker, C., Gundlach, D.: A Flexible Solution-Processed Memristor. *Electron Device Letters, IEEE* **30**(7), 706–708 (2009)

30. Ghosal, P., Mohanty, S.P.: Power Minimization of a Memristor-Based Wien Bridge Oscillator through a Simscape Framework. In: 2015 IEEE International Symposium on Nanoelectronic and Information Systems, pp. 83–88 (2015). DOI 10.1109/iNIS.2015.63
31. Ho, P.W.C., Hatem, F.O., Almurib, H.A.F., Kumar, T.N.: Enhanced SPICE Memristor Model with Dynamic Ground. In: IEEE International Circuits and Systems Symposium (ICSyS), pp. 130–132 (2015). DOI 10.1109/CircuitsAndSystems.2015.7394079
32. Hu, M., Li, H., Chen, Y., Wu, Q., Rose, G.S., Linderman, R.W.: Memristor Crossbar-Based Neuromorphic Computing System: A Case Study. *IEEE Transactions on Neural Networks and Learning Systems* **25**(10), 1864–1878 (2014). DOI 10.1109/TNNLS.2013.2296777
33. Hu, M., Li, H., Pino, R.E.: Fast Statistical Model of TiO₂ Thin-Film Memristor and Design Implication. In: IEEE/ACM International Conference on Computer-Aided Design (ICCAD), pp. 345–352 (2011). DOI 10.1109/ICCAD.2011.6105353
34. Jo, S.H., Chang, T., Ebong, I., Bhadviya, B.B., Mazumder, P., Lu, W.: Nanoscale Memristor Device as Synapse in Neuromorphic Systems. *Nano Letters* **10**(4), 1297–1301 (2010). DOI 10.1021/nl904092h. URL <http://pubs.acs.org/doi/abs/10.1021/nl904092h>. PMID: 20192230
35. Jo, S.H., Lu, W.: CMOS Compatible Nanoscale Nonvolatile Resistance Switching Memory. *Nano Letters* **8**(2), 392–397 (2008). DOI 10.1021/nl073225h. URL <http://pubs.acs.org/doi/abs/10.1021/nl073225h>. PMID: 18217785
36. Johnson, D.: The Memristor's Fundamental Secrets Revealed. *IEEE Spectrum* (2013)
37. Khanal, G.M., Acciarito, S., Cardarilli, G.C., Chakraborty, A., Nunzio, L.D., Fazzolari, R., Cristini, A., Re, M., Susi, G.: Synaptic Behaviour in ZnO-rGO Composites Thin Film Memristor. *Electronics Letters* **53**(5), 296–298 (2017)
38. Khanal, G.M., Cardarilli, G., Chakraborty, A., Acciarito, S., Mulla, M.Y., Nunzio, L.D., Fazzolari, R., Re, M.: A ZnO-rGO Composite Thin Film Discrete Memristor. In: IEEE International Conference on Semiconductor Electronics (ICSE), pp. 129–132 (2016). DOI 10.1109/SMELEC.2016.7573608
39. Kim, H., Sah, M.P., Yang, C., Cho, S., Chua, L.O.: Memristor Emulator for Memristor Circuit Applications. *IEEE Transactions on Circuits and Systems: Regular Papers* **59**(10), 2422–2431 (2012). DOI 10.1109/TCSI.2012.2188957
40. Kundert, K., Zinke, O.: The Designer's Guide to Verilog-AMS, 1st edn. Kluwer Academic Publishers, Boston (2004)
41. Kuon, I., Tessier, R., Rose, J.: Fpga Architecture: Survey and Challenges. *Foundations and Trends in Electronic Design Automation* **2**(2), 135–253 (2008)
42. Kvatinsky, S., Belousov, D., Liman, S., Satat, G., Wald, N., Friedman, E.G., Kolodny, A., Weiser, U.C.: MAGIC - Memristor-Aided Logic. *IEEE Transactions on Circuits and Systems II: Express Briefs* **61**(11), 895–899 (2014). DOI 10.1109/TCSI.2014.2357292
43. Kvatinsky, S., Friedman, E.G., Kolodny, A., Weiser, U.C.: TEAM: ThrEshold Adaptive Memristor Model. *IEEE Transactions on Circuits and Systems I: Regular Papers* **60**(1), 211–221 (2013). DOI 10.1109/TCSI.2012.2215714
44. Kvatinsky, S., Kolodny, A., Weiser, U.C., Friedman, E.G.: Memristor-Based IMPLY Logic Design Procedure. In: IEEE 29th International Conference on Computer Design (ICCD), pp. 142–147 (2011). DOI 10.1109/ICCD.2011.6081389
45. Kvatinsky, S., Talisveyberg, K., Fliter, D., Kolodny, A., Weiser, U., Friedman, E.: Models of memristors for SPICE simulations. In: Proceedings of the 27th Convention of Electrical & Electronics Engineers in Israel (IEEEI), pp. 1–5 (2012). URL <http://webee.technion.ac.il/people/skva/memristor.htm>
46. Lee, W., Park, J., Kim, J.: Electromagnetic Simulations of a Neuromorphic Hardware Using PEEC and Memristor SPICE Models. In: IEEE Electrical Design of Advanced Packaging and Systems Symposium (EDAPS), pp. 1–3 (2017). DOI 10.1109/EDAPS.2017.8276969
47. Lehtonen, E., Laiho, M.: Stateful Implication Logic With Memristors. In: Proceedings of the IEEE/ACM International Symposium on Nanoscale Architectures, pp. 33–36 (2009). DOI 10.1109/NANOARCH.2009.5226356
48. Lehtonen, E., Poikonen, J.H., Laiho, M.: Two Memristors Suffice to Compute All Boolean Functions. *Electronics Letters* **46**(3), 239–240 (2010). DOI 10.1049/el.2010.3407
49. Lin, D., Chua, L., Hui, S.Y.: The First Man-Made Memristor: Circa 1801 [Scanning Our Past]. *Proceedings of the IEEE* **103**(1), 131–136 (2015). DOI 10.1109/JPROC.2014.2374754
50. Liu, G., Fang, L., Li, N., Sui, B.C., Duan, Z.K.: New Behavioral Modeling Method for Crossbar-Based Memristor. In: Microelectronics and Electronics (PrimeAsia), 2010 Asia Pacific Conference on Postgraduate Research in, pp. 356 –359 (2010)
51. Locatelli, N., Cros, V., Grollier, J.: Spin-Torque Building Blocks . *Nature Materials* **13**, 11–20 (2013)
52. Loong, J.T.H., Hashim, N.A.N., Hamid, M.S., Hamid, F.A.: Performance Analysis of CMOS-Memristor Hybrid Ring Oscillator Physically Unclonable Function (RO-PUF). In: IEEE International Conference on Semiconductor Electronics (ICSE), pp. 304–307 (2016). DOI 10.1109/SMELEC.2016.7573652
53. Loong, J.T.H., Ismail, K.A.S.C., Hamid, F.A.: Effect of Different Memristor Window Function with Variable Random Resistance on the Performance of Memristor-Based RO-PUF. In: International Conference on Advances in Electrical, Electronic and Systems Engineering (ICAES), pp. 445–450 (2016). DOI 10.1109/ICAES.2016.7888086
54. Lv, X., Cai, Y., Yang, Y., Yu, Z., Fang, Y., Wang, Z., Wu, L., Liu, J., Zhang, W., Huang, R.: A Neural Network Circuit with Associative Learning and Forgetting Process Based on Memristor Neuromorphic Device. In: 2017 IEEE 12th International Conference on ASIC (ASICON), pp. 211–214 (2017). DOI 10.1109/ASICON.2017.8252449
55. Mahvash, M., Parker, A.: A Memristor SPICE Model for Designing Memristor Circuits. In: Proceedings of the 53rd IEEE International Midwest Symposium on Circuits and Systems (MWSCAS),, pp. 989–992 (2010)
56. Mathworks: <https://www.mathworks.com>
57. Mbarek, K., Rziga, F.O., Ghedira, S., Besbes, K.: An Analysis of the Dynamics of SPICE Memristor Model. In: International Conference on Control, Automation and Diagnosis (ICCAD), pp. 054–059 (2017). DOI 10.1109/CADIAG.2017.8075630
58. Mohanty, S.P.: Memristor: From Basics to Deployment. *IEEE Potentials* **32**(3), 34–39 (2013)

59. Mohanty, S.P.: Nanoelectronic Mixed-Signal System Design. 9780071825719. McGraw-Hill Education (2015)
60. Moser, M., Zangl, H.: Temperature and Pressure Monitoring of a Whipped Cream Device. In: IEEE Sensors, pp. 683–686 (2009)
61. Nandakumar, S.R., Rajendran, B.: Verilog-A Compact Model for a Novel Cu/SiO₂/W Quantum Memristor. In: International Conference on Simulation of Semiconductor Processes and Devices (SISPAD), pp. 169–172 (2016). DOI 10.1109/SISPAD.2016.7605174
62. Ntinis, V., Vourkas, I., Abusleme, A., Sirakoulis, G.C., Rubio, A.: Experimental Study of Artificial Neural Networks Using a Digital Memristor Simulator. *IEEE Transactions on Neural Networks and Learning Systems* **PP**(99), 1–13 (2018). DOI 10.1109/TNNLS.2018.2791458
63. Oblea, A., Timilsina, A., Moore, D., Campbell, K.: Silver chalcogenide based memristor devices. In: Neural Networks (IJCNN), The 2010 International Joint Conference on, pp. 1 –3 (2010)
64. Pershin, Y.V., Di Ventra, M.: Memristive Circuits Simulate Memcapacitors and Meminductors. *Electronics Letters* **46**(7), 517–518 (2010)
65. Pershin, Y.V., Di Ventra, M.: Practical Approach to Programmable Analog Circuits With Memristors. *IEEE Transactions on Circuits and Systems I: Regular Papers* **57**(8), 1857–1864 (2010)
66. Pino, R., Bohl, J., McDonald, N., Wysocki, B., Rozwood, P., Campbell, K., Oblea, A., Timilsina, A.: Compact Method for Modeling and Simulation of Memristor Devices: Ion Conductor Chalcogenide-Based Memristor Devices. In: Nanoscale Architectures (NANOARCH), 2010 IEEE/ACM International Symposium on, pp. 1 –4 (2010)
67. Prodromakis, T., Michelakis, K., Toumazou, C.: Fabrication and Electrical Characteristics of Memristors with TiO_2/TiO_{2-x} Active Layers. In: Circuits and Systems (ISCAS), Proceedings of 2010 IEEE International Symposium on, pp. 1520 –1522 (2010)
68. Radwan, A.G., Zidan, M.A., Salama, K.N.: HP Memristor Mathematical Model for Periodic Signals and DC. In: 53rd IEEE International Midwest Symposium on Circuits and Systems, pp. 861–864 (2010). DOI 10.1109/MWSCAS.2010.5548670
69. Rak, A., Cserey, G.: Macromodeling of the Memristor in SPICE. *IEEE Transactions on Computer-Aided Design of Integrated Circuits and Systems* **29**(4), 632–636 (2010)
70. Ranjan, R., Kyrmandidis, A., Hellweg, W.L., Ponce, P.M., Saleh, L.A., Schroeder, D., Krautschneider, W.H.: Integrated Circuit with Memristor Emulator Array and Neuron Circuits for Neuromorphic Pattern Recognition. In: 39th International Conference on Telecommunications and Signal Processing (TSP), pp. 265–268 (2016). DOI 10.1109/TSP.2016.7760875
71. Rose, G.S., McDonald, N., Yan, L.K., Wysocki, B., Xu, K.: Foundations of Memristor Based PUF Architectures. In: 2013 IEEE/ACM International Symposium on Nanoscale Architectures (NANOARCH), pp. 52–57 (2013). DOI 10.1109/NanoArch.2013.6623044
72. Rose, G.S., Rajendran, J., Manem, H., Karri, R., Pino, R.E.: Leveraging Memristive Systems in the Construction of Digital Logic Circuits. *Proceedings of the IEEE* **100**(6), 2033–2049 (2012)
73. Sah, M.P., Kim, H., Chua, L.O.: Brains Are Made of Memristors. *IEEE Circuits and Systems Magazine* **14**(1), 12–36 (2014). DOI 10.1109/MCAS.2013.2296414
74. Sah, M.P., Yang, C., Budhathoki, R.K., Kim, H.: Features of Memristor Emulator-Based Artificial Neural Synapses. In: IEEE International Symposium on Circuits and Systems (ISCAS2013), pp. 421–424 (2013). DOI 10.1109/ISCAS.2013.6571870
75. Salama, K.N.: Memristor Model. <http://sensors.kaust.edu.sa/tools/memristor-model>
76. Sampath, M., Mane, P.S., Ramesha, C.K.: Hybrid CMOS-Memristor Based FPGA Architecture. In: International Conference on VLSI Systems, Architecture, Technology and Applications (VLSI-SATA), pp. 1–6 (2015). DOI 10.1109/VLSI-SATA.2015.7050461
77. Shim, Y., Sengupta, A., Roy, K.: Low-Power Approximate Convolution Computing Unit with Domain-Wall Motion Based - Spin-Memristor - for Image Processing Applications. In: 2016 53nd ACM/EDAC/IEEE Design Automation Conference (DAC), pp. 1–6 (2016). DOI 10.1145/2897937.2898042
78. Shin, S., Kim, K., Kang, S.: Memristor applications for programmable analog ICs. *Nanotechnology, IEEE Transactions on* **10**(2), 266–274 (2011)
79. Shrivastava, A., Singh, J.: Dual Sided Doped Memristor and It's Mathematical Modelling. In: IEEE 20th International Conference on Electronics, Circuits, and Systems (ICECS), pp. 49–51 (2013). DOI 10.1109/ICECS.2013.6815342
80. Strukov, D.B., Snider, G.S., Stewart, D.R., Williams, R.S.: The Missing Memristor Found. *Nature* **453**, 80–83 (2008)
81. Thang, H.M.: Memristor Model-MATLAB Central. www.mathworks.com/matlabcentral/fileexchange/25082
82. Thangkhiew, P.L., Gharpinde, R., Datta, K.: Efficient Mapping of Boolean Functions to Memristor Crossbar Using MAGIC NOR Gates. *IEEE Transactions on Circuits and Systems I: Regular Papers* **PP**(99), 1–11 (2018). DOI 10.1109/TCSI.2018.2792474
83. Vincent, A.F., Larroque, J., Locatelli, N., Romdhane, N.B., Bichler, O., Gamrat, C., Zhao, W.S., Klein, J.O., Galdin-Retailleau, S., Querlioz, D.: Spin-Transfer Torque Magnetic Memory as a Stochastic Memristive Synapse for Neuromorphic Systems. *IEEE Transactions on Biomedical Circuits and Systems* **9**(2), 166–174 (2015). DOI 10.1109/TBCAS.2015.2414423
84. Vourkas, I., Abusleme, A., Ntinis, V., Sirakoulis, G.C., Rubio, A.: A Digital Memristor Emulator for FPGA-Based Artificial Neural Networks. In: 1st IEEE International Verification and Security Workshop (IVSW), pp. 1–4 (2016). DOI 10.1109/IVSW.2016.7566607
85. Wang, F.Y.: Memristor for Introductory Physics. *Physics* pp. 1–4 (2008)
86. Wang, X., Chen, Y.: Spintronic Memristor Devices and Application. In: Design, Automation Test in Europe Conference Exhibition (DATE), 2010, pp. 667–672 (2010)
87. Wang, X., Chen, Y., Xi, H., Li, H., Dimitrov, D.: Spintronic Memristor Through Spin-Torque-Induced Magnetization Motion. *IEEE Electron Device Letters* **30**(3), 294–297 (2009). DOI 10.1109/LED.2008.2012270

88. Wang, X., Deng, H., Feng, W., Yang, Y., Chen, K.: Memristor-Based XOR Gate for Full Adder. In: 35th Chinese Control Conference (CCC), pp. 5847–5851 (2016). DOI 10.1109/ChiCC.2016.7554272
89. Wang, X., Yu, Y., Yang, N., Ma, H., Yang, C., Zhong, S., Nyima, T.: New Synchronization Criteria for Memristor-Based Recurrent Neural Networks with Mixed Delays. In: 2017 International Workshop on Complex Systems and Networks (IWCSN), pp. 210–217 (2017). DOI 10.1109/IWCSN.2017.8276529
90. Williams, R.: How We Found The Missing Memristor. *Spectrum, IEEE* **45**(12), 28–35 (2008)
91. Xu, N., Fang, L., Chi, Y., Zhang, C., Tang, Z.: Resistance Uniformity of TiO₂ Memristor With Different Thin Film Thickness. In: 14th IEEE International Conference on Nanotechnology, pp. 727–731 (2014). DOI 10.1109/NANO.2014.6968102
92. Yakopcic, C., Rahman, N., Atahary, T., Taha, T.M., Douglass, S.: Cognitive Domain Ontologies in a Memristor Crossbar Architecture. In: 2017 IEEE National Aerospace and Electronics Conference (NAECON), pp. 76–83 (2017). DOI 10.1109/NAECON.2017.8268748
93. Yakopcic, C., Taha, T.M., Subramanyam, G., Pino, R.E.: Memristor SPICE Model and Crossbar Simulation Based on Devices with Nanosecond Switching Time. In: The 2013 International Joint Conference on Neural Networks (IJCNN), pp. 1–7 (2013). DOI 10.1109/IJCNN.2013.6706773
94. Yanambaka, V.P., Mohanty, S.P., Kougiannos, E.: Novel FinFET Based Physical Unclonable Functions for Efficient Security Integration in the IoT. In: 2016 IEEE International Symposium on Nanoelectronic and Information Systems (iNIS), pp. 172–177 (2016). DOI 10.1109/iNIS.2016.047
95. Zaplatilek, K.: Memristor modeling in MATLAB® & Simulink®. In: Proceedings of the 5th European conference on European Computing Conference, pp. 62–67 (2011)
96. Zhang, Y., Zhang, X., Yu, J.: Approximated SPICE Model for Memristor. In: Proceedings of the International Conference on Communications, Circuits and Systems, pp. 928–931 (2009)
97. Zheng, G., Mohanty, S.P., Kougiannos, E.: Verilog-AMS-POM: Verilog-AMS Integrated POlynomial Metamodelling of a Memristor-based Oscillator (June 2-6, 2013, Austin, Texas)
98. Zheng, G., Mohanty, S.P., Kougiannos, E., Okobiah, O.: Polynomial Metamodel Integrated Verilog-AMS for Memristor-Based Mixed-Signal System Design. In: Proceedings of the IEEE 56th International Midwest Symposium on Circuits and Systems (MWSCAS), pp. 916–919 (2013)

Advanced Technologies for Next Generation Integrated Circuits

Although existing nanometer CMOS technology is expected to remain dominant for the next decade, new non-classical devices are being developed as the potential replacements of silicon CMOS, in order to meet the ever-present demand for faster, smaller, more efficient integrate circuits.

Many new devices are based on novel emerging materials such as one-dimensional carbon nanotubes and two-dimensional graphene, non-graphene two-dimensional materials, and transition metal dichalcogenides. Such devices use on/off operations based on quantum mechanical current transport, and so their design and fabrication require an understanding of the electronic structures of materials and technologies. Moreover, new electronic design automation (EDA) tools and techniques need to be developed based on integrating devices from emerging novel material-based technologies.

The aim of this book is to explore the materials and design requirements of these emerging integrated circuit technologies, and to outline their prospective applications. It will be useful for academics and research scientists interested in future directions and developments in design, materials and applications of novel integrated circuit technologies, and for research and development professionals working at the cutting edge of integrated circuit development.

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