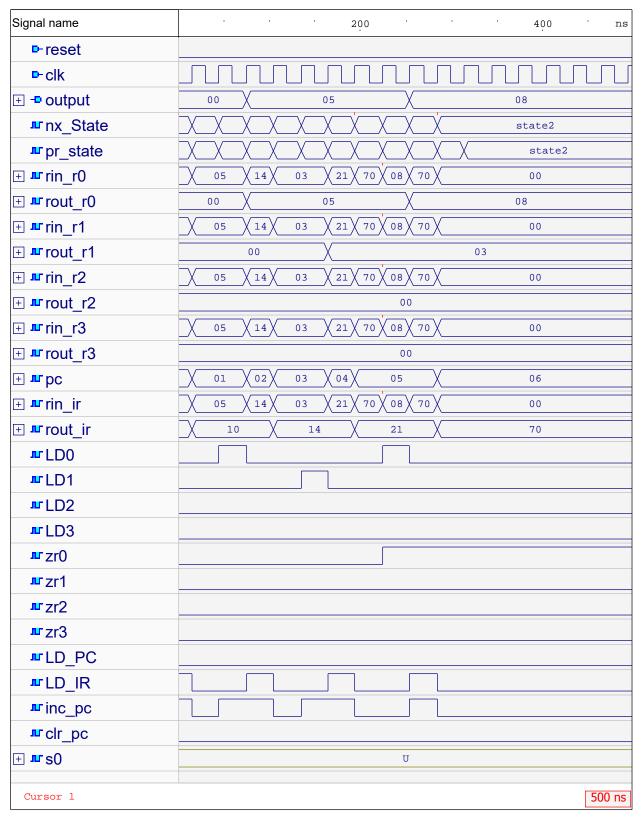
1 add.awc



Signal name	200 400	' ns
± 1 s1	υ	
reg_select_mux0 reg_select_mux0	0 1 0	
reg_select_mux1 reg_select_mux1	0 1 0	
	1 2 7	
± ™ mux0	00 \(05 \) 00 \(\) 03 \(\) 05 \(\) 08	
± ™ mux1	00 05 03 08	
	00 X 08 X 0B X 10	
⊕ ™ busline	05 14 03 21 70 08 70 00	
⊕ ™ MData	05 \(14 \) 03 \(\) 21 \(\) 70 \(\) 00	
r bus_sel		
± r cmd	0 1	
⊕ ≖ data	00	
⊞ V=my_Rom	7F, 00, 00, 00, 00, 00, 70, 21, 03, 14, 05, 10	
Cursor 1		500 ns