



## Memory Ordering in Modern Microprocessors, Part I

Jun 30, 2005 By [Paul E. McKenney \(/user/1001349\)](#)  
in

26 people like this. [Sign Up](#) to see what your friends like.

*One important difference among CPU families is how they allow memory accesses to be reordered. Linux has to support them all.*

### Conclusions

As noted earlier, the good news is Linux's memory-ordering primitives and synchronization primitives make it unnecessary for most Linux kernel hackers to worry about memory barriers. This is especially good news given the large number of CPUs and systems that Linux supports and the resulting wide variety of memory-consistency models. However, there are times when knowing about memory barriers can be helpful, and I hope that this article has served as a good introduction to them.



### Acknowledgements

I owe thanks to many CPU architects for patiently explaining the instruction- and memory-reordering features of their CPUs, particularly Wayne Cardoza, Ed Silha, Anton Blanchard, Tim Slegel, Juergen Probst, Ingo Adlung and Ravi Arimilli. Wayne deserves special thanks for his patience in explaining Alpha's reordering of dependent loads, a lesson that I resisted learning quite strenuously!

### Legal Statement

This work represents the view of the author and does not necessarily represent the view of IBM. IBM, zSeries and Power PC are trademarks or registered trademarks of International Business Machines Corporation in the United States, other countries, or both. Linux is a registered trademark of Linus Torvalds. i386 is a trademarks of Intel Corporation or its subsidiaries in the United States, other countries, or both. Other company, product, and service names may be trademarks or service marks of such companies. Copyright (c) 2005 by IBM Corporation.

**Resources for this article:** [/article/8331 \(/article/8331\)](#).

Paul E. McKenney is a Distinguished Engineer with IBM's Linux Technology Center. He has worked on NUMA and SMP algorithms and, in particular, RCU for longer than he cares to admit. In his spare time, he jogs and supports the usual house-wife-and-kids habit.

## Comments

### Comment viewing options

Threaded list - expanded ▼ Date - newest first ▼ 50 comments per page ▼ Save settings

Select your preferred way to display the comments and click "Save settings" to activate your changes.

#### **First article I've seen on** (</article/8211#comment-355160>)

Submitted by RossC (not verified) on Sun, 08/22/2010 - 16:31.

First article I've seen on CPU reordering that explained \*why\* it happens. Great stuff.



#### **Interesting post for anyone reading this article** (</article/8211#comment-347240>)

Submitted by [Sudhanshu \(http://www.42klines.com\)](http://www.42klines.com) (not verified) on Wed, 01/06/2010 - 18:27.

<http://timetoblead.com/mysql-doesnt-always-suck-this-time-its-amd/> (<http://timetoblead.com/mysql-doesnt-always-suck-this-time-its-amd/>)



#### **More details** (</article/8211#comment-347073>)

Submitted by [Sudhanshu \(http://www.42klines.com\)](http://www.42klines.com) (not verified) on Fri, 01/01/2010 - 15:29.

<http://www.rdrop.com/users/paulmck/scalability/paper/whymb.2009.04.05a.pdf>  
(<http://www.rdrop.com/users/paulmck/scalability/paper/whymb.2009.04.05a.pdf>)

Great article. Thanks Paul.



#### **Thank you, Sudhanshu!** (</article/8211#comment-347074>)

Submitted by [Paul E. McKenney \(http://www.rdrop.com/users/paulmck/\)](http://www.rdrop.com/users/paulmck/) (not verified) on Fri, 01/01/2010 - 18:11.

The updated table of memory-ordering constraints is shown on Page 16 of the above URL.

