

SARAVANAN D.

GENERALIST SOFTWARE ENGINEER

github.com/saravan2/ · ORCID iD : 0009-0007-7285-1946

Palo Alto, CA · saravananinca@gmail.com · https://saravanand.com · https://linkedin.com/in/saravananinca/

CORE COMPETENCIES

Kernel, Distributed Systems, Networking

Programming: C, C++, Python, Rust

OS: GNU Linux, FreeBSD, VxWorks

Databases: MySQL, MongoDB, SQLite

Hypervisors: XenServer, KVM

VMM: Cloud-Hypervisor, QEMU

Debugging: Drgn, BPF, GDB

EDUCATION

BACHELOR OF ENGINEERING

Computer Science, May 2007

Sathyabama University, Chennai, India

PUBLICATIONS

- **IOCost: Block Input-Output Control for Containers in Datacenters |**
<https://doi.org/10.1109/MM.2023.3277783>
(IEEE Micro Top Picks 2023)
- **IOCost: block IO control for containers in datacenters |**
<https://doi.org/10.1145/3503222.3507727>
(ASPLOS 2022)
- **CASH: A Credit Aware Scheduling for Public Cloud Platforms |**
<https://doi.org/10.1109/CCGrid51090.2021.00032>
(CCGRID 2021)

CERTIFICATIONS

Foundations in Computer Science
Graduate Certificate, Aug 2014
Stanford University

Advanced FreeBSD Kernel Internals
May 2016

Systems Software
Graduate Certificate, March 2019
Stanford University

PROFESSIONAL EXPERIENCE

CRUSOE AI

Dec 15, 2025 – Present

Sunnyvale, CA

Staff Software Engineer

- Open Source Projects: Cloud-Hypervisor, Linux Kernel
- Developed virtualization capabilities in Cloud-Hypervisor VMM
- Engineered Linux Kernel and observability stack for Crusoe Cloud

ORACLE

Jul 1, 2024 – Dec 2, 2025

Redwood Shores, CA

Principal Software Engineer

- Open Source Projects: SPDK
- Developed ultra-low latency, high throughput software-defined storage and journal services.
- Designed, built and maintained metrics library for high performance services in OCI

META

Nov 11, 2019 – Dec 27, 2023

Menlo Park, CA

Production Engineer, Kernel

- Open Source Projects: Linux Kernel, resctl-demo, chef
- Derived sequential, random r/w cost associated with SSD models and tuned cgroup controllers to improve I/O isolation
- Stacked [12:1] Control Plane Coordination and Storage service workloads of different sizes
- Improved kdump, post-mortem and debugging capabilities of all server platforms deployed at datacenters and edge
- Worked on GRUB boot manager, EFI bootloader, BPF to accurately measure block device latencies
- Maintained kernel build infrastructure, testing pool and framework to validate latest Linux kernels

CITRIX SYSTEMS, INC

May 15, 2011 – Nov 8, 2019

Santa Clara, CA

Principal Software Engineer

- Worked on Load Balancers, Application Delivery Controllers
- Developed device drivers and kernel customizations for BSD-like proprietary Operating System.
- Server Platform development activities involving integration of drivers, libraries, firmware, tools and hardware validation.
- Developed supervisory infrastructure to monitor health and extract vital system stats through IPMI, actively participated in FIPS 140 Level 1, 2, 3 Citrix ADC projects.

ALCATEL-LUCENT (Contractor)

January 18, 2010 – April 29, 2011

WIPRO TECHNOLOGIES

August 27, 2007 – April 29, 2011

Project Engineer (Contractor) Calabasas, CA

Project Engineer, *Promoted in January 2010* Chennai, India

- Developed Software for high performance switches and routers.
- Worked extensively on Real Time (VxWorks) Kernel issues
- Developed Microprocessor (MPC 8248) profiling utility, i2c driver
- **Prodigy of the Year 2008 Award**