## BHAGWAN PARSHURAM INSTITUTE OF TECHNOLOGY

## ASSIGNMENT (IN LIEU OF 2ND MID TERM EXAMINATION)

## **B.TECH PROGRAMMES (UNDER THE AEGIS OF USICT)**

3<sup>rd</sup> Semester, November, 2024

Paper Code: ECC 207 Subject: DLCD

Time: 1½ Hrs. Max. Marks: 30

Note: Attempt Q. No. 1 which is compulsory and any two more questions from remaining.

Q. N	Question	Max. Marks	CO(s)
1	Attempt the following:		
(:	a) What is virtual memory?	2	CO4
()	b) What is assembly language?	2	CO3
(	e) What is DMA?	2	CO4
(	d) What is RTL?	2	CO3
(	e) What is Cache memory?	2	CO4
2	Explain Booth's algorithm in computer architecture with suitable example.	10	CO4
3 a	Differentiate between hardwired control and micro programmed control.	5	CO3
b	Differentiate between RISC and CISC computers with their features.	5	CO3
4 a	What is addressing mode? Explain different types of addressing modes.	5	CO3
b	Explain different modes of data transfer in computer systems.	5	CO4