

SIVARAMA SARMA VALLABHAJYOSYULA

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CAREER OBJECTIVE:

Aspiring to utilize a strong foundation in electrical and electronics engineering, along with software development skills, to drive technological innovation and gain practical experience in a dynamic tech environment.

EDUCATION QUALIFICATIONS:

COURSE	INSTITUTE	BOARD/UNIVERSITY	BATCH	CGPA
B. Tech(EEE)	Andhra Loyola Institute of Engineering & Technology	JNTU	2021-2025	6.84
INTERMEDIATE (MPC)	Sri Chaitanya Jr college	Board of Intermediate Education	2018-2020	8.09
10th	Swarna Bharathi High School	Board of Secondary Education	2017-2018	9.8

PROJECTS:

Title: Automatic Power Factor Correction (APFC) Using Capacitor Banks

Aim: This project automatically improves the power factor of electrical systems. Capacitor banks are switched based on load conditions to compensate reactive power. It reduces power losses and improves overall system efficiency.

Duration: 2-3 Weeks

Title: Automatic Street Light Control Using LDR

Aim: This project automatically turns street lights ON and OFF using an LDR sensor. Lights switch ON at night and OFF during daylight without manual control. It saves electrical energy and reduces human effort.

Duration: 3-4 Weeks

Title: Smart Grid Based Power Monitoring and Fault Detection System

Aim: This project monitors voltage, current, and power in a smart grid system. It detects faults like overload and short circuit in real time. The system improves power reliability, safety, and efficient energy management.

Duration: 11-12 Weeks

SKILLS:

TECHNICAL SKILLS	SOFT SKILLS
<ul style="list-style-type: none">• Programming languages: C, C++, Java• Familiar Tools: Matlab, Tinkercad	<ul style="list-style-type: none">• Time Management• Problem-solving skills• Active listener• Positive thinker and Self motivated• Good planning skills

INTERSHIPS/EXPERIENCE:

Technical Internship: AutoCAD

Duration: 15-16 weeks

Social Internship: Smart Village Revolution(SVR)

Duration: 2-3 weeks

CERTIFICATIONS:

AutoCAD - SkillDzire

Matlab Onramp - Matlab

Simulink Onramp - Matlab

PERSONAL DETAILS:

Date of Birth : 21-09-2003

Name : Sivarama Sarma Vallabhajyosyula

Father's Name : Seetharama Pavan Kumar Vallabhajyosyula

Mother's Name : Gayathri Vallabhajyosyula

Nationality : Indian

Hobbies : Painting, Dance, Cooking, Badminton

Languages Known : Telugu, English, Hindi

Place: Vijayawada

Date:

Sivarama Sarma Vallabhajyosyula