



**Name: Khizar Ali**

**Roll No: 22P-9269**

**Subject: COAL\_Lab**

**Submitted to: Usman Abbasi**

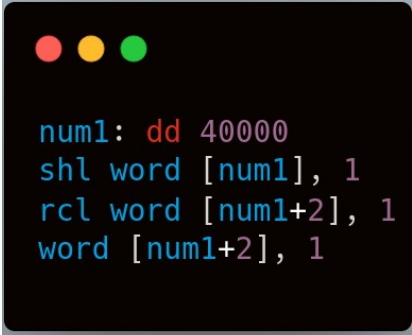
## Problem Statement:

When multiplying numbers, especially larger ones that exceed the register size (typically 16 or 32 bits), conventional shifting operations like SHL and SHR may not be sufficient. These operations can only manipulate bits within the register size, leading to loss of significant bits when dealing with larger numbers. For instance, when multiplying two 16-bit numbers, the result can be up to 32 bits long, but if we're limited to 16-bit registers, we face challenges storing and processing the partial products without encountering overflow issues.

## Solution:

To address the memory limitation, we employ extended shifting. This technique hinges on two primary instructions: SHL (Shift Left) and RCL (Rotate Carry Left). By utilizing this method, we can shift a 32-bit number left by 16 bits, thereby safeguarding against the loss of significant bits.

## For Example:

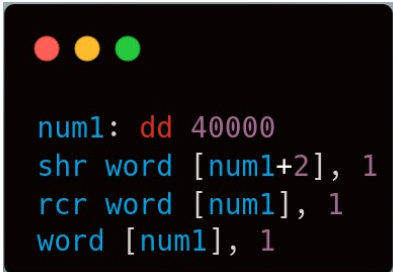


```
num1: dd 40000
shl word [num1], 1
rcl word [num1+2], 1
word [num1+2], 1
```

## Explanation:

In this example, **num1** represents a **32-bit** number stored in memory. The **SHL** instruction performs a left shift operation on the lower 16 bits of **num1**, causing the most significant bit to be shifted out and stored in the carry flag. Subsequently, the **RCL** instruction rotates the carry flag into the least significant bit of the next word, thereby effectively combining the two 16-bit words.

For shifting right, the process is reversed. The SHR (Shift Right) and RCR instructions are used to ensure that no valuable bit is lost.



```
num1: dd 40000
shr word [num1+2], 1
rcr word [num1], 1
word [num1], 1
```

## Code:

```
[org 0x0100]
jmp start
multiplicand: dd 1500 ; 16bit multiplicand 32bit space
multiplier: dw 300 ; 16bit multiplier
result: dd 0 ; 32bit result
start: mov cl, 16 ; initialize bit count to 16
mov dx, [multiplier] ; load multiplier in dx
checkbit: shr dx, 1 ; move right most bit in carry
jnc skip ; skip addition if bit is zero
mov ax, [multiplicand]
add [result], ax ; add less significant word
mov ax, [multiplicand+2]
adc [result+2], ax ; add more significant word
skip: shl word [multiplicand], 1
rcl word [multiplicand+2], 1 ; shift multiplicand left
dec cl ; decrement bit count
jnz checkbit ; repeat if bits left
mov ax, 0x4c00
```

## Explanation of the code:

1. **[org 0x0100]:** This directive sets the origin of the program to memory address 0x0100.
2. **jmp start:** This instruction jumps to the **start label** to begin execution of the program.
3. **multiplicand: dd 1500:** This line declares a double word (**32 bits**) variable named **multiplicand** and initializes it with the value **1500** (in decimal). It allocates **32 bits** of memory for the multiplicand.
4. **multiplier: dw 300:** This line declares a word (**16 bits**) variable named multiplier and initializes it with the value **300 (in decimal)**. It allocates **16 bits** of memory for the multiplier.
5. **result: dd 0:** This line declares a double word (**32 bits**) variable named **result** and initializes it with the **value 0**. It allocates **32 bits** of memory for the result.

6. **start: mov cl, 16:** This instruction moves the value **16** into the **CL** register, initializing the bit count to **16**.

7. **mov dx, [multiplier]:** This instruction loads the value stored in the **multiplier** variable into the **DX** register.

8. **checkbit: shr dx, 1:** This instruction performs a **bitwise** shift right operation on the DX register, effectively shifting all bits to the right by one position. **The rightmost bit is moved into the carry flag.**

9. **jnc skip:** This instruction **jumps** to the **skip** label if the carry flag is not **set** (jumps if no carry).

10. **mov ax, [multiplicand]:** This instruction loads the **lower** 16 bits of the **multiplicand** variable into the **AX** register.

11. **add [result], ax:** This instruction adds the value in the **AX** register to the **memory location** pointed to by the result variable, effectively adding the less **significant word** of the **multiplicand** to the result.

12. **mov ax, [multiplicand+2]:** This instruction loads the **upper 16 bits** of the **multiplicand** variable into the **AX** register.

13. **adc [result+2], ax:** This instruction adds the value in the **AX** register to the memory location **two bytes after result**, taking into **account** the carry flag from the **previous addition**.

14. **skip: shl word [multiplicand], 1:** This instruction performs a **bitwise** shift **left operation** on the lower **16 bits of the multiplicand variable**, effectively shifting it left by one position.

15. **rcl word [multiplicand+2], 1:** This instruction **rotates** the **carry flag** into the least significant bit of the **upper 16 bits** of the **multiplicand** variable, effectively shifting it left by **one position** and **preserving** the **carry** from the previous operation.


16. **dec cl:** This instruction **decrements** the value in the **CL** register.

17. **jnz checkbit:** This instruction jumps back to the **checkbit** label if the zero flag is not set (jumps if the bit count is not zero).

18. **mov ax, 0x4c00**: This instruction moves the value **0x4c00** into the AX register, which is typically used to signal the end of a program in DOS.

### Step by step execution of the code on DOS:

#### Step 1:





AX	0000	SI	0000	CS	19F5	IP	010F
BX	0000	DI	0000	DS	19F5		
CX	0010	BP	0000	ES	19F5	HS	19F5
DX	0000	SP	FFFE	SS	19F5	FS	19F5

CMD >			
010D	B110	MOV	CL,10
010F	8B160701	MOV	DX,[0107]
0113	D1EA	SHR	DX,1

Move the value **16** in **CX** register (10) in HEX

#### Step 2:



AX	0000	SI	0000	CS	19F5	IP	0113	Stack	+0 0000	Flags	7200
BX	0000	DI	0000	DS	19F5				+2 20CD		
CX	0010	BP	0000	ES	19F5	HS	19F5		+4 9FFF	OF	DF
DX	012C	SP	FFFE	SS	19F5	FS	19F5		+6 EA00	0	0

CMD >				1	7	8	9	A	B	C	D	E
010F	8B160701	MOV	DX,[0107]	DS:010	2C	01	00	00	00	00	B1	10
0113	D1EA	SHR	DX,1	DS:011	BB	16	07	01	D1	EA	73	0E
0115	730E	JNC	0125	DS:011F	A1	03	01	01	06	09	01	A1
0117	A10301	MOV	AX,[0103]	DS:0127	05	01	11	06	0B	01	D1	26
011A	01060901	ADD	[0109],AX	DS:012F	03	01	D1	16	05	01	FE	C9
011E	A10501	MOV	AX,[0105]	DS:012F	75	E2	B8	00	4C	85	D2	75
0121	11060B01	ADC	[010B],AX	DS:0137	04	85	C0	74	1C	C7	46	DC
0125	D1260301	SHL	W/[0103],1	DS:013F	00	00	8E	5E	FC	83	7D	0E
0129	D1160501	RCL	W/[0105],1	DS:0147	00	74	09	8B	46	F2	48	3B
				DS:014F	46	F6	7E	08	B8	01	00	EB

2	0	1	2	3	4	5	6	7	8	9	A	B	C	D	E	F
DS:0000	CD	20	FF	9F	00	EA	F0	FE	AD	DE	1B	05	C5	06	00	00
DS:0010	18	01	10	01	18	01	92	01	01	01	00	02	FF	FF	FF	FF
DS:0020	FF	FF	FF	FF	FF	FF	FF	FF	FF	FF	FF	EB	19	C0	11	
DS:0030	A2	01	14	00	18	00	F5	19	FF	FF	FF	FF	00	00	00	00
DS:0040	05	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00

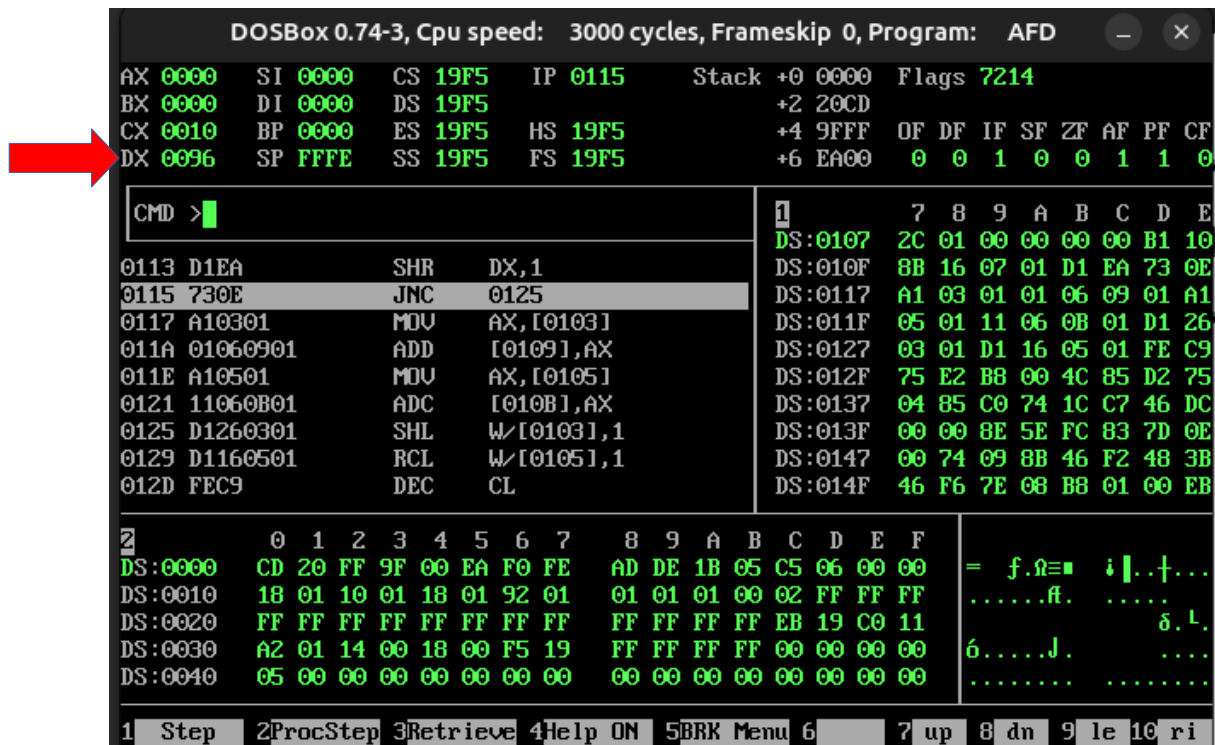
  

1	Step	2	ProcStep	3	Retrieve	4	Help ON	5	BRK Menu	6		7	up	8	dn	9	le	10	ri
---	------	---	----------	---	----------	---	---------	---	----------	---	--	---	----	---	----	---	----	----	----

Moved the value of **multiplier (300)** 012C in HEX in the register DX

### Step 3:

loop starts iteration :1



DOSBox 0.74-3, Cpu speed: 3000 cycles, Frameskip 0, Program: AFD

Register	Value	Register	Value	Register	Value	Register	Value	Stack	Flags
AX	0000	SI	0000	CS	19F5	IP	0115	+0 0000	7214
BX	0000	DI	0000	DS	19F5			+2 20CD	
CX	0010	BP	0000	ES	19F5	HS	19F5	+4 9FFF	OF DF IF SF ZF AF PF CF
DX	0096	SP	FFFE	SS	19F5	FS	19F5	+6 EA00	0 0 1 0 0 1 1 0

CMD > |

0113 D1EA SHR DX,1  
0115 730E JNC 0125  
0117 A10301 MOV AX,[0103]  
011A 01060901 ADD [0109],AX  
011E A10501 MOV AX,[0105]  
0121 11060B01 ADC [010B],AX  
0125 D1260301 SHL W/[0103],1  
0129 D1160501 RCL W/[0105],1  
012D FEC9 DEC CL

1 7 8 9 A B C D E  
DS:0107 2C 01 00 00 00 00 B1 10  
DS:010F 8B 16 07 01 D1 EA 73 0E  
DS:0117 A1 03 01 01 06 09 01 A1  
DS:011F 05 01 11 06 0B 01 D1 26  
DS:0127 03 01 D1 16 05 01 FE C9  
DS:012F 75 E2 B8 00 4C 85 D2 75  
DS:0137 04 85 C0 74 1C C7 46 DC  
DS:013F 00 00 8E 5E FC 83 7D 0E  
DS:0147 00 74 09 8B 46 F2 48 3B  
DS:014F 46 F6 7E 08 B8 01 00 EB

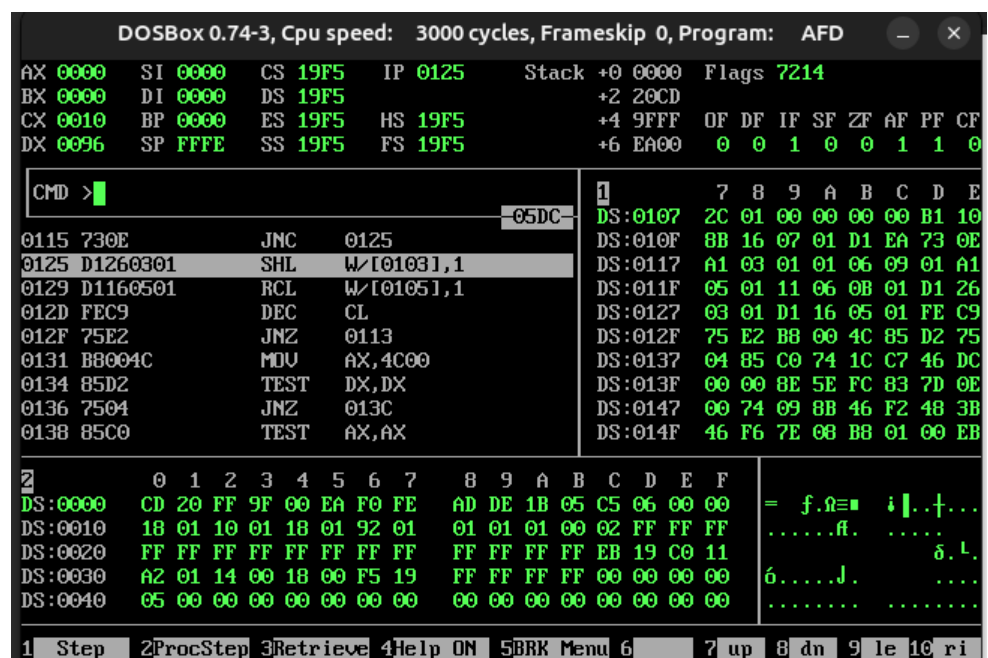
2 0 1 2 3 4 5 6 7 8 9 A B C D E F  
DS:0000 CD 20 FF 9F 00 EA F0 FE AD DE 1B 05 C5 06 00 00 = f.Ω≡ i|..+...  
DS:0010 18 01 10 01 18 01 92 01 01 01 01 00 02 FF FF FF .....f. ....  
DS:0020 FF FF FF FF FF FF FF FF FF FF FF FF EB 19 C0 11 .....δ..L.  
DS:0030 A2 01 14 00 18 00 F5 19 FF FF FF FF 00 00 00 00 6.....J. ....  
DS:0040 05 00 00 00 00 00 00 00 00 00 00 00 00 00 00 ..... .....

1 Step 2ProcStep 3Retrieve 4Help ON 5BRK Menu 6 7 up 8 dn 9 le 10 ri

Shifted the multiplier right by one bit and moved the rightmost bit to the carry as the Value of **DX** changed to **0096**

jnc skip ; skip the addition

As carry flag is not set we are skipping the addition.



DOSBox 0.74-3, Cpu speed: 3000 cycles, Frameskip 0, Program: AFD

Register	Value	Register	Value	Register	Value	Register	Value	Stack	Flags
AX	0000	SI	0000	CS	19F5	IP	0125	+0 0000	7214
BX	0000	DI	0000	DS	19F5			+2 20CD	
CX	0010	BP	0000	ES	19F5	HS	19F5	+4 9FFF	OF DF IF SF ZF AF PF CF
DX	0096	SP	FFFE	SS	19F5	FS	19F5	+6 EA00	0 0 1 0 0 1 1 0

CMD > |

0115 730E JNC 0125  
0125 D1260301 SHL W/[0103],1  
0129 D1160501 RCL W/[0105],1  
012D FEC9 DEC CL  
012F 75E2 JNZ 0113  
0131 B8004C MOV AX,4C00  
0134 85D2 TEST DX,DX  
0136 7504 JNZ 013C  
0138 85C0 TEST AX,AX

1 7 8 9 A B C D E  
DS:0107 2C 01 00 00 00 00 B1 10  
DS:010F 8B 16 07 01 D1 EA 73 0E  
DS:0117 A1 03 01 01 06 09 01 A1  
DS:011F 05 01 11 06 0B 01 D1 26  
DS:0127 03 01 D1 16 05 01 FE C9  
DS:012F 75 E2 B8 00 4C 85 D2 75  
DS:0137 04 85 C0 74 1C C7 46 DC  
DS:013F 00 00 8E 5E FC 83 7D 0E  
DS:0147 00 74 09 8B 46 F2 48 3B  
DS:014F 46 F6 7E 08 B8 01 00 EB

2 0 1 2 3 4 5 6 7 8 9 A B C D E F  
DS:0000 CD 20 FF 9F 00 EA F0 FE AD DE 1B 05 C5 06 00 00 = f.Ω≡ i|..+...  
DS:0010 18 01 10 01 18 01 92 01 01 01 01 00 02 FF FF FF .....f. ....  
DS:0020 FF FF FF FF FF FF FF FF FF FF FF FF EB 19 C0 11 .....δ..L.  
DS:0030 A2 01 14 00 18 00 F5 19 FF FF FF FF 00 00 00 00 6.....J. ....  
DS:0040 05 00 00 00 00 00 00 00 00 00 00 00 00 00 00 ..... .....

1 Step 2ProcStep 3Retrieve 4Help ON 5BRK Menu 6 7 up 8 dn 9 le 10 ri

```

DOSBox 0.74-3, Cpu speed: 3000 cycles, Frameskip 0, Program: AFD
AX 0000 SI 0000 CS 19F5 IP 0129 Stack +0 0000 Flags 7214
BX 0000 DI 0000 DS 19F5 +2 20CD
CX 0010 BP 0000 ES 19F5 HS 19F5 +4 9FFF OF DF IF SF ZF AF PF CF
DX 0096 SP FFFE SS 19F5 FS 19F5 +6 EA00 0 0 1 0 0 1 1 0

CMD >

0125 D1260301 SHL W[0103],1
0129 D1160501 RCL W[0105],1
012D FEC9 DEC CL
012F 75E2 JNZ 0113
0131 B8004C MOV AX,4C00
0134 85D2 TEST DX,DX
0136 7504 JNZ 013C
0138 85C0 TEST AX,AX
013A 741C JZ 0158

DS:0100 01 00 00 00 00 B1 10 8B
DS:0108 16 07 01 D1 EA 73 0E A1
DS:0110 03 01 01 06 09 01 A1 05
DS:0118 01 11 06 0B 01 D1 26 03
DS:0120 01 D1 16 05 01 FE C9 75
DS:0128 E2 B8 00 4C 85 D2 75 04
DS:0130 85 C0 74 1C C7 46 DC 00
DS:0138 00 8E 5E FC 83 7D 0E 00
DS:0140 74 09 8B 46 F2 48 3B 46

DS:0000 CD 20 FF 9F 00 EA F0 FE AD DE 1B 05 C5 06 00 00 = f.Ω= i|.+.
DS:0010 18 01 10 01 18 01 92 01 01 01 01 00 02 FF FF FF .....f.
DS:0020 FF FF FF FF FF FF FF FF FF FF FF FF EB 19 C0 11 .....δ.
DS:0030 A2 01 14 00 18 00 F5 19 FF FF FF FF 00 00 00 00 00 6.....J.
DS:0040 05 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 .....

```

**skip: shl word [multiplicand], 1**

This instruction shifted the bits in the lower 16 bits of multiplicand to the left by one position. The leftmost bit is moved to the carry flag, and the rightmost bit is filled with a 0.

```

DOSBox 0.74-3, Cpu speed: 3000 cycles, Frameskip 0, Program: AFD
AX 0000 SI 0000 CS 19F5 IP 012D Stack +0 0000 Flags 7214
BX 0000 DI 0000 DS 19F5 +2 20CD
CX 0010 BP 0000 ES 19F5 HS 19F5 +4 9FFF OF DF IF SF ZF AF PF CF
DX 0096 SP FFFE SS 19F5 FS 19F5 +6 EA00 0 0 1 0 0 1 1 0

CMD >

0129 D1160501 RCL W[0105],1
012D FEC9 DEC CL
012F 75E2 JNZ 0113
0131 B8004C MOV AX,4C00
0134 85D2 TEST DX,DX
0136 7504 JNZ 013C
0138 85C0 TEST AX,AX
013A 741C JZ 0158
013C C746DC0000 MOV [BP-24],0000

DS:0100 E9 0A 00 B8 0B 00 00 2C
DS:0108 01 00 00 00 00 B1 10 8B
DS:0110 16 07 01 D1 EA 73 0E A1
DS:0118 03 01 01 06 09 01 A1 05
DS:0120 01 11 06 0B 01 D1 26 03
DS:0128 01 D1 16 05 01 FE C9 75
DS:0130 E2 B8 00 4C 85 D2 75 04
DS:0138 85 C0 74 1C C7 46 DC 00
DS:0140 00 8E 5E FC 83 7D 0E 00
DS:0148 74 09 8B 46 F2 48 3B 46

DS:0000 CD 20 FF 9F 00 EA F0 FE AD DE 1B 05 C5 06 00 00 = f.Ω= i|.+.
DS:0010 18 01 10 01 18 01 92 01 01 01 01 00 02 FF FF FF .....f.
DS:0020 FF FF FF FF FF FF FF FF FF FF FF FF EB 19 C0 11 .....δ.
DS:0030 A2 01 14 00 18 00 F5 19 FF FF FF FF 00 00 00 00 00 6.....J.
DS:0040 05 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 .....

```

This instruction rotated the carry left into the least significant bit of the upper 16 bits of multiplicand. Since the carry flag was not set by the previous shift operation,it remain same



```

DOSBox 0.74-3, Cpu speed: 3000 cycles, Frameskip 0, Program: AFD
AX 0000 SI 0000 CS 19F5 IP 0113 Stack +0 0000 Flags 7214
BX 0000 DI 0000 DS 19F5 +2 20CD
CX 000F BP 0000 ES 19F5 HS 19F5 +4 9FFF OF DF IF SF ZF AF PF CF
DX 0096 SP FFFE SS 19F5 FS 19F5 +6 EA00 0 0 1 0 0 1 1 0

CMD >
012F 75E2 JNZ 0113
0113 D1EA SHR DX,1
0115 730E JNC 0125
0117 A10301 MOV AX,[0103]
011A 01060901 ADD [0109],AX
011E A10501 MOV AX,[0105]
0121 11060B01 ADC [010B],AX
0125 D1260301 SHL W/[0103],1
0129 D1160501 RCL W/[0105],1

DS:0100 E9 0A 00 B8 0B 00 00 2C
DS:0108 01 00 00 00 00 B1 10 8B
DS:0110 16 07 01 D1 EA 73 0E A1
DS:0118 03 01 01 06 09 01 A1 05
DS:0120 01 11 06 0B 01 D1 26 03
DS:0128 01 D1 16 05 01 FE C9 75
DS:0130 E2 B8 00 4C 85 D2 75 04
DS:0138 85 C0 74 1C C7 46 DC 00
DS:0140 00 8E 5E FC 83 7D 0E 00
DS:0148 74 09 8B 46 F2 48 3B 46

2 0 1 2 3 4 5 6 7 8 9 A B C D E F
DS:0000 CD 20 FF 9F 00 EA F0 FE AD DE 1B 05 C5 06 00 00 = f.Ω= i|..+...
DS:0010 18 01 10 01 18 01 92 01 01 01 01 00 02 FF FF FF .....f. ....
DS:0020 FF FF FF FF FF FF FF FF FF FF FF FF EB 19 C0 11 .....δ.L.
DS:0030 A2 01 14 00 18 00 F5 19 FF FF FF FF 00 00 00 00 6.....J. ....
DS:0040 05 00 00 00 00 00 00 00 00 00 00 00 00 00 00 .....

1 Step 2ProcStep 3Retrieve 4Help ON 5BRK Menu 6 7 up 8 dn 9 le 10 ri

```

AS CX was not zero jumped back to the checkbit labels

**Iteration 2:**

```

DOSBox 0.74-3, Cpu speed: 3000 cycles, Frameskip 0, Program: AFD
AX 0000 SI 0000 CS 19F5 IP 0117 Stack +0 0000 Flags 7211
BX 0000 DI 0000 DS 19F5 +2 20CD
CX 000E BP 0000 ES 19F5 HS 19F5 +4 9FFF OF DF IF SF ZF AF PF CF
DX 0025 SP FFFE SS 19F5 FS 19F5 +6 EA00 0 0 1 0 0 1 0 1

CMD >
0115 730E JNC 0125
0117 A10301 MOV AX,[0103]
011A 01060901 ADD [0109],AX
011E A10501 MOV AX,[0105]
0121 11060B01 ADC [010B],AX
0125 D1260301 SHL W/[0103],1
0129 D1160501 RCL W/[0105],1
012D FEC9 DEC CL
012F 75E2 JNZ 0113

DS:0100 E9 0A 00 70 17 00 00 2C
DS:0108 01 00 00 00 00 B1 10 8B
DS:0110 16 07 01 D1 EA 73 0E A1
DS:0118 03 01 01 06 09 01 A1 05
DS:0120 01 11 06 0B 01 D1 26 03
DS:0128 01 D1 16 05 01 FE C9 75
DS:0130 E2 B8 00 4C 85 D2 75 04
DS:0138 85 C0 74 1C C7 46 DC 00
DS:0140 00 8E 5E FC 83 7D 0E 00
DS:0148 74 09 8B 46 F2 48 3B 46

2 0 1 2 3 4 5 6 7 8 9 A B C D E F
DS:0000 CD 20 FF 9F 00 EA F0 FE AD DE 1B 05 C5 06 00 00 = f.Ω= i|..+...
DS:0010 18 01 10 01 18 01 92 01 01 01 01 00 02 FF FF FF .....f. ....
DS:0020 FF FF FF FF FF FF FF FF FF FF FF FF EB 19 C0 11 .....δ.L.
DS:0030 A2 01 14 00 18 00 F5 19 FF FF FF FF 00 00 00 00 6.....J. ....
DS:0040 05 00 00 00 00 00 00 00 00 00 00 00 00 00 00 .....

1 Step 2ProcStep 3Retrieve 4Help ON 5BRK Menu 6 7 up 8 dn 9 le 10 ri

```



```

DOSBox 0.74-3, Cpu speed: 3000 cycles, Frameskip 0, Program: AFD
AX 0000 SI 0000 CS 19F5 IP 0117 Stack +0 0000 Flags 7211
BX 0000 DI 0000 DS 19F5 +2 20CD
CX 000E BP 0000 ES 19F5 HS 19F5 +4 9FFF OF DF IF SF ZF AF PF CF
DX 0025 SP FFFE SS 19F5 FS 19F5 +6 EA00 0 0 1 0 1

CMD >
0115 730E JNC 0125
0117 A10301 MOV AX,[0103]
011A 01060901 ADD [0109],AX
011E A10501 MOV AX,[0105]
0121 11060B01 ADC [010B],AX
0125 D1260301 SHL W,[0103],1
0129 D1160501 RCL W,[0105],1
012D FEC9 DEC CL
012F 75E2 JNZ 0113

1 0 1 2 3 4 5 6 7
DS:0100 E9 0A 00 70 17 00 00 2C
DS:0108 01 00 00 00 00 B1 10 8B
DS:0110 16 07 01 D1 EA 73 0E A1
DS:0118 03 01 01 06 09 01 A1 05
DS:0120 01 11 06 0B 01 D1 26 03
DS:0128 01 D1 16 05 01 FE C9 75
DS:0130 E2 B8 00 4C 85 D2 75 04
DS:0138 85 C0 74 1C C7 46 DC 00
DS:0140 00 8E 5E FC 83 7D 0E 00
DS:0148 74 09 8B 46 F2 48 3B 46

2 0 1 2 3 4 5 6 7 8 9 A B C D E F
DS:0000 CD 20 FF 9F 00 EA F0 FE AD DE 1B 05 C5 06 00 00
DS:0010 18 01 10 01 18 01 92 01 01 01 01 00 02 FF FF FF
DS:0020 FF FF FF FF FF FF FF FF FF FF FF FF EB 19 C0 11
DS:0030 A2 01 14 00 18 00 F5 19 FF FF FF FF 00 00 00 00
DS:0040 05 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00

1 Step 2ProcStep 3Retrieve 4Help ON 5BRK Menu 6 7 up 8 dn 9 le 10 ri

```

After shift right carry flag is set.

```

DOSBox 0.74-3, Cpu speed: 3000 cycles, Frameskip 0, Program: AFD
AX 1770 SI 0000 CS 19F5 IP 011A Stack +0 0000 Flags 7211
BX 0000 DI 0000 DS 19F5 +2 20CD
CX 000E BP 0000 ES 19F5 HS 19F5 +4 9FFF OF DF IF SF ZF AF PF CF
DX 0025 SP FFFE SS 19F5 FS 19F5 +6 EA00 0 0 1 0 0 1 0 1

CMD >
0117 A10301 MOV AX,[0103]
011A 01060901 ADD [0109],AX
011E A10501 MOV AX,[0105]
0121 11060B01 ADC [010B],AX

1 0 1 2 3 4 5 6 7
DS:0100 E9 0A 00 70 17 00 00 2C
DS:0108 01 00 00 00 00 B1 10 8B
DS:0110 16 07 01 D1 EA 73 0E A1
DS:0118 03 01 01 06 09 01 A1 05
DS:0120 01 11 06 0B 01 D1 26 03

```

Moved the the value of multiplicand to AX

```

DOSBox 0.74-3, Cpu speed: 3000 cycles, Frameskip 0, Program: AFD
AX 1770 SI 0000 CS 19F5 IP 011E Stack +0 0000 Flags 7200
BX 0000 DI 0000 DS 19F5 +2 20CD
CX 000E BP 0000 ES 19F5 HS 19F5 +4 9FFF OF DF IF SF ZF AF PF CF
DX 0025 SP FFFE SS 19F5 FS 19F5 +6 EA00 0 0 1 0 0 0 0 0

CMD >
011A 01060901 ADD [0109],AX
011E A10501 MOV AX,[0105]
0121 11060B01 ADC [010B],AX
0125 D1260301 SHL W,[0103],1
0129 D1160501 RCL W,[0105],1
012D FEC9 DEC CL
012F 75E2 JNZ 0113

1 0 1 2 3 4 5 6 7
DS:0100 E9 0A 00 70 17 00 00 2C
DS:0108 01 70 17 00 00 B1 10 8B
DS:0110 16 07 01 D1 EA 73 0E A1
DS:0118 03 01 01 06 09 01 A1 05
DS:0120 01 11 06 0B 01 D1 26 03

```

Added the Value into the Ax register to the result

```

DOSBox 0.74-3, Cpu speed: 3000 cycles, Frameskip 0, Program: AFD
AX 0000 SI 0000 CS 19F5 IP 0121 Stack +0 0000 Flags 7200
BX 0000 DI 0000 DS 19F5      +2 20CD
CX 000E BP 0000 ES 19F5 HS 19F5  +4 9FFF OF DF IF SF ZF AF PF CF
DX 0025 SP FFFE SS 19F5 FS 19F5  +6 EA00 0 0 1 0 0 0 0 0

CMD > 0000
011E A10501 MOV AX,[0105]
0121 11060B01 ADC [010B],AX
0125 D1260301 SHL W/[0103],1
0129 D1160501 RCL W/[0105],1

```

moved upper 16 bits of the multiplicand variable into the AX register

```

DOSBox 0.74-3, Cpu speed: 3000 cycles, Frameskip 0, Program: AFD
AX 0000 SI 0000 CS 19F5 IP 0125 Stack +0 0000 Flags 7244
BX 0000 DI 0000 DS 19F5      +2 20CD
CX 000E BP 0000 ES 19F5 HS 19F5  +4 9FFF OF DF IF SF ZF AF PF CF
DX 0025 SP FFFE SS 19F5 FS 19F5  +6 EA00 0 0 1 0 1 0 1 0

CMD > -1770
0121 11060B01 ADC [010B],AX
0125 D1260301 SHL W/[0103],1
0129 D1160501 RCL W/[0105],1
012B FFC9 DEC CI

```

added more significant word.

### Iteration 3:

DOSBox 0.74-3, Cpu speed: 3000 cycles, Frameskip 0, Program: AFD															
AX 0000	SI 0000	CS 19F5	IP 012D	Stack +0 0000	Flags 7214										
BX 0000	DI 0000	DS 19F5		+2 20CD											
CX 000D	BP 0000	ES 19F5	HS 19F5	+4 9FFF	OF DF IF SF ZF AF PF CF										
DX 0012	SP FFFE	SS 19F5	FS 19F5	+6 EA00	0 0 1 0 0 1 1 0										
CMD >				1 0 1 2 3 4 5 6 7											
0129 D1160501 RCL W/[0105],1				DS:0100 E9 0A 00 C0 5D 00 00 20											
012D FEC9 DEC CL				DS:0108 01 50 46 00 00 B1 10 8F											
012F 75E2 JNZ 0113				DS:0110 16 07 01 D1 EA 73 0E A1											
0131 B8004C MOV AX,4C00				DS:0118 03 01 01 06 09 01 A1 05											
0134 85D2 TEST DX,DX				DS:0120 01 11 06 0B 01 D1 26 03											
0136 7504 JNZ 013C				DS:0128 01 D1 16 05 01 FE C9 75											
0138 85C0 TEST AX,AX				DS:0130 E2 B8 00 4C 85 D2 75 04											
013A 741C JZ 0158				DS:0138 85 C0 74 1C C7 46 DC 06											
013C C746DC0000 MOV [BP-24],0000				DS:0140 00 8E 5E FC 83 7D 0E 06											
				DS:0148 74 09 8B 46 F2 48 3B 46											
2 0 1 2 3 4 5 6 7 8 9 A B C D E F															
DS:0000 CD 20 FF 9F 00 EA F0 FE AD DE 1B 05 C5 06 00 00 = f.Ω≡ i  ..+...															
DS:0010 18 01 10 01 18 01 92 01 01 01 01 00 02 FF FF FF .....ff. ....															
DS:0020 FF FF FF FF FF FF FF FF FF FF FF FF EB 19 C0 11 δ.L.															
DS:0030 A2 01 14 00 18 00 F5 19 FF FF FF FF 00 00 00 00 6.....J. ....															
DS:0040 05 00 00 00 00 00 00 00 00 00 00 00 00 00 00 ..... .....															
1 Step 2ProcStep 3Retrieve 4Help ON 5BRK Menu 6 7 up 8 dn 9 le 10 ri															

### Iteration 4:

DOSBox 0.74-3, Cpu speed: 3000 cycles, Frameskip 0, Program: AFD															-		x	
AX	0000	SI	0000	CS	19F5	IP	012D	Stack	+0	0000	Flags	7290						
BX	0000	DI	0000	DS	19F5				+2	20CD								
CX	000C	BP	0000	ES	19F5	HS	19F5		+4	9FFF	OF	DF	IF	SF	ZF	AF	PF	CF
DX	0009	SP	FFFE	SS	19F5	FS	19F5		+6	EA00	0	0	1	1	0	1	0	0
CMD >										1 0 1 2 3 4 5 6 7								
0129 D1160501 RCL W/[0105],1										DS:0100 E9 0A 00 80 BB 00 00 20								
012D FEC9 DEC CL										DS:0108 01 50 46 00 00 B1 10 8F								
012F 75E2 JNZ 0113										DS:0110 16 07 01 D1 EA 73 0E A1								
0131 B8004C MOV AX,4C00										DS:0118 03 01 01 06 09 01 A1 05								
0134 85D2 TEST DX,DX										DS:0120 01 11 06 0B 01 D1 26 03								
0136 7504 JNZ 013C										DS:0128 01 D1 16 05 01 FE C9 75								
0138 85C0 TEST AX,AX										DS:0130 E2 B8 00 4C 85 D2 75 04								
013A 741C JZ 0158										DS:0138 85 C0 74 1C C7 46 DC 06								
013C C746DC0000 MOV [BP-24],0000										DS:0140 00 8E 5E FC 83 7D 0E 06								
										DS:0148 74 09 8B 46 F2 48 3B 46								
2 0 1 2 3 4 5 6 7 8 9 A B C D E F																		
DS:0000 CD 20 FF 9F 00 EA F0 FE AD DE 1B 05 C5 06 00 00										= f.Ω≡ i  ..+...								
DS:0010 18 01 10 01 18 01 92 01 01 01 01 00 02 FF FF FF										.....ff. ....								
DS:0020 FF FF FF FF FF FF FF FF FF FF FF FF EB 19 C0 11										δ.L.								
DS:0030 A2 01 14 00 18 00 F5 19 FF FF FF FF 00 00 00 00										6.....J. ....								
DS:0040 05 00 00 00 00 00 00 00 00 00 00 00 00 00 00										.....								
1 Step 2ProcStep 3Retrieve 4Help ON 5BRK Menu 6 7 up 8 dn 9 le 10 ri																		

## Iteration 5:

DOSBox 0.74-3, Cpu speed: 3000 cycles, Frameskip 0, Program: AFD

AX 0000	SI 0000	CS 19F5	IP 012D	Stack +0 0000	Flags 7214
BX 0000	DI 0000	DS 19F5		+2 20CD	
CX 000B	BP 0000	ES 19F5	HS 19F5	+4 9FFF	OF DF IF SF ZF AF PF CF
DX 0004	SP FFFE	SS 19F5	FS 19F5	+6 EA00	0 0 1 0 0 1 1 0

CMD >

0129 D1160501	RCL	W/[0105],1	DS:0100 E9 0A 00 00 77 01 00 20
012D FEC9	DEC	CL	DS:0108 01 D0 01 01 00 B1 10 8B
012F 75E2	JNZ	0113	DS:0110 16 07 01 D1 EA 73 0E A1
0131 B8004C	MOV	AX,4C00	DS:0118 03 01 01 06 09 01 A1 05
0134 85D2	TEST	DX,DX	DS:0120 01 11 06 0B 01 D1 26 03
0136 7504	JNZ	013C	DS:0128 01 D1 16 05 01 FE C9 75
0138 85C0	TEST	AX,AX	DS:0130 E2 B8 00 4C 85 D2 75 04
013A 741C	JZ	0158	DS:0138 85 C0 74 1C C7 46 DC 00
013C C746DC0000	MOV	[BP-24],0000	DS:0140 00 8E 5E FC 83 7D 0E 00
			DS:0148 74 09 8B 46 F2 48 3B 46

2	0	1	2	3	4	5	6	7	8	9	A	B	C	D	E	F	
DS:0000	CD	20	FF	9F	00	EA	F0	FE	AD	DE	1B	05	C5	06	00	00	= f.Ω≡ i ..†...
DS:0010	18	01	10	01	18	01	92	01	01	01	00	02	FF	FF	FF	FF	.....ff. ....
DS:0020	FF	FF	FF	FF	FF	FF	FF	FF	FF	FF	FF	EB	19	C0	11		δ.L
DS:0030	A2	01	14	00	18	00	F5	19	FF	FF	FF	FF	00	00	00	00	ó.....J. ....
DS:0040	05	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	.....

1 Step 2ProcStep 3Retrieve 4Help ON 5BRK Menu 6 7 up 8 dn 9 le 10 ri

## Iteration 6:

DOSBox 0.74-3, Cpu speed: 3000 cycles, Frameskip 0, Program: AFD

AX 0000	SI 0000	CS 19F5	IP 012D	Stack +0 0000	Flags 7294
BX 0000	DI 0000	DS 19F5		+2 20CD	
CX 000A	BP 0000	ES 19F5	HS 19F5	+4 9FFF	OF DF IF SF ZF AF PF CF
DX 0002	SP FFFE	SS 19F5	FS 19F5	+6 EA00	0 0 1 1 0 1 1 0

CMD >

0129 D1160501	RCL	W/[0105],1	DS:0100 E9 0A 00 00 EE 02 00 20
012D FEC9	DEC	CL	DS:0108 01 D0 01 01 00 B1 10 8B
012F 75E2	JNZ	0113	DS:0110 16 07 01 D1 EA 73 0E A1
0131 B8004C	MOV	AX,4C00	DS:0118 03 01 01 06 09 01 A1 05
0134 85D2	TEST	DX,DX	DS:0120 01 11 06 0B 01 D1 26 03
0136 7504	JNZ	013C	DS:0128 01 D1 16 05 01 FE C9 75
0138 85C0	TEST	AX,AX	DS:0130 E2 B8 00 4C 85 D2 75 04
013A 741C	JZ	0158	DS:0138 85 C0 74 1C C7 46 DC 00
013C C746DC0000	MOV	[BP-24],0000	DS:0140 00 8E 5E FC 83 7D 0E 00
			DS:0148 74 09 8B 46 F2 48 3B 46

2	0	1	2	3	4	5	6	7	8	9	A	B	C	D	E	F	
DS:0000	CD	20	FF	9F	00	EA	F0	FE	AD	DE	1B	05	C5	06	00	00	= f.Ω≡ i ..†...
DS:0010	18	01	10	01	18	01	92	01	01	01	00	02	FF	FF	FF	FF	.....ff. ....
DS:0020	FF	FF	FF	FF	FF	FF	FF	FF	FF	FF	FF	EB	19	C0	11		δ.L
DS:0030	A2	01	14	00	18	00	F5	19	FF	FF	FF	FF	00	00	00	00	ó.....J. ....
DS:0040	05	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	.....

1 Step 2ProcStep 3Retrieve 4Help ON 5BRK Menu 6 7 up 8 dn 9 le 10 ri

### Iteration 7:

DOSBox 0.74-3, Cpu speed: 3000 cycles, Frameskip 0, Program: AFD

AX 0000	SI 0000	CS 19F5	IP 012D	Stack +0 0000	Flags 7294
BX 0000	DI 0000	DS 19F5		+2 20CD	
CX 0009	BP 0000	ES 19F5	HS 19F5	+4 9FFF	OF DF IF SF ZF AF PF CF
DX 0001	SP FFFE	SS 19F5	FS 19F5	+6 EA00	0 0 1 1 0 1 1 0

CMD >

0129 D1160501	RCL	W/[0105],1	DS:0100	E9 0A 00 00 DC 05 00 20
012D FEC9	DEC	CL	DS:0108	01 D0 01 01 00 B1 10 8F
012F 75E2	JNZ	0113	DS:0110	16 07 01 D1 EA 73 0E A1
0131 B8004C	MOV	AX,4C00	DS:0118	03 01 01 06 09 01 A1 05
0134 85D2	TEST	DX,DX	DS:0120	01 11 06 0B 01 D1 26 03
0136 7504	JNZ	013C	DS:0128	01 D1 16 05 01 FE C9 75
0138 85C0	TEST	AX,AX	DS:0130	E2 B8 00 4C 85 D2 75 04
013A 741C	JZ	0158	DS:0138	85 C0 74 1C C7 46 DC 00
013C C746DC0000	MOV	[BP-24],0000	DS:0140	00 8E 5E FC 83 7D 0E 00
			DS:0148	74 09 8B 46 F2 48 3B 46

2	0	1	2	3	4	5	6	7	8	9	A	B	C	D	E	F
DS:0000	CD	20	FF	9F	00	EA	F0	FE	AD	DE	1B	05	C5	06	00	00
DS:0010	18	01	10	01	18	01	92	01	01	01	01	00	02	FF	FF	FF
DS:0020	FF	FF	FF	FF	FF	FF	FF	FF	FF	FF	FF	FF	EB	19	C0	11
DS:0030	A2	01	14	00	18	00	F5	19	FF	FF	FF	FF	00	00	00	00
DS:0040	05	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00

1 Step 2ProcStep 3Retrieve 4Help ON 5BRK Menu 6 7 up 8 dn 9 le 10 ri

### Iteration 8:

DOSBox 0.74-3, Cpu speed: 3000 cycles, Frameskip 0, Program: AFD

AX 0005	SI 0000	CS 19F5	IP 012D	Stack +0 0000	Flags 7294
BX 0000	DI 0000	DS 19F5		+2 20CD	
CX 0008	BP 0000	ES 19F5	HS 19F5	+4 9FFF	OF DF IF SF ZF AF PF CF
DX 0000	SP FFFE	SS 19F5	FS 19F5	+6 EA00	0 0 1 1 0 1 1 0

CMD >

0129 D1160501	RCL	W/[0105],1	DS:0100	E9 0A 00 00 B8 0B 00 20
012D FEC9	DEC	CL	DS:0108	01 D0 DD 06 00 B1 10 8F
012F 75E2	JNZ	0113	DS:0110	16 07 01 D1 EA 73 0E A1
0131 B8004C	MOV	AX,4C00	DS:0118	03 01 01 06 09 01 A1 05
0134 85D2	TEST	DX,DX	DS:0120	01 11 06 0B 01 D1 26 03
0136 7504	JNZ	013C	DS:0128	01 D1 16 05 01 FE C9 75
0138 85C0	TEST	AX,AX	DS:0130	E2 B8 00 4C 85 D2 75 04
013A 741C	JZ	0158	DS:0138	85 C0 74 1C C7 46 DC 00
013C C746DC0000	MOV	[BP-24],0000	DS:0140	00 8E 5E FC 83 7D 0E 00
			DS:0148	74 09 8B 46 F2 48 3B 46

2	0	1	2	3	4	5	6	7	8	9	A	B	C	D	E	F
DS:0000	CD	20	FF	9F	00	EA	F0	FE	AD	DE	1B	05	C5	06	00	00
DS:0010	18	01	10	01	18	01	92	01	01	01	01	00	02	FF	FF	FF
DS:0020	FF	FF	FF	FF	FF	FF	FF	FF	FF	FF	FF	FF	EB	19	C0	11
DS:0030	A2	01	14	00	18	00	F5	19	FF	FF	FF	FF	00	00	00	00
DS:0040	05	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00

1 Step 2ProcStep 3Retrieve 4Help ON 5BRK Menu 6 7 up 8 dn 9 le 10 ri

### Iteration 9:

DOSBox 0.74-3, Cpu speed: 3000 cycles, Frameskip 0, Program: AFD															-	×	
AX 0005	SI 0000	CS 19F5	IP 012D	Stack +0	0000	Flags 7214											
BX 0000	DI 0000	DS 19F5		+2	20CD												
CX 0007	BP 0000	ES 19F5	HS 19F5	+4	9FFF	OF DF IF SF ZF AF PF CF											
DX 0000	SP FFFE	SS 19F5	FS 19F5	+6	EA00	0 0 1 0 0 1 1 0											
CMD >						1	0	1	2	3	4	5	6	7			
0129 D1160501 RCL W/[0105],1						DS:0100	E9	0A	00	00	70	17	00	20			
012D FEC9 DEC CL						DS:0108	01	D0	DD	06	00	B1	10	8F			
012F 75E2 JNZ 0113						DS:0110	16	07	01	D1	EA	73	0E	A1			
0131 B8004C MOV AX,4C00						DS:0118	03	01	01	06	09	01	A1	05			
0134 85D2 TEST DX,DX						DS:0120	01	11	06	0B	01	D1	26	03			
0136 7504 JNZ 013C						DS:0128	01	D1	16	05	01	FE	C9	75			
0138 85C0 TEST AX,AX						DS:0130	E2	B8	00	4C	85	D2	75	04			
013A 741C JZ 0158						DS:0138	85	C0	74	1C	C7	46	DC	00			
013C C746DC0000 MOV [BP-24],0000						DS:0140	00	8E	5E	FC	83	7D	0E	00			
						DS:0148	74	09	8B	46	F2	48	3B	46			
2	0	1	2	3	4	5	6	7	8	9	A	B	C	D	E	F	
DS:0000	CD	20	FF	9F	00	EA	F0	FE	AD	DE	1B	05	C5	06	00	00	= f.Ω≡ i  .†...
DS:0010	18	01	10	01	18	01	92	01	01	01	01	00	02	FF	FF	FF	.....f. ....
DS:0020	FF	FF	FF	FF	FF	FF	FF	FF	FF	FF	FF	FF	EB	19	C0	11	δ. L
DS:0030	A2	01	14	00	18	00	F5	19	FF	FF	FF	FF	00	00	00	00	ó.....J. ....
DS:0040	05	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	.....
1	Step	2	ProcStep	3	Retrieve	4	Help ON	5	BRK Menu	6		7	up	8	dn	9	le 10 ri

### Iteration 10:

DOSBox 0.74-3, Cpu speed: 3000 cycles, Frameskip 0, Program: AFD															-	×
AX	0005	SI	0000	CS	19F5	IP	012D	Stack	+0 0000	Flags	7294					
BX	0000	DI	0000	DS	19F5				+2 20CD							
CX	0006	BP	0000	ES	19F5	HS	19F5		+4 9FFF	OF	DF	IF	SF	ZF		
DX	0000	SP	FFFE	SS	19F5	FS	19F5		+6 EA00	0	0	1	1	0		
CMD >																
0129 D1160501 RCL W[0105],1																
012D FEC9 DEC CL																
012F 75E2 JNZ 0113																
0131 B8004C MOV AX,4C00																
0134 85D2 TEST DX,DX																
0136 7504 JNZ 013C																
0138 85C0 TEST AX,AX																
013A 741C JZ 0158																
013C C746DC0000 MOV [BP-24],0000																
1 0 1 2 3 4 5 6 7 8 9 A B C D E F																
DS:0000	CD	20	FF	9F	00	EA	F0	FE	AD	DE	1B	05	C5	06		
DS:0010	18	01	10	01	18	01	92	01	01	01	01	00	02	FF		
DS:0020	FF	FF	FF	FF	FF	FF	FF	FF	FF	FF	FF	EB	19	C0		
DS:0030	A2	01	14	00	18	00	F5	19	FF	FF	FF	FF	00	00		
DS:0040	05	00	00	00	00	00	00	00	00	00	00	00	00	00		
= f.Ω≡ i ..†...																
.....f. .... δ.L																
ó.....J. ....																
.....																
1 Step 2ProcStep 3Retrieve 4Help ON 5BRK Menu 6 7 up 8 dn 9 le 10 ri																

### Iteration 11:

DOSBox 0.74-3, Cpu speed: 3000 cycles, Frameskip 0, Program: AFD															-	×	
AX	0005	SI	0000	CS	19F5	IP	012D	Stack	+0	0000	Flags	7294					
BX	0000	DI	0000	DS	19F5				+2	20CD							
CX	0005	BP	0000	ES	19F5	HS	19F5	+4	9FFF	OF	DF	IF	SF	ZF	AF	PF	CF
DX	0000	SP	FFFE	SS	19F5	FS	19F5	+6	EA00	0	0	1	1	0	1	1	0
CMD >																	
0129	D1160501	RCL	W/[0105],1					1	0	1	2	3	4	5	6	7	
012D	FEC9	DEC	CL					DS:0100	E9	0A	00	00	C0	5D	00	20	
012F	75E2	JNZ	0113					DS:0108	01	D0	DD	06	00	B1	10	8F	
0131	B8004C	MOV	AX,4C00					DS:0110	16	07	01	D1	EA	73	0E	A1	
0134	85D2	TEST	DX,DX					DS:0118	03	01	01	06	09	01	A1	05	
0136	7504	JNZ	013C					DS:0120	01	11	06	0B	01	D1	26	03	
0138	85C0	TEST	AX,AX					DS:0128	01	D1	16	05	01	FE	C9	75	
013A	741C	JZ	0158					DS:0130	E2	B8	00	4C	85	D2	75	04	
013C	C746DC0000	MOV	[BP-24],0000					DS:0138	85	C0	74	1C	C7	46	DC	00	
								DS:0140	00	8E	5E	FC	83	7D	0E	00	
								DS:0148	74	09	8B	46	F2	48	3B	46	
2	0	1	2	3	4	5	6	7	8	9	A	B	C	D	E	F	
DS:0000	CD	20	FF	9F	00	EA	F0	FE	AD	DE	1B	05	C5	06	00	00	= f.Ω≡ i  .+. . . . .ff. . . . .δ. L. ó. . . .J. . . . .
DS:0010	18	01	10	01	18	01	92	01	01	01	00	02	FF	FF	FF	FF	.....
DS:0020	FF	FF	FF	FF	FF	FF	FF	FF	FF	FF	FF	FF	EB	19	C0	11	.....
DS:0030	A2	01	14	00	18	00	F5	19	FF	FF	FF	FF	00	00	00	00	.....
DS:0040	05	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	.....
1	Step	2	ProcStep	3	Retrieve	4	Help ON	5	BRK Menu	6		7	up	8	dn	9	le 10 ri

### Iteration 12:

DOSBox 0.74-3, Cpu speed: 3000 cycles, Frameskip 0, Program: AFD															-		X																																																																																		
AX	0005	SI	0000	CS	19F5	IP	012D	Stack	+0	0000	Flags	7294																																																																																							
BX	0000	DI	0000	DS	19F5				+2	20CD																																																																																									
CX	0004	BP	0000	ES	19F5	HS	19F5	+4	9FFF	OF	DF	IF	SF	ZF	AF	PF	CF																																																																																		
DX	0000	SP	FFFE	SS	19F5	FS	19F5	+6	EA00	0	0	1	1	0	1	1	0																																																																																		
CMD >										1								0		1		2		3		4		5		6		7																																																																			
0129 D1160501										RCL		W/[0105],1										DS:0100		E9		0A		00		00		80		BB		00		20																																																													
012D FEC9										DEC		CL										DS:0108		01		D0		DD		06		00		B1		10		80																																																													
012F 75E2										JNZ		0113										DS:0110		16		07		01		D1		EA		73		0E		A0																																																													
0131 B8004C										MOV		AX,4C00										DS:0118		03		01		01		06		09		01		A1		00																																																													
0134 85D2										TEST		DX,DX										DS:0120		01		11		06		0B		01		D1		26		00																																																													
0136 7504										JNZ		013C										DS:0128		01		D1		16		05		01		FE		C9		70																																																													
0138 85C0										TEST		AX,AX										DS:0130		E2		B8		00		4C		85		D2		75		00																																																													
013A 741C										JZ		0158										DS:0138		85		C0		74		1C		C7		46		DC		00																																																													
013C C746DC0000										MOV		[BP-24],0000										DS:0140		00		8E		5E		FC		83		7D		0E		00																																																													
																						DS:0148		74		09		8B		46		F2		48		3B		40																																																													
2										0		1		2		3		4		5		6		7		8		9		A		B		C		D		E		F																																																											
DS:0000										CD		20		FF		9F		00		EA		F0		FE		AD		DE		1B		05		C5		06		00		00		= f.Ω≡ i  .+. .																																																									
DS:0010										18		01		10		01		18		01		92		01		01		01		00		02		FF		FF		FF		.....ff. ....																																																											
DS:0020										FF		FF		FF		FF		FF		FF		FF		FF		FF		FF		FF		EB		19		C0		11		δ. L.																																																											
DS:0030										A2		01		14		00		18		00		F5		19		FF		FF		FF		FF		00		00		00		00		6.....J. ...																																																									
DS:0040										05		00		00		00		00		00		00		00		00		00		00		00		00		00		00		00		.....																																																									
1 Step										2ProcStep										3Retrieve										4Help ON										5BRK Menu										6										7 up										8 dn										9 le										10 ri									



### Iteration 13:

DOSBox 0.74-3, Cpu speed: 3000 cycles, Frameskip 0, Program: AFD

AX 0005	SI 0000	CS 19F5	IP 012D	Stack +0 0000	Flags 7254
BX 0000	DI 0000	DS 19F5		+2 20CD	
CX 0003	BP 0000	ES 19F5	HS 19F5	+4 9FFF	OF DF IF SF ZF AF PF CF
DX 0000	SP FFFE	SS 19F5	FS 19F5	+6 EA00	0 0 1 0 1 1 1 0

CMD >

0129 D1160501	RCL	W/[0105],1	DS:0100	E9 0A 00 00 00 77 01 20
012D FEC9	DEC	CL	DS:0108	01 D0 DD 06 00 B1 10 8F
012F 75E2	JNZ	0113	DS:0110	16 07 01 D1 EA 73 0E A1
0131 B8004C	MOV	AX,4C00	DS:0118	03 01 01 06 09 01 A1 05
0134 85D2	TEST	DX,DX	DS:0120	01 11 06 0B 01 D1 26 03
0136 7504	JNZ	013C	DS:0128	01 D1 16 05 01 FE C9 75
0138 85C0	TEST	AX,AX	DS:0130	E2 B8 00 4C 85 D2 75 04
013A 741C	JZ	0158	DS:0138	85 C0 74 1C C7 46 DC 00
013C C746DC0000	MOV	[BP-24],0000	DS:0140	00 8E 5E FC 83 7D 0E 00
			DS:0148	74 09 8B 46 F2 48 3B 46

2	0	1	2	3	4	5	6	7	8	9	A	B	C	D	E	F
DS:0000	CD	20	FF	9F	00	EA	F0	FE	AD	DE	1B	05	C5	06	00	00
DS:0010	18	01	10	01	18	01	92	01	01	01	01	00	02	FF	FF	FF
DS:0020	FF	FF	FF	FF	FF	FF	FF	FF	FF	FF	FF	FF	EB	19	C0	11
DS:0030	A2	01	14	00	18	00	F5	19	FF	FF	FF	FF	00	00	00	00
DS:0040	05	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00

1 Step 2ProcStep 3Retrieve 4Help ON 5BRK Menu 6 7 up 8 dn 9 le 10 ri

### Iteration 14

DOSBox 0.74-3, Cpu speed: 3000 cycles, Frameskip 0, Program: AFD

AX 0005	SI 0000	CS 19F5	IP 012D	Stack +0 0000	Flags 7254
BX 0000	DI 0000	DS 19F5		+2 20CD	
CX 0002	BP 0000	ES 19F5	HS 19F5	+4 9FFF	OF DF IF SF ZF AF PF CF
DX 0000	SP FFFE	SS 19F5	FS 19F5	+6 EA00	0 0 1 0 1 1 1 0

CMD >

0129 D1160501	RCL	W/[0105],1	DS:0100	E9 0A 00 00 00 EE 02 20
012D FEC9	DEC	CL	DS:0108	01 D0 DD 06 00 B1 10 8F
012F 75E2	JNZ	0113	DS:0110	16 07 01 D1 EA 73 0E A1
0131 B8004C	MOV	AX,4C00	DS:0118	03 01 01 06 09 01 A1 05
0134 85D2	TEST	DX,DX	DS:0120	01 11 06 0B 01 D1 26 03
0136 7504	JNZ	013C	DS:0128	01 D1 16 05 01 FE C9 75
0138 85C0	TEST	AX,AX	DS:0130	E2 B8 00 4C 85 D2 75 04
013A 741C	JZ	0158	DS:0138	85 C0 74 1C C7 46 DC 00
013C C746DC0000	MOV	[BP-24],0000	DS:0140	00 8E 5E FC 83 7D 0E 00
			DS:0148	74 09 8B 46 F2 48 3B 46

2	0	1	2	3	4	5	6	7	8	9	A	B	C	D	E	F
DS:0000	CD	20	FF	9F	00	EA	F0	FE	AD	DE	1B	05	C5	06	00	00
DS:0010	18	01	10	01	18	01	92	01	01	01	01	00	02	FF	FF	FF
DS:0020	FF	FF	FF	FF	FF	FF	FF	FF	FF	FF	FF	FF	EB	19	C0	11
DS:0030	A2	01	14	00	18	00	F5	19	FF	FF	FF	FF	00	00	00	00
DS:0040	05	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00

1 Step 2ProcStep 3Retrieve 4Help ON 5BRK Menu 6 7 up 8 dn 9 le 10 ri

### Iteration 15:

```

DOSBox 0.74-3, Cpu speed: 3000 cycles, Frameskip 0, Program: AFD
AX 0005 SI 0000 CS 19F5 IP 012D Stack +0 0000 Flags 7254
BX 0000 DI 0000 DS 19F5 +2 20CD
CX 0001 BP 0000 ES 19F5 HS 19F5 +4 9FFF OF DF IF SF ZF AF PF CF
DX 0000 SP FFFE SS 19F5 FS 19F5 +6 EA00 0 0 1 0 1 1 1 0

CMD >

0129 D1160501 RCL W/[0105],1
012D FEC9 DEC CL
012F 75E2 JNZ 0113
0131 B8004C MOV AX,4C00
0134 85D2 TEST DX,DX
0136 7504 JNZ 013C
0138 85C0 TEST AX,AX
013A 741C JZ 0158
013C C746DC0000 MOV [BP-24],0000

2 0 1 2 3 4 5 6 7 8 9 A B C D E F
DS:0000 CD 20 FF 9F 00 EA F0 FE AD DE 1B 05 C5 06 00 00 = f.Ω≡ i|.+.
DS:0010 18 01 10 01 18 01 92 01 01 01 01 00 02 FF FF FF .....f. ....
DS:0020 FF FF FF FF FF FF FF FF FF FF FF FF EB 19 C0 11 δ.L
DS:0030 A2 01 14 00 18 00 F5 19 FF FF FF FF 00 00 00 00 6.....J. ...
DS:0040 05 00 00 00 00 00 00 00 00 00 00 00 00 00 00 .....

1 Step 2ProcStep 3Retrieve 4Help ON 5BRK Menu 6 7 up 8 dn 9 le 10 ri

```

### Iteration 16:

```

DOSBox 0.74-3, Cpu speed: 3000 cycles, Frameskip 0, Program: AFD
AX 4C00 SI 0000 CS 19F5 IP 0134 Stack +0 0000 Flags 7244
BX 0000 DI 0000 DS 19F5
CX 0000 BP 0000 ES 19F5 HS 19F5 +4 9FFF OF DF IF SF ZF AF PF CF
DX 0000 SP FFFE SS 19F5 FS 19F5 +6 EA00 0 0 1 0 1 0 1 0

CMD >

0131 B8004C MOV AX,4C00
0134 85D2 TEST DX,DX
0136 7504 JNZ 013C
0138 85C0 TEST AX,AX
013A 741C JZ 0158
013C C746DC0000 MOV [BP+24],0000
0141 8E5EFC MOV DS,[BP-04]
0144 837D0E00 CMP [DI+0E],0000
0148 7409 JZ 0153

1 0 1 2 3 4 5 6 7
DS:0100 E9 0A 00 00 00 DC 05 2C
DS:0108 01 D0 DD 06 00 ←
DS:0110 16 07 01 D1 EA 73 0E A1
DS:0118 03 01 01 06 09 01 A1 05
DS:0120 01 11 06 0B 01 D1 26 03
DS:0128 01 D1 16 05 01 FE C9 75
DS:0130 E2 B8 00 4C 85 D2 75 04
DS:0138 85 C0 74 1C C7 46 DC 00
DS:0140 00 8E 5E FC 83 7D 0E 00
DS:0148 74 09 8B 46 F2 48 3B 46

2 0 1 2 3 4 5 6 7 8 9 A B C D E F
DS:0000 CD 20 FF 9F 00 EA F0 FE AD DE 1B 05 C5 06 00 00 = f.Ω■ i|.†...
DS:0010 18 01 10 01 18 01 92 01 01 01 01 00 02 FF FF FF .....f. ....
DS:0020 FF FF FF FF FF FF FF FF FF FF FF FF EB 19 C0 11 .....δ.L.
DS:0030 A2 01 14 00 18 00 F5 19 FF FF FF FF 00 00 00 00 6.....J. ....
DS:0040 05 00 00 00 00 00 00 00 00 00 00 00 00 00 00 .....

1 Step 2ProcStep 3Retrieve 4Help ON 5BRK Menu 6 7 up 8 dn 9 le 10 ri

```

1500×300

=

450000

00006DDD0

Hexadecimal ▾

$1556720_8 = 450000_{10}$