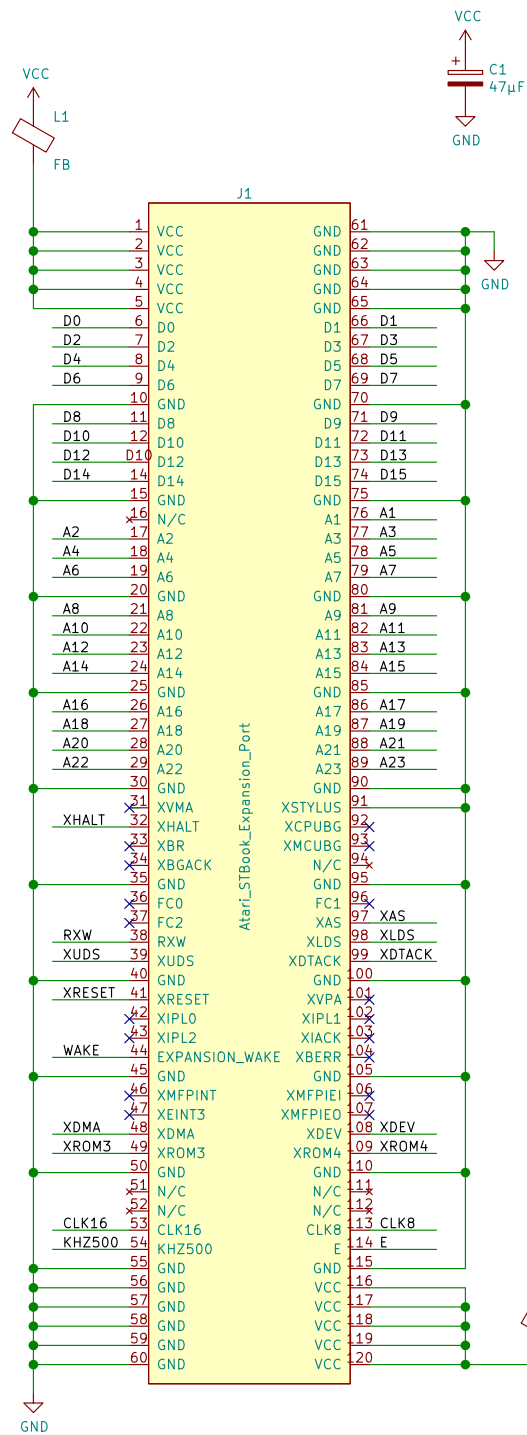


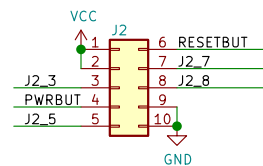
ST BOOK EXPANSION PORT



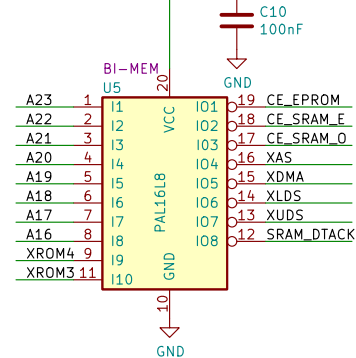
TEST POINTS



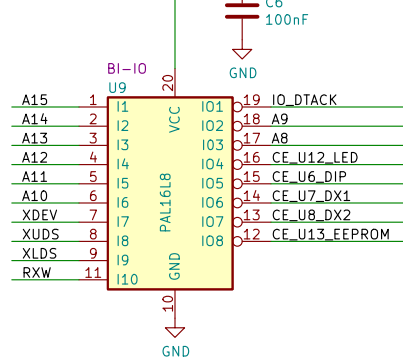
TEST CONNECTOR



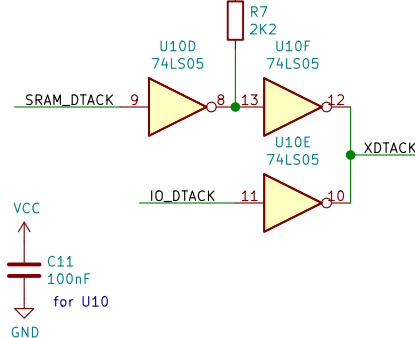
MEMORY DECODER



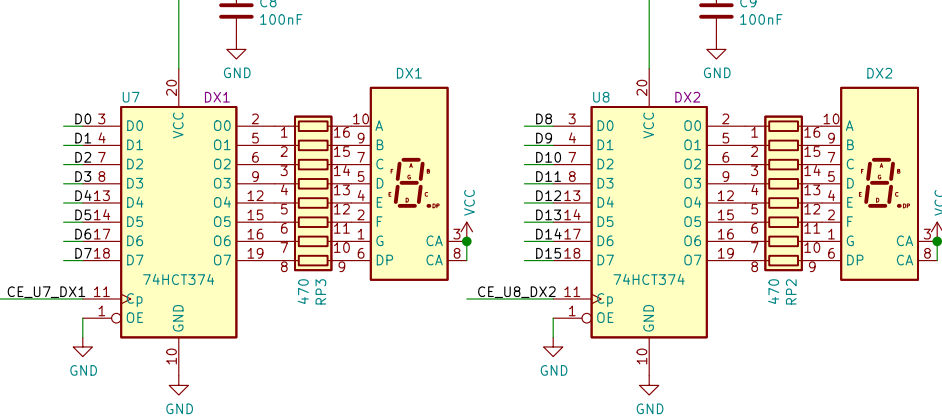
IO DECODER



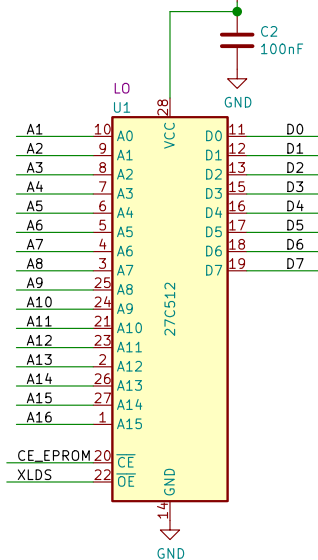
DTACK GENERATION



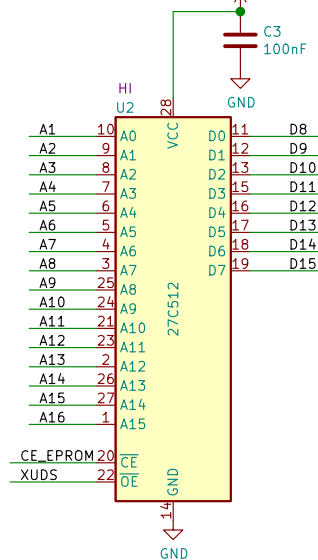
TWO 7-DIGIT LEDS



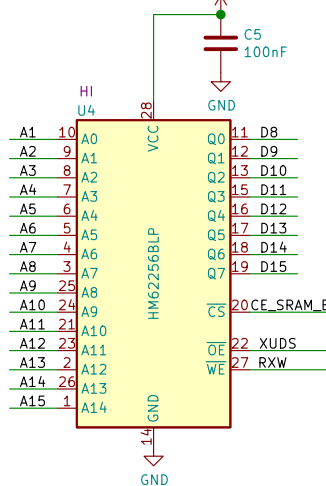
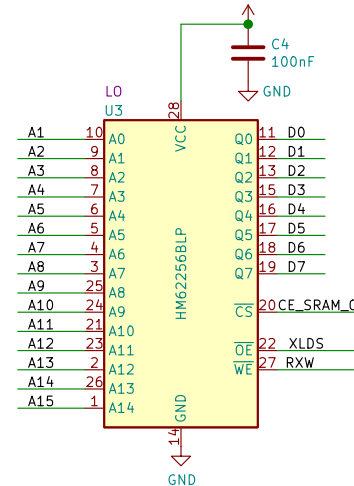
64KB ROM



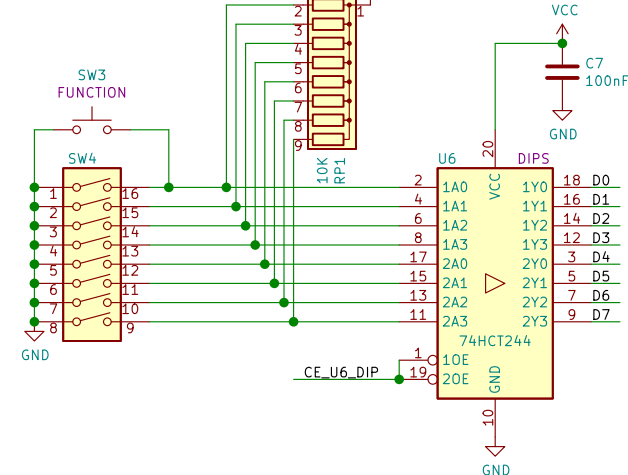
64KB SRAM



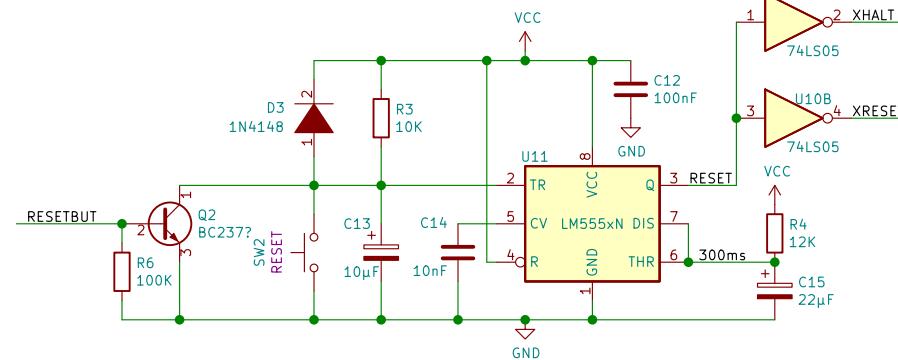
64KB SRAM



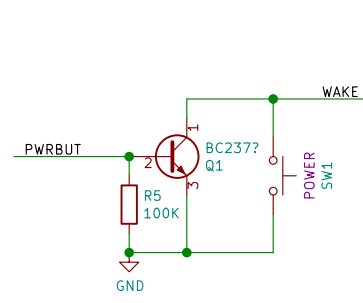
DIP SWITCHES



RESET



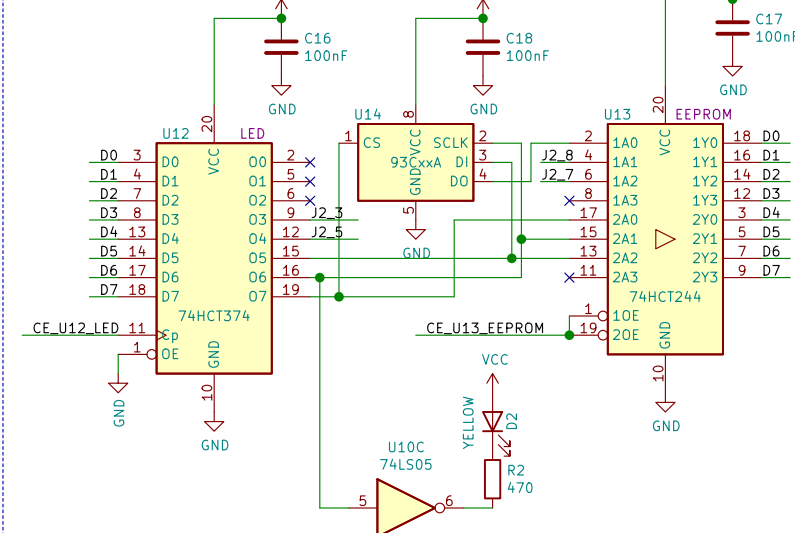
POWER ON



POWER LED



FEEDBACK LED and EEPROM



Reverse engineered by Markus Fritze, 2021

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File: STBookDiagnosticTest.sch

Title: STBook/STylus test

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Rev: 2

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