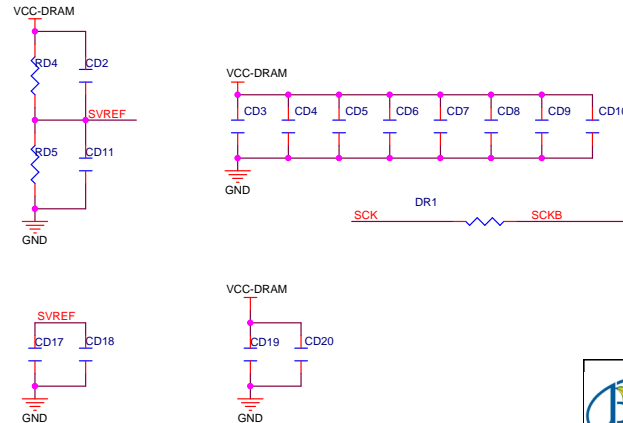
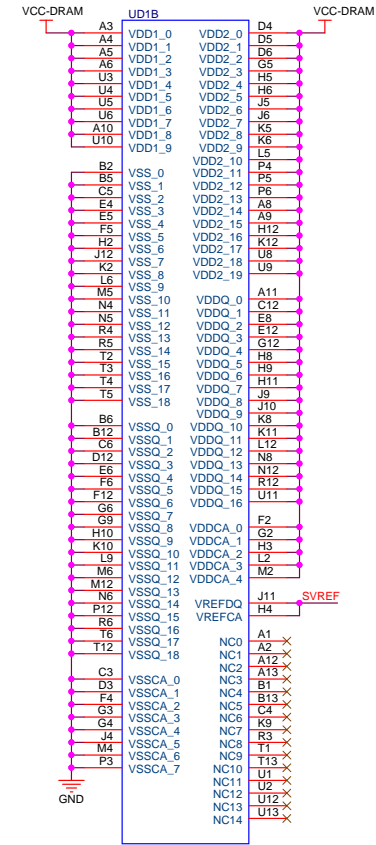
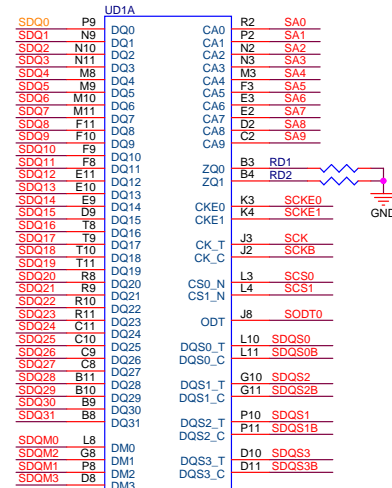
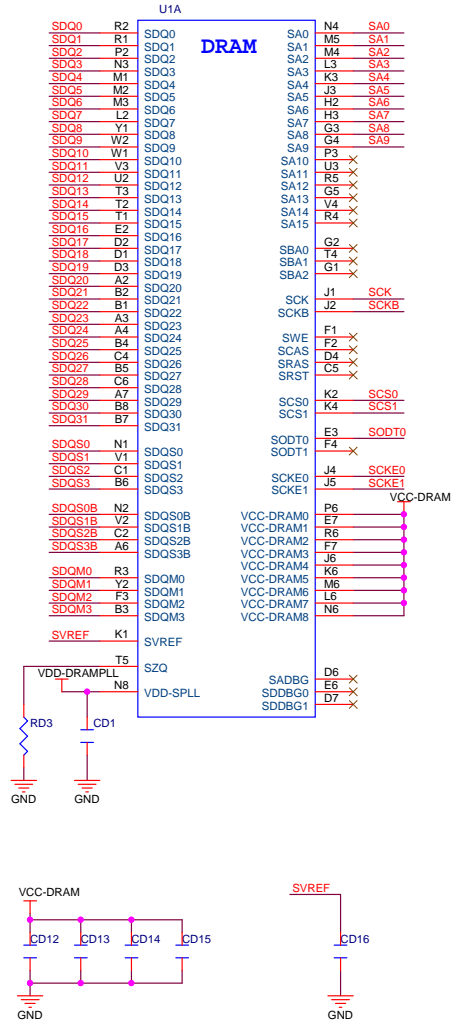


LPDDR3_32X1

VDD-DRAMLL
VCC-DRAM



SINOVOIP CO., LIMITED			
Design Name		BPI-M3	
Size	Page Name	Rev	
A3	LPDDR3_32x1	V1.1	
Date:	Friday, October 16, 2015	Sheet	5 of 16



2



2



CODEC

Pin connections for CODEC:

- AVCC
- VCC-IO1
- VCC-IO2
- LDOIN
- VCC-RTC
- CPVDD
- VCC-PL
- PMU-SCK
- PMU-SDA
- IRQ-AUDIO
- AP-NMI#
- AP-CK32KO
- AP-I2S-MCLK
- AP-I2S-BCLK
- AP-I2S-LRCK
- AP-I2S-DIN
- AP-I2S-DOU0
- BT-PCM-CLK
- BT-PCM-SYNC
- BT-PCM-DIN
- BT-PCM-DOU0
- CODEC-SCK
- CODEC-SDA

Main CODEC circuit diagram showing connections to U2B and various components:

- Power: AVCC, VCC-IO1, VCC-IO2, LDOIN, VCC-RTC, CPVDD, VCC-PL.
- Audio: MIC1P, MIC1N, MIC2P, MIC2N, HPOUTL, HPOUTR, HPOUTFB, SPOLP, SPOLN, SPORP, SPORN, EAROUTP, EAROUTN, LINEOUTP, LINEOUTN, CKO1-RTC, CKO2-RTC, CKO3-RTC, IRQ-RTC, IRQ-AUDIO.
- Control: MBIAS, HBIAS, MIC1P, MIC1N, MIC2P, MIC2N, LINEINL, LINEINR, AXIL, AXIR, AGND_2, AGND_1, AGND, VRP, SCK, SDA, MCLK1, BCLK1, LRCK1, SDOUT1, SDIN1, MCLK2, BCLK2, LRCK2, SDOUT2, SDIN2, BCLK3, LRCK3, SDOUT3, SDIN3.
- Other: VDD-CORE, C58, C59, C54, C55, C56, C57, C50, C45, R41, R42, R43, R44, R45, R46, R47, R48, R49, C64, C65, C66, C63, G1, F1, G2, H1, F2, D1, E2, C1, D2, A1, B1, B2, A2, B9, X32KI, A10, X32KO, R50, R51, L1.

HP_JACK

HP_JACK circuit diagram showing connections to CN2 and various components:

- Power: HBIAS, HS-MIC, MIC2N-N, HPOUTL, HPOUTR, HPOUTFB.
- Audio: MIC2P, MIC2N, HS-MIC, MIC2N-N, HPOUTL, HPOUTR, HPOUTFB.
- Control: R41, C45, C50, C54, C55, C56, C57, C61, C62, R46, R47, R48, R143, R144, ESD7, ESD8, ESD9, ESD10.

POWER section diagram showing connections to various components:

- VCC-RTC, AVCC, VCC-IO1, VCC-IO2, LDOIN.
- CP43, CP44, CP45, CP46, CP47.

OSC-RTC section diagram showing connections to various components:

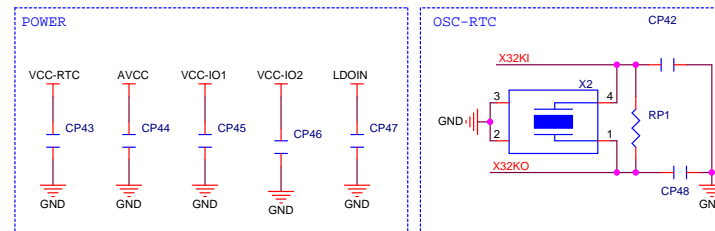
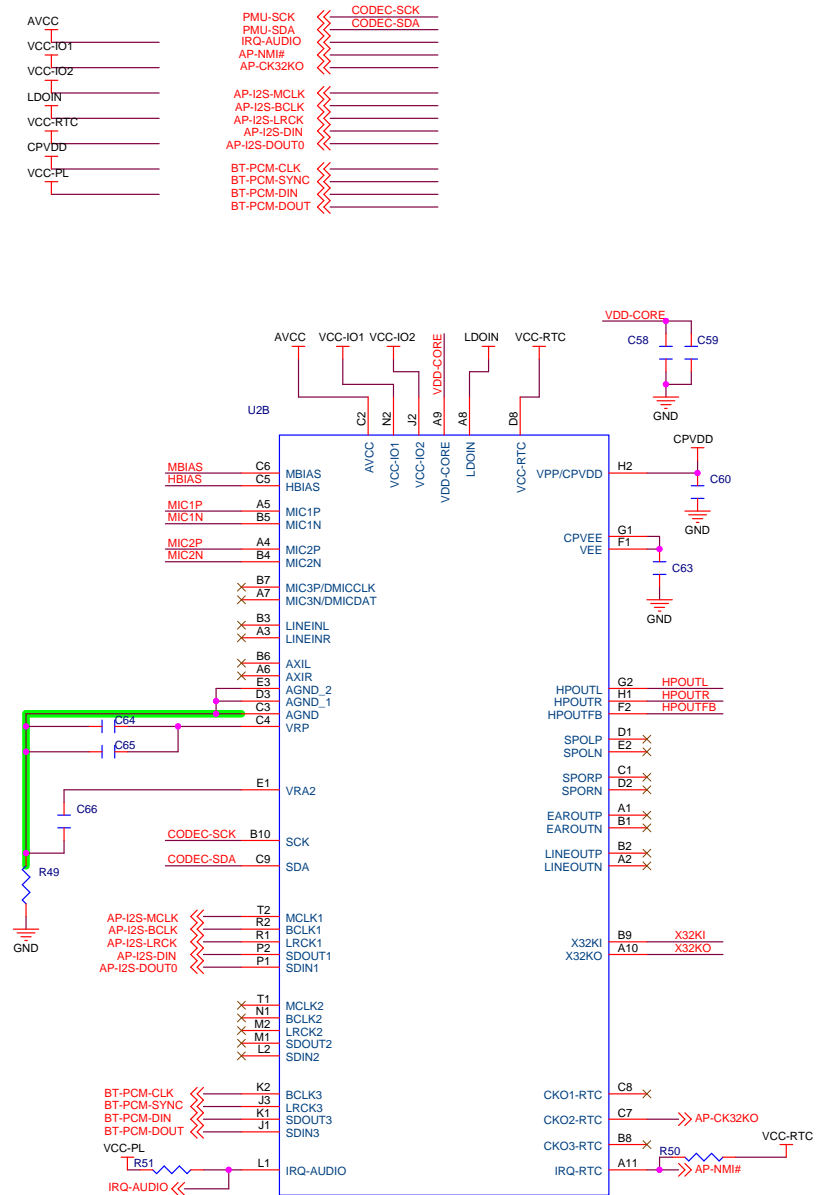
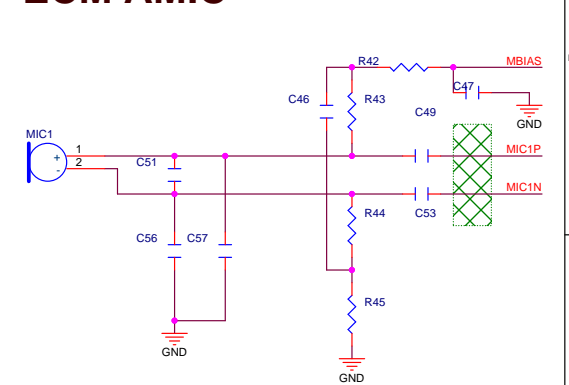
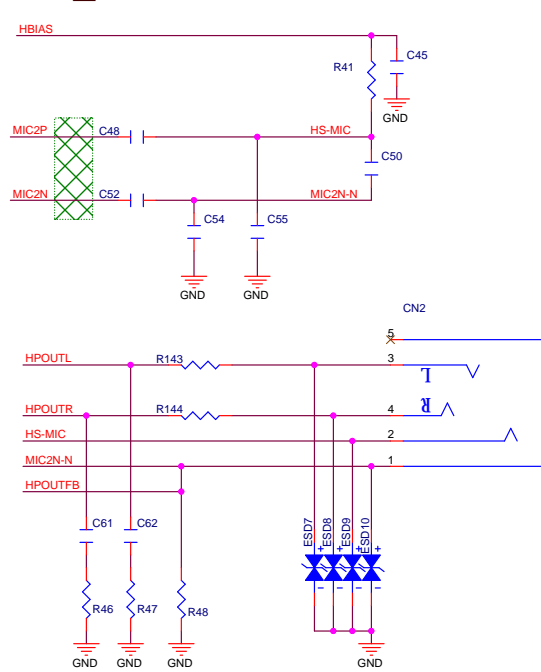
- OSC-RTC, CP42, X32KI, X32KO, RP1, CP48.

ECM AMIC

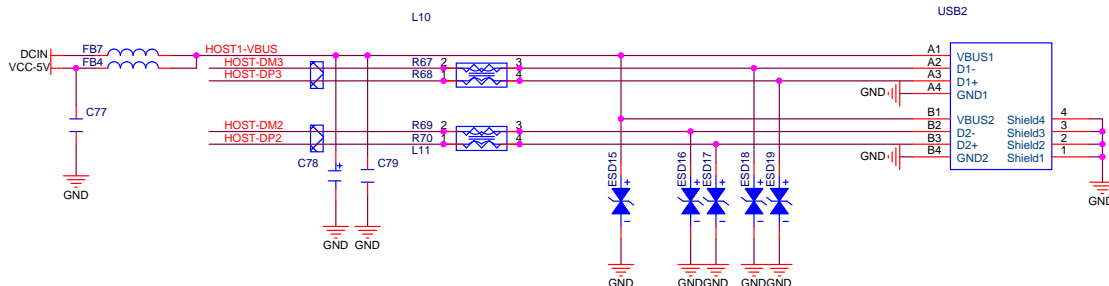
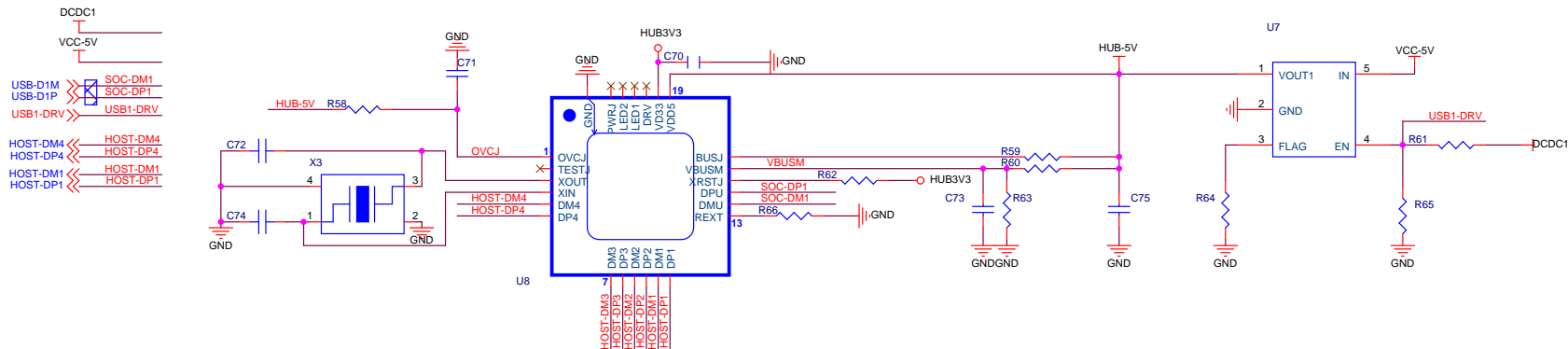
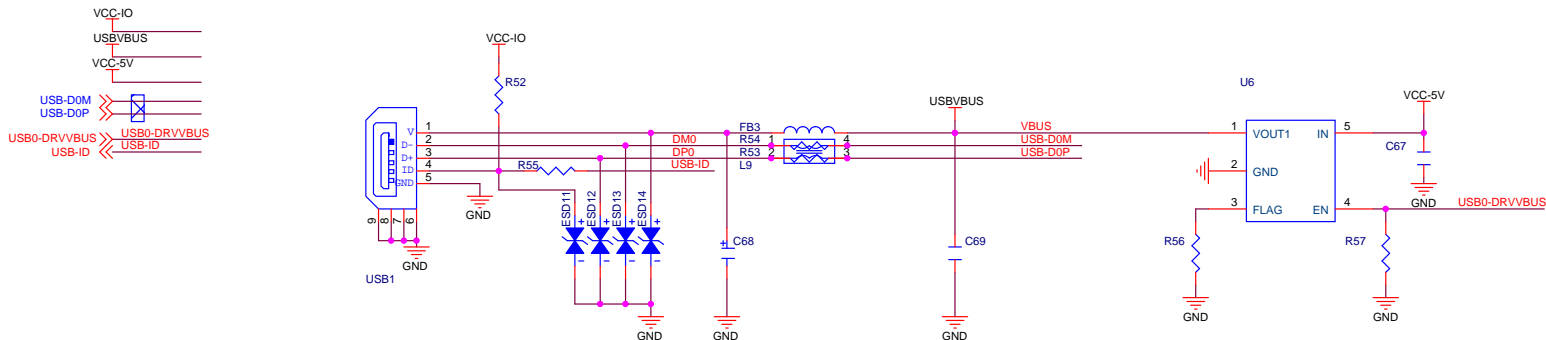
ECM AMIC circuit diagram showing connections to MIC1 and various components:

- Power: MBIAS, MIC1P, MIC1N, MIC2P, MIC2N, HS-MIC, MIC2N-N, HPOUTL, HPOUTR, HPOUTFB.
- Audio: MIC1P, MIC1N, MIC2P, MIC2N, HS-MIC, MIC2N-N, HPOUTL, HPOUTR, HPOUTFB.
- Control: R42, R43, R44, R45, C46, C47, C49, C51, C52, C53, C54, C55, C56, C57, C60.

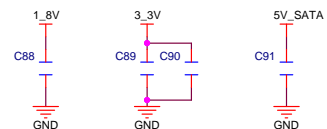
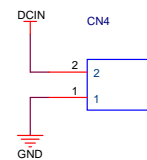
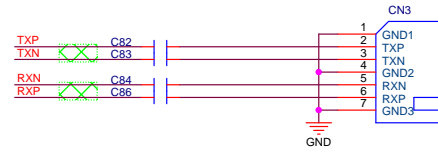
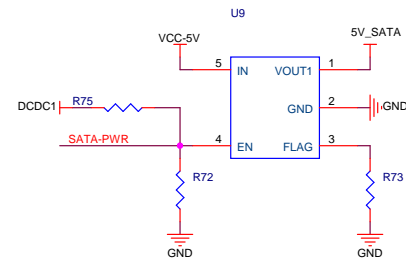
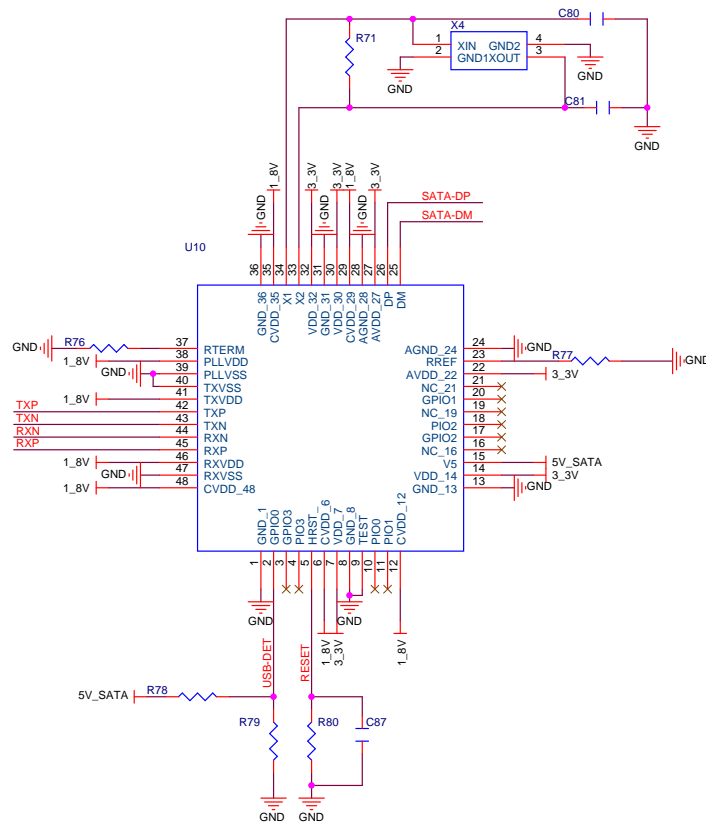
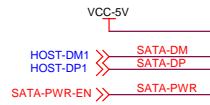
SINOVOIP CO., LIMITED			
Design Name			
BPI-M3			
Size	Page Name	Rev	
A3	CODEC	V1.1	
Date:	Friday, October 16, 2015	Sheet	9 of 16




USB

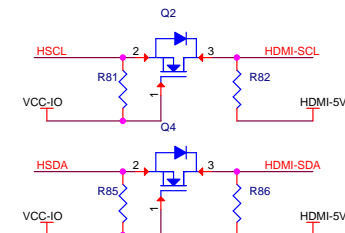
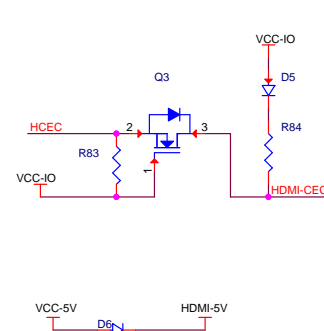
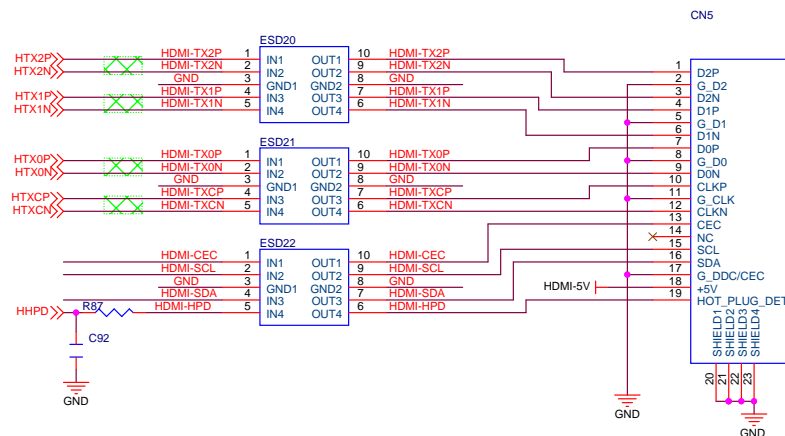


USB-SATA

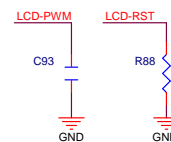
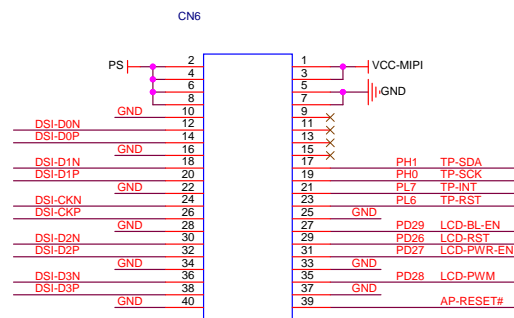
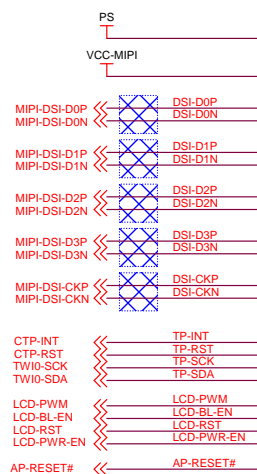


		SINOVOIP CO.,LIMITED	
		Design Name	
Size		Page Name	
A3		USB-SATA	
Date:		Friday, October 16, 2015	Sheet 11 of 16
Rev		V1.1	

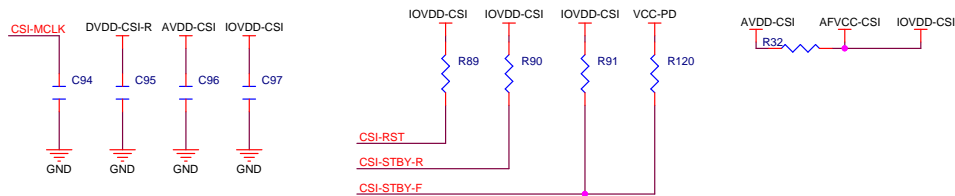
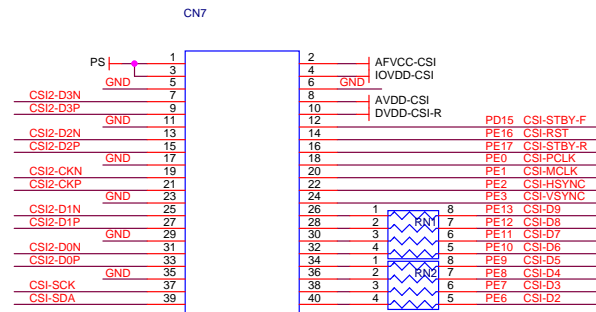
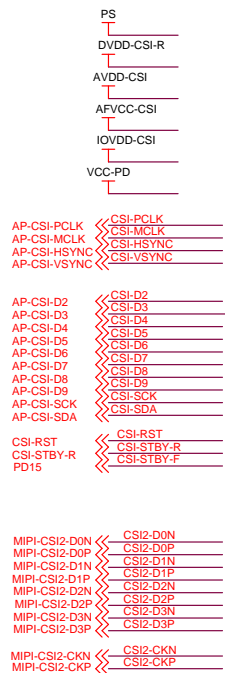
HDMI



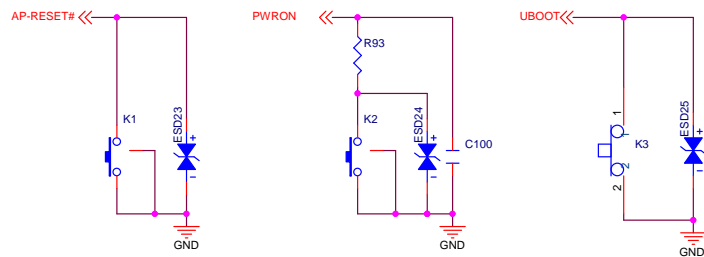
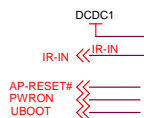
MIPI DSI



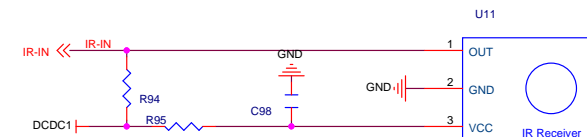
CAMERA



KEYs

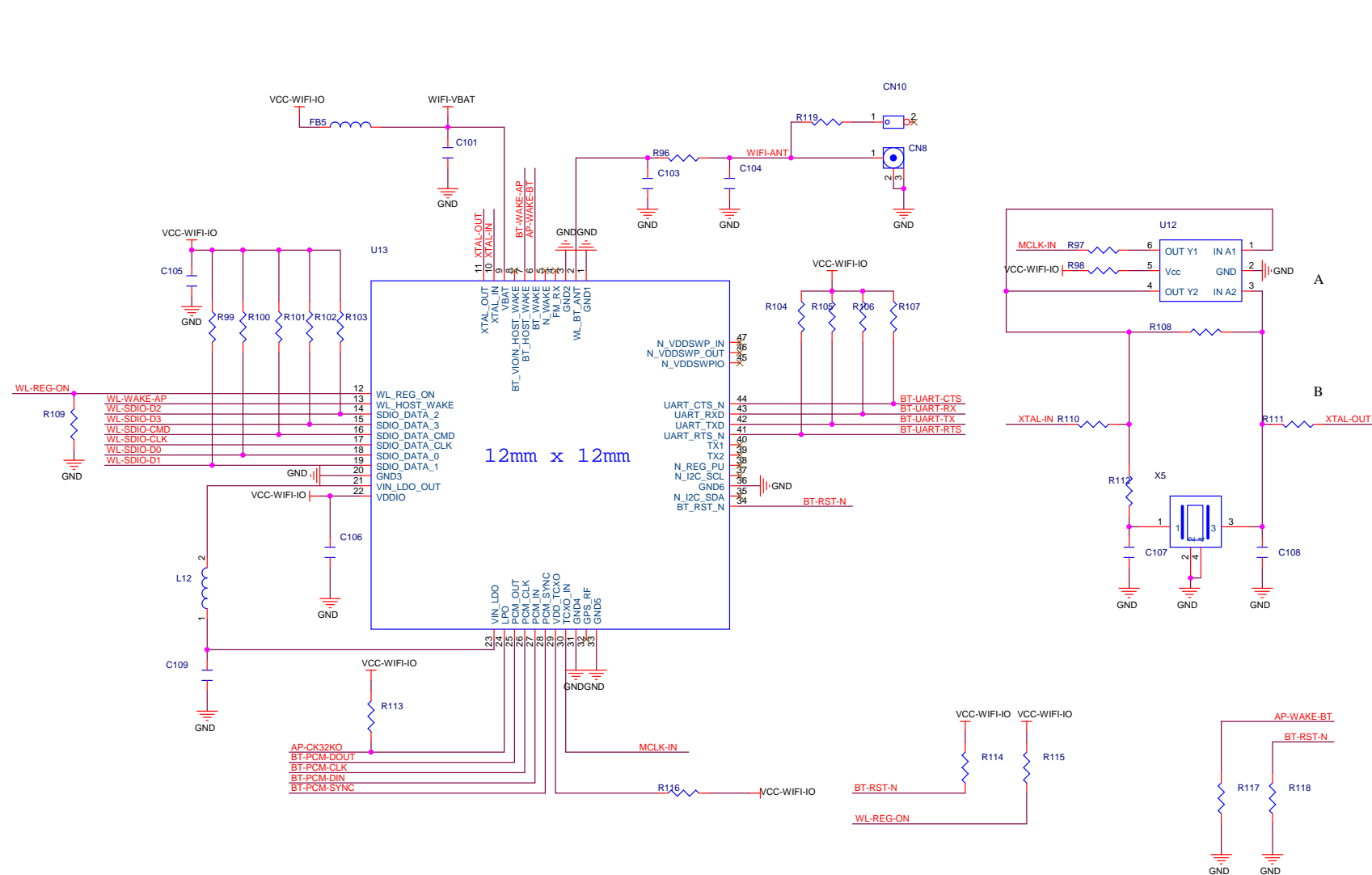


IR MODULE

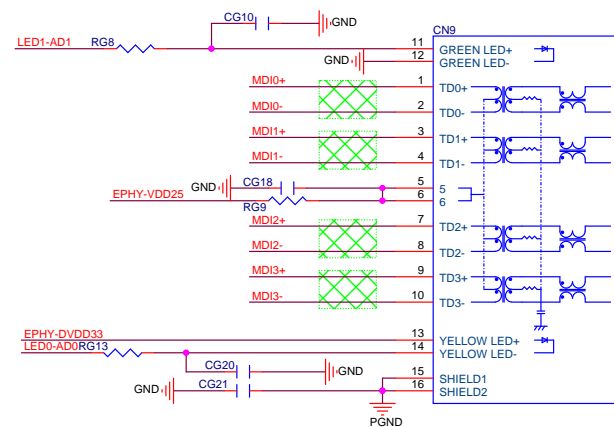
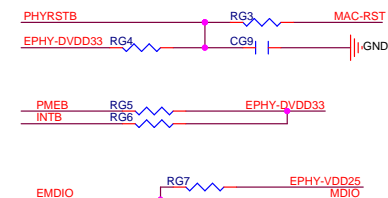
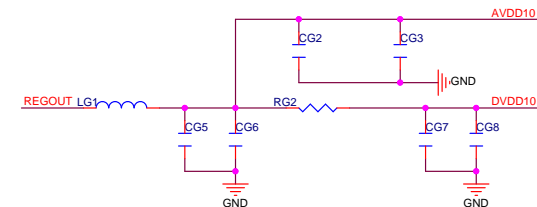
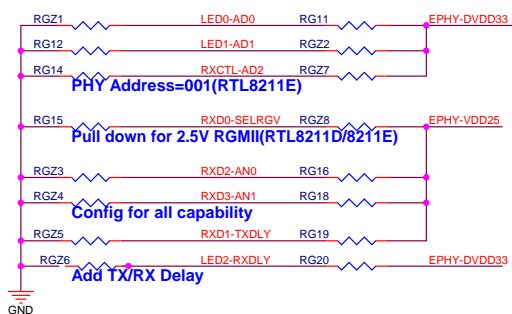
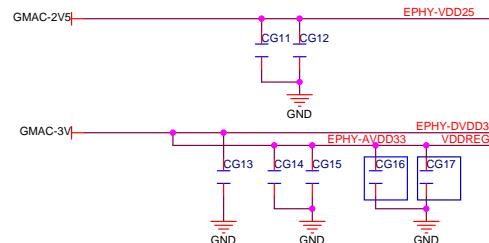
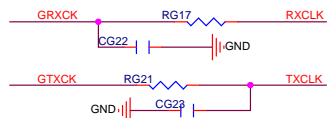
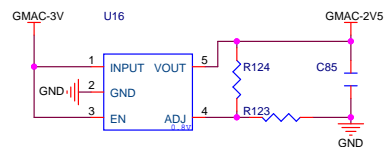


SINOVOIP CO.,LIMITED	
Design Name	BPI-M3
Size	Page Name
A3	CAMERA-IR-KEYs
Date:	Friday, October 16, 2015
Sheet	13 of 16
Rev	V1.1

SDIO WIFI+BT

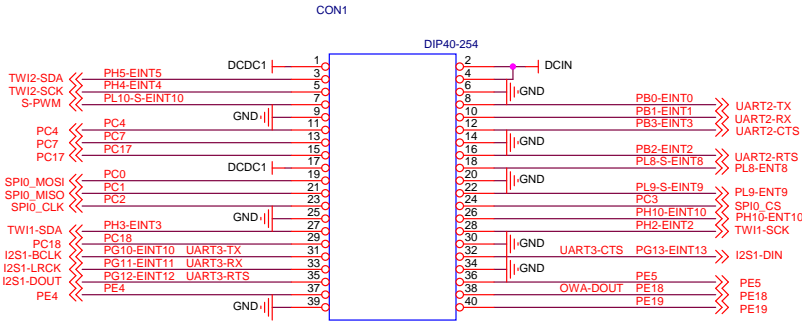


10/100/1000 RGMII Ethernet PHY

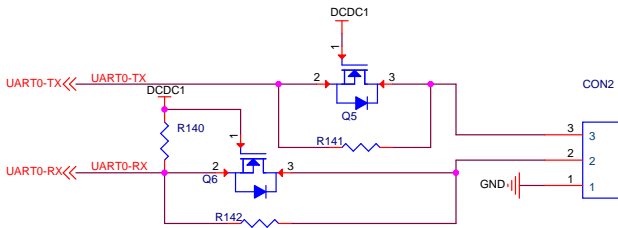


EXT PORT

EXT



DEBUG



USB

