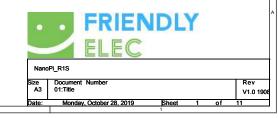
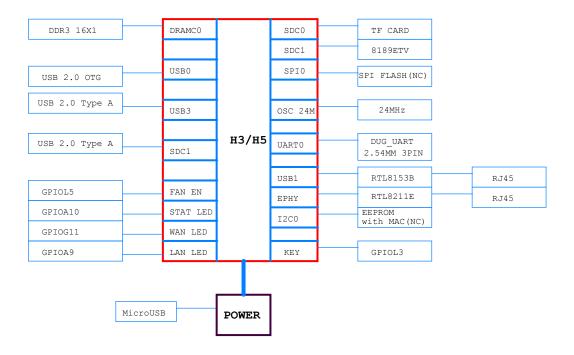
# NanoPi\_R1S

Revision

1908 , V1.0 :first release

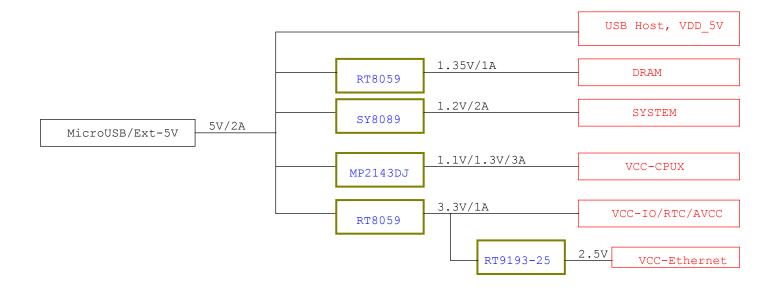


**BLOCK** 

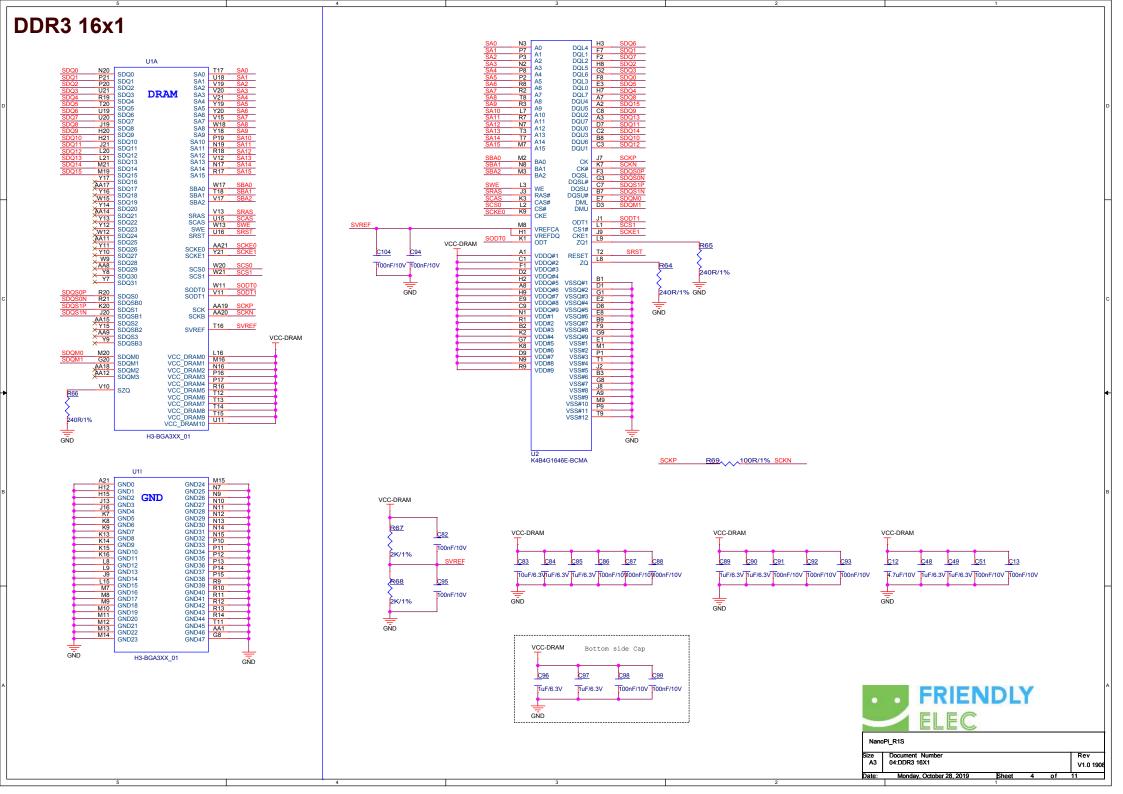


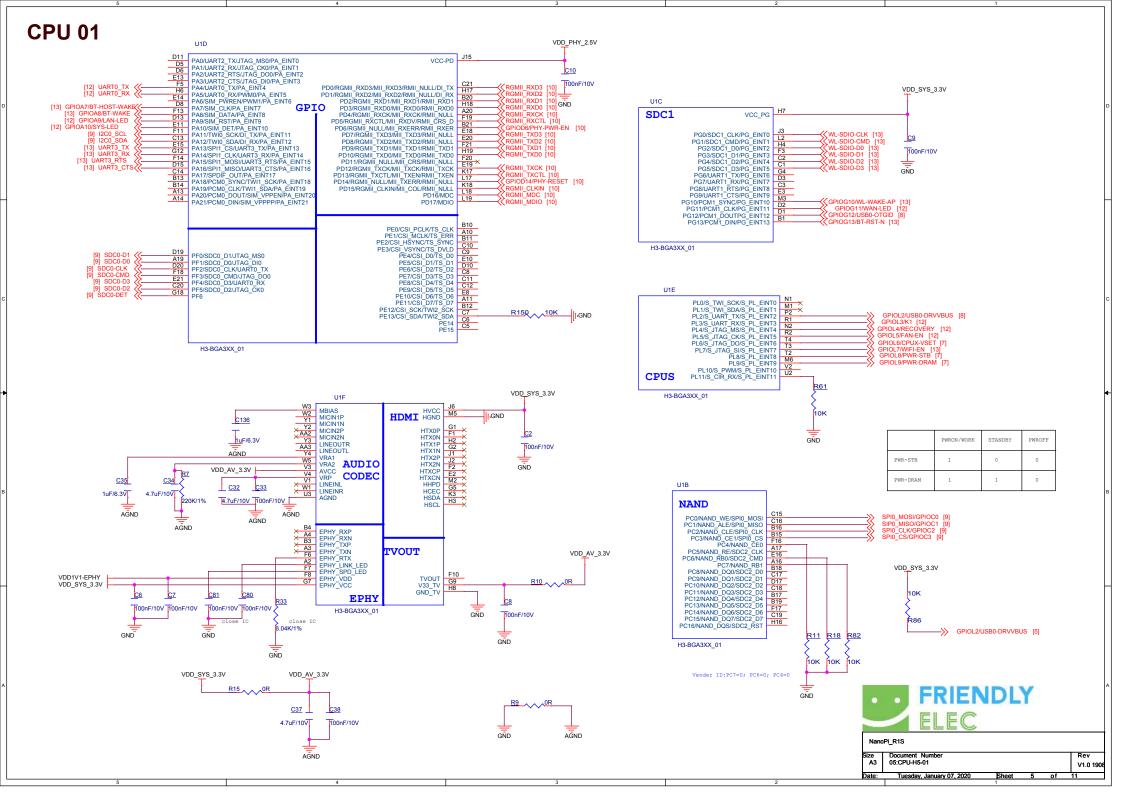


## **POWER TREE**

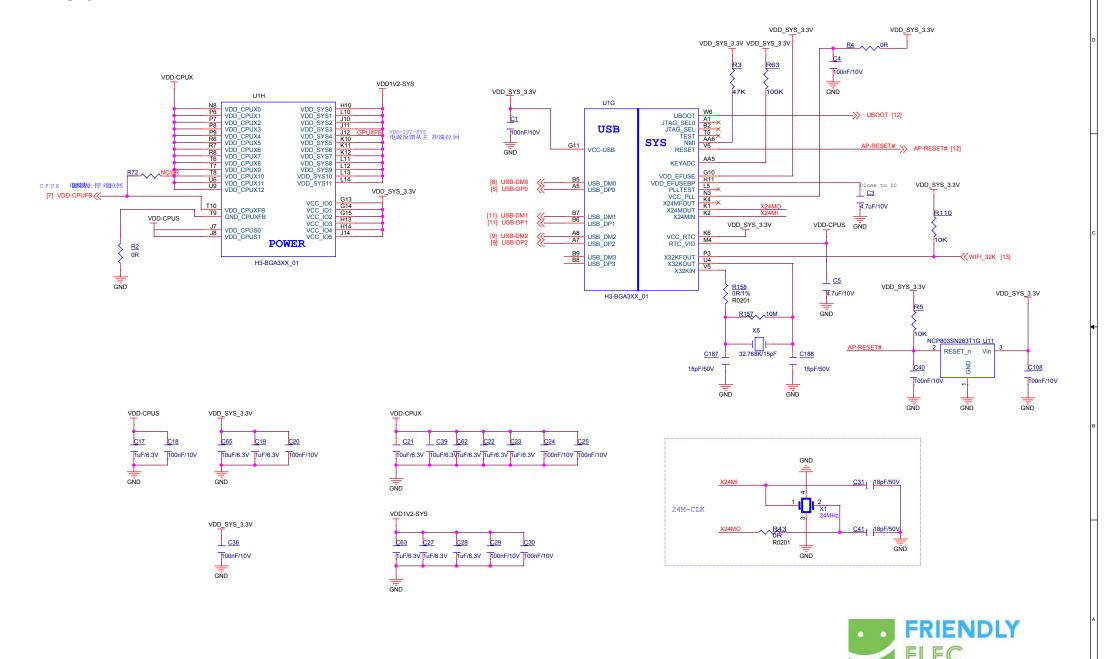








### **CPU 02**



NanoPi\_R1S

Document Number

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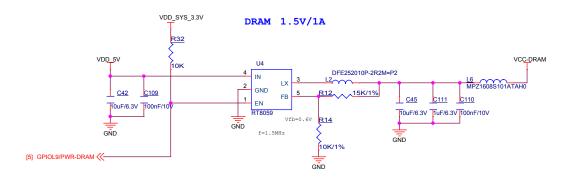
06:CPU-H5-02

Rev

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V1.0 1908





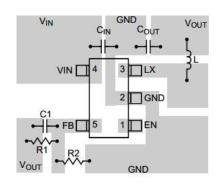
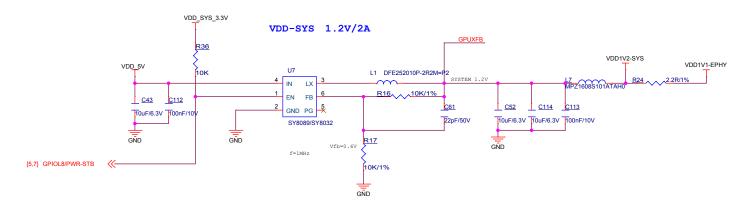
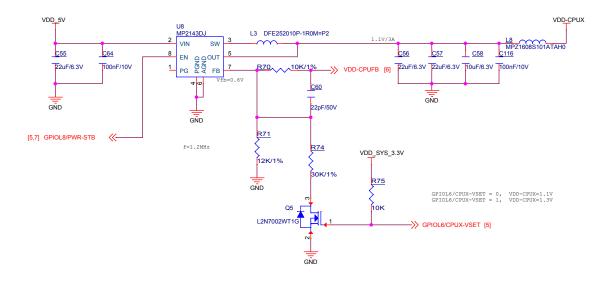


Figure 3. PCB Layout Guide



#### VCC-CPUX 1.1V/3A



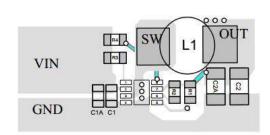
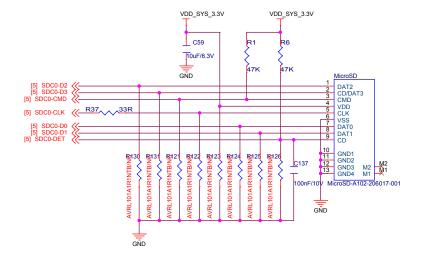


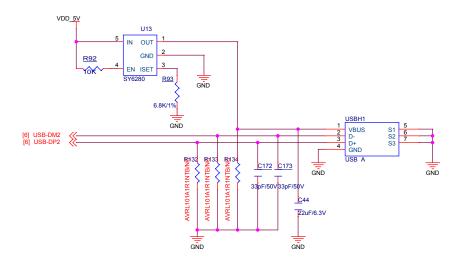
Figure 3: Layout Recommendation



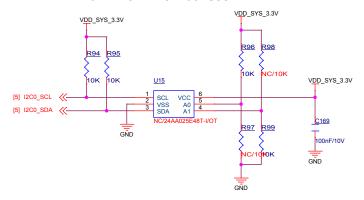
5 4 3 POWER 02 VDD\_SYS\_3.3V Q1 AO3415A VDD\_5V MicroUSB 6 % VBUS 2 7 D- 3 8 D+ 4 9 DD 5 R29 (00R) G USB-DM0 [6] USB-DP0 [6] GPIOG12/USB0-OTGID [5] D1 BCM856BS Q4A AVRL10141R1NTBNR AVRL10 22uF/6.3V AZ5825-01 를 GND VCC-IO 3.3V/1A VDD\_5V VDD\_SYS\_3.3V L4 DFE252010P-2R2M=P2 C50 GND C47 EN 10uF/6.3V 1uF/6.3V 100nF/10V RT8059 Vfb=0.6V GND GND 6.8K/1% **FRIENDLY** NanoPi\_R1S Size Document Number A3 08:Power-02 Date: Tuesday, January Rev V1.0 1908

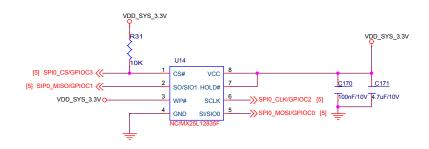
## **TF Card, EEPROM, Flash, USB Host**



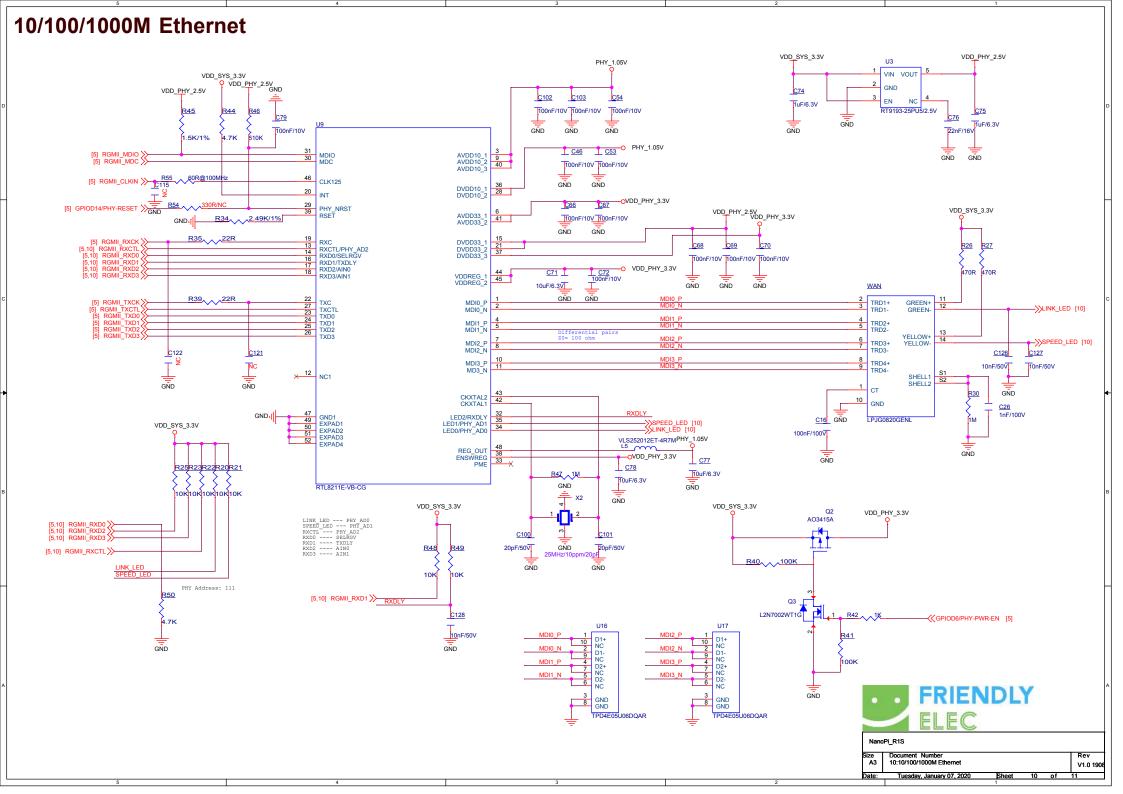


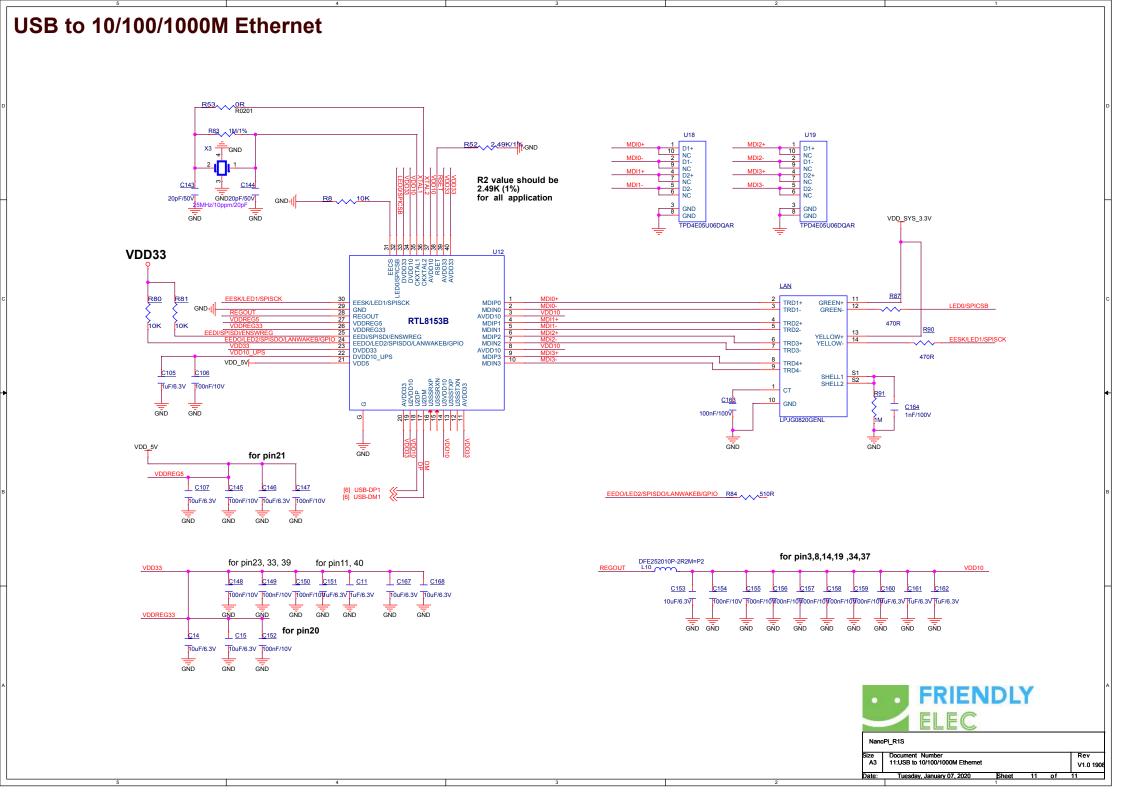
#### EEPROM with MAC Address



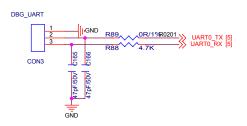


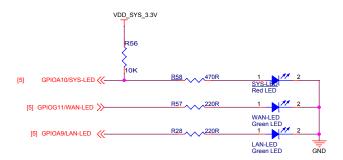


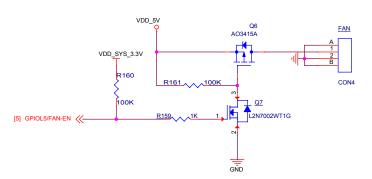


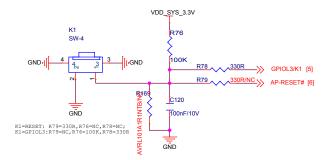


# DBG\_UART,LEDs,KEY





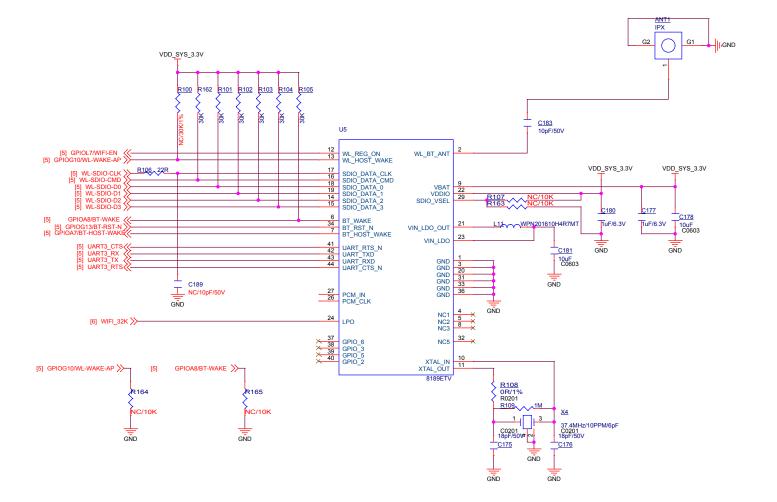








Wi-Fi



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Size A3	Document Number 13:Wi-Fi	Rev V1.0
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