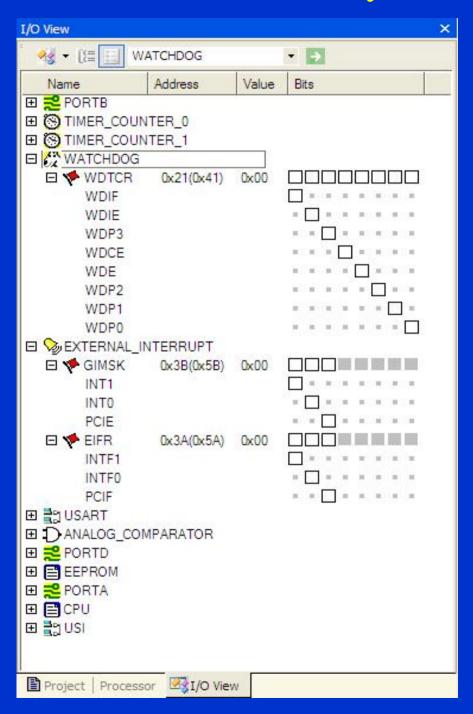
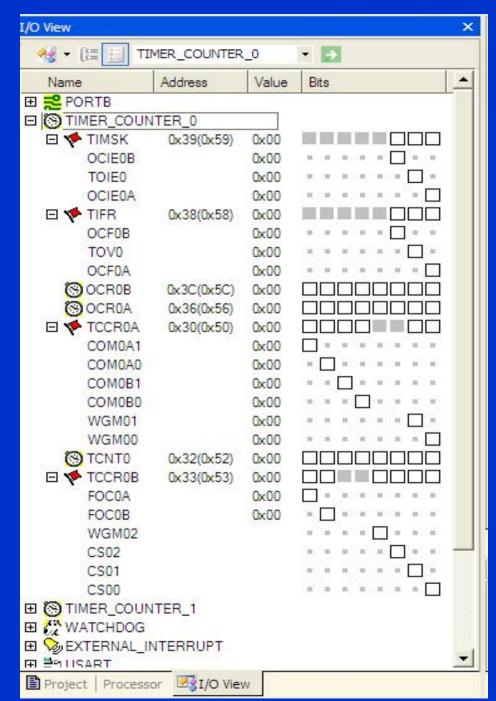
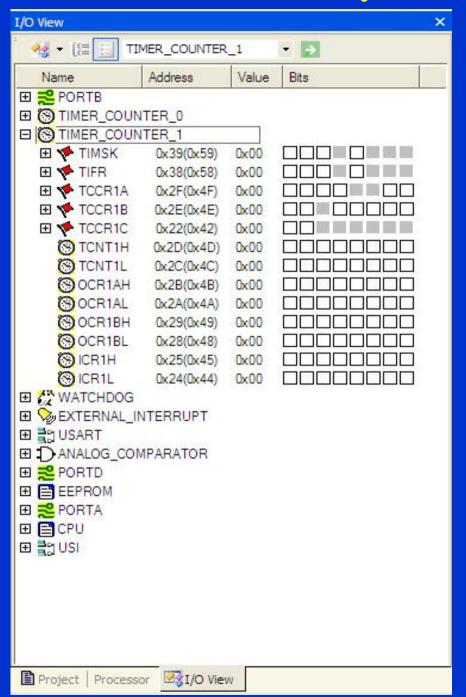


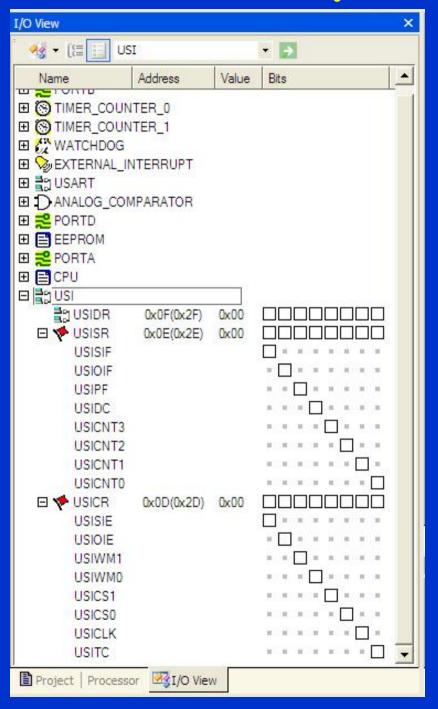
√ √ √ √ √ √ √ √ √ √ √ √ √ √ √ √ √ √ √	יט		- 5
Name	Address	Value	Bits
E CPU			
⊞ SREG	0x3F(0x5F)	0x00	
□ SPL	0x3D(0x5D)	0x00	
■ SPMCSR	0x37(0x57)	0x00	
☐ ☐ MCUCR	0x35(0x55)	0x00	
PUD			
SM1			* - * * * * *
SE			
SM0			* * *
ISC11			* * * * 🗆 * * *
ISC10			* * * * * * *
ISC01			
ISC00			* * * * * *
☐ ☐ CLKPR	0x26(0x46)	0x00	
CLKPCE			
CLKPS3			
CLKPS2			* * * * * * * *
CLKPS1			
CLKPS0			* * * * * * *
☐ ☐ MCUSR	0x34(0x54)	0x00	
WDRF			* * * * * * * *
BORF			
EXTRF			* * * * *
PORF			
□ OSCCAL	0x31(0x51)	0x00	
⊕ GTCCR	0x23(0x43)	0x00	
■ PCMSK	0x20(0x40)	0x00	
□ GPIOR2	0x15(0x35)	0x00	
□ GPIOR1	0x14(0x34)	0x00	
□ GPIOR0	0x13(0x33)	0x00	



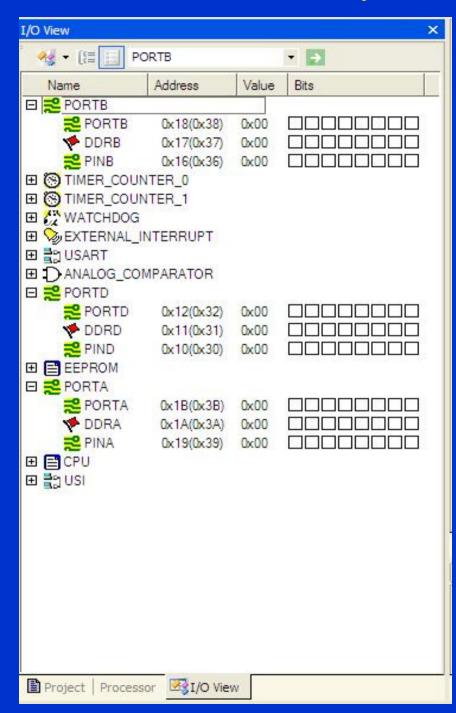




I/O View			×
* 🚜 🕶 📳 TII	MER_COUNTER	_1	· >
Name	Address	Value	Bits
☐ S TIMER_COUN	ITER_1		
□ ▼ TIMSK	0x39(0x59)	0x00	
TOIE1			
OCIE1A			
OCIE1B			
ICIE1			🗆
□ 🌾 TIFR	0x38(0x58)	0x00	
TOV1			
OCF1A			
OCF1B			H = H H = H
ICF1			* * * *
□ ▼ TCCR1A	0x2F(0x4F)	0x00	
COM1A1			
COM1A0			
COM1B1			H H H H H H H H
COM1B0			
WGM11			
WGM10			× × ×
□ ▼ TCCR1B	0x2E(0x4E)	0x00	
ICNC1			
ICES1			H: OFFICE HONECONCER
WGM13			
WGM12			
CS12			
CS11			
CS10			* * *
□ V TCCR1C	0x22(0x42)	0x00	
FOC1A			
FOC1B			* - * * * * * *
TCNT1H		0x00	
TONT1	P3C/IP4C/	n-nn	
Project Process	or ws 1/O Viet	N	



I/O View				×
* 🚜 + [{≣🗒 US	ART		* 3	
Name	Address	Value	Bits	_
□ 📆 USART				
≣g UDR	0x0C(0x2C)	0x00		
□ 🎺 UCSRA	0x0B(0x02B)	0x00		
RXC				
TXC		0x00		
UDRE		0x00		
FE			* * * * * * *	
DOR				
UPE				
U2X				
MPCM				
□ 🎺 UCSRB	0x0A(0x02A)	0x00		
RXCIE				
TXCIE				
UDRIE				
RXEN		0x00		
TXEN		0x00		
UCSZ2				
RXB8		0x00		
TXB8				
□ 🎺 UCSRC	0x03(0x23)	0×00		
UMSEL				
UPM1			* * [] * * * * *	
UPM0				
USBS				
UCSZ1		0x00		
UCSZ0		0x00		
UCPOL	200222200220	020225		
≣g UBRRH	0x02(0x22)	0x00		
≣g UBRRL	0x09(0x29)	0x00		-
Project Processo	or 🛂 I/O Viev	v		
E Project Processo	-STO VIEV	*0		



I/O View				×	
46 → [{= [] ANALOG_COMPARATOR → []					
Name	Address	Value	Bits	•	
⊞ S TIMER_COUN	TER 0				
TIMER_COUN					
	_				
	TERRUPT				
⊞ 🚉 USART					
□ D ANALOG_CON					
□ : D∙ACSR	0x08(0x28)	0x00			
ACD			П		
ACBG					
ACO ACI					
ACIE					
ACIC					
ACIS1					
ACISO					
DDIDR	0x01(0x21)	0x00			
☐ EEPROM			<u> </u>		
EEAR	0x1E(0x3E)				
■ EEDR	0x1D(0x3D)				
□ ♥ EECR	0x1C(0x3C)	0x00			
EEPM1 EEPM0					
EERIE					
EEMPE					
EEPE					
EERE					
⊕ ≅ PORTA					
⊞ E CPU					
⊞ ≣g USI				•	
Project Processo	or 🛂 I/O View	N			