

# ATtiny2313 25-07-2007

I/O View

CPU

Name	Address	Value	Bits
ANALOG_COMPARATOR			
PORTD			
EEPROM			
PORTA			
CPU			
SREG	0x3F(0x5F)	0x00	<input type="checkbox"/> <input type="checkbox"/> <input type="checkbox"/> <input type="checkbox"/> <input type="checkbox"/> <input type="checkbox"/> <input type="checkbox"/> <input type="checkbox"/>
I			<input type="checkbox"/>
T			<input type="checkbox"/>
H			<input type="checkbox"/>
S		0x00	<input type="checkbox"/>
V			<input type="checkbox"/>
N		0x00	<input type="checkbox"/>
Z			<input type="checkbox"/>
C			<input type="checkbox"/>
SPL	0x3D(0x5D)	0x00	<input type="checkbox"/> <input type="checkbox"/> <input type="checkbox"/> <input type="checkbox"/> <input type="checkbox"/> <input type="checkbox"/> <input type="checkbox"/> <input type="checkbox"/>
SPMCSR	0x37(0x57)	0x00	<input type="checkbox"/> <input type="checkbox"/> <input type="checkbox"/> <input type="checkbox"/> <input type="checkbox"/> <input type="checkbox"/> <input type="checkbox"/> <input type="checkbox"/>
CTPB			<input type="checkbox"/>
RFLB			<input type="checkbox"/>
PGWRT			<input type="checkbox"/>
PGERS			<input type="checkbox"/>
SPMEN			<input type="checkbox"/>
MCUCR	0x35(0x55)	0x00	<input type="checkbox"/> <input type="checkbox"/> <input type="checkbox"/> <input type="checkbox"/> <input type="checkbox"/> <input type="checkbox"/> <input type="checkbox"/> <input type="checkbox"/>
CLKPR	0x26(0x46)	0x00	<input type="checkbox"/> <input type="checkbox"/> <input type="checkbox"/> <input type="checkbox"/> <input type="checkbox"/> <input type="checkbox"/> <input type="checkbox"/> <input type="checkbox"/>
MCUSR	0x34(0x54)	0x00	<input type="checkbox"/> <input type="checkbox"/> <input type="checkbox"/> <input type="checkbox"/> <input type="checkbox"/> <input type="checkbox"/> <input type="checkbox"/> <input type="checkbox"/>
OSCCAL	0x31(0x51)	0x00	<input type="checkbox"/> <input type="checkbox"/> <input type="checkbox"/> <input type="checkbox"/> <input type="checkbox"/> <input type="checkbox"/> <input type="checkbox"/> <input type="checkbox"/>
GTCCR	0x23(0x43)	0x00	<input type="checkbox"/> <input type="checkbox"/> <input type="checkbox"/> <input type="checkbox"/> <input type="checkbox"/> <input type="checkbox"/> <input type="checkbox"/> <input type="checkbox"/>
PCMSK	0x20(0x40)	0x00	<input type="checkbox"/> <input type="checkbox"/> <input type="checkbox"/> <input type="checkbox"/> <input type="checkbox"/> <input type="checkbox"/> <input type="checkbox"/> <input type="checkbox"/>
GPIOR2	0x15(0x35)	0x00	<input type="checkbox"/> <input type="checkbox"/> <input type="checkbox"/> <input type="checkbox"/> <input type="checkbox"/> <input type="checkbox"/> <input type="checkbox"/> <input type="checkbox"/>
GPIOR1	0x14(0x34)	0x00	<input type="checkbox"/> <input type="checkbox"/> <input type="checkbox"/> <input type="checkbox"/> <input type="checkbox"/> <input type="checkbox"/> <input type="checkbox"/> <input type="checkbox"/>
GPIOR0	0x13(0x33)	0x00	<input type="checkbox"/> <input type="checkbox"/> <input type="checkbox"/> <input type="checkbox"/> <input type="checkbox"/> <input type="checkbox"/> <input type="checkbox"/> <input type="checkbox"/>
USI			

Project | Processor | I/O View

I/O View

CPU

Name	Address	Value	Bits
CPU			
SREG	0x3F(0x5F)	0x00	<input type="checkbox"/> <input type="checkbox"/> <input type="checkbox"/> <input type="checkbox"/> <input type="checkbox"/> <input type="checkbox"/> <input type="checkbox"/> <input type="checkbox"/>
SPL	0x3D(0x5D)	0x00	<input type="checkbox"/> <input type="checkbox"/> <input type="checkbox"/> <input type="checkbox"/> <input type="checkbox"/> <input type="checkbox"/> <input type="checkbox"/> <input type="checkbox"/>
SPMCSR	0x37(0x57)	0x00	<input type="checkbox"/> <input type="checkbox"/> <input type="checkbox"/> <input type="checkbox"/> <input type="checkbox"/> <input type="checkbox"/> <input type="checkbox"/> <input type="checkbox"/>
MCUCR	0x35(0x55)	0x00	<input type="checkbox"/> <input type="checkbox"/> <input type="checkbox"/> <input type="checkbox"/> <input type="checkbox"/> <input type="checkbox"/> <input type="checkbox"/> <input type="checkbox"/>
PUD			<input type="checkbox"/>
SM1			<input type="checkbox"/>
SE			<input type="checkbox"/>
SM0			<input type="checkbox"/>
ISC11			<input type="checkbox"/>
ISC10			<input type="checkbox"/>
ISC01			<input type="checkbox"/>
ISC00			<input type="checkbox"/>
CLKPR	0x26(0x46)	0x00	<input type="checkbox"/> <input type="checkbox"/> <input type="checkbox"/> <input type="checkbox"/> <input type="checkbox"/> <input type="checkbox"/> <input type="checkbox"/> <input type="checkbox"/>
CLKPCE			<input type="checkbox"/>
CLKPS3			<input type="checkbox"/>
CLKPS2			<input type="checkbox"/>
CLKPS1			<input type="checkbox"/>
CLKPS0			<input type="checkbox"/>
MCUSR	0x34(0x54)	0x00	<input type="checkbox"/> <input type="checkbox"/> <input type="checkbox"/> <input type="checkbox"/> <input type="checkbox"/> <input type="checkbox"/> <input type="checkbox"/> <input type="checkbox"/>
WDRF			<input type="checkbox"/>
BORF			<input type="checkbox"/>
EXTRF			<input type="checkbox"/>
PORF			<input type="checkbox"/>
OSCCAL	0x31(0x51)	0x00	<input type="checkbox"/> <input type="checkbox"/> <input type="checkbox"/> <input type="checkbox"/> <input type="checkbox"/> <input type="checkbox"/> <input type="checkbox"/> <input type="checkbox"/>
GTCCR	0x23(0x43)	0x00	<input type="checkbox"/> <input type="checkbox"/> <input type="checkbox"/> <input type="checkbox"/> <input type="checkbox"/> <input type="checkbox"/> <input type="checkbox"/> <input type="checkbox"/>
PCMSK	0x20(0x40)	0x00	<input type="checkbox"/> <input type="checkbox"/> <input type="checkbox"/> <input type="checkbox"/> <input type="checkbox"/> <input type="checkbox"/> <input type="checkbox"/> <input type="checkbox"/>
GPIOR2	0x15(0x35)	0x00	<input type="checkbox"/> <input type="checkbox"/> <input type="checkbox"/> <input type="checkbox"/> <input type="checkbox"/> <input type="checkbox"/> <input type="checkbox"/> <input type="checkbox"/>
GPIOR1	0x14(0x34)	0x00	<input type="checkbox"/> <input type="checkbox"/> <input type="checkbox"/> <input type="checkbox"/> <input type="checkbox"/> <input type="checkbox"/> <input type="checkbox"/> <input type="checkbox"/>
GPIOR0	0x13(0x33)	0x00	<input type="checkbox"/> <input type="checkbox"/> <input type="checkbox"/> <input type="checkbox"/> <input type="checkbox"/> <input type="checkbox"/> <input type="checkbox"/> <input type="checkbox"/>

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**I/O View**

WATCHDOG

Name	Address	Value	Bits
PORTB			
TIMER_COUNTER_0			
TIMER_COUNTER_1			
WATCHDOG			
WDTCSR	0x21(0x41)	0x00	□ □ □ □ □ □ □ □
WDIF			□ . . . . .
WDIE			. □ . . . . .
WDP3			. . □ . . . .
WDCE			. . . □ . . .
WDE			. . . . □ . .
WDP2			. . . . . □ .
WDP1			. . . . . . □
WDP0			. . . . . . . □
EXTERNAL_INTERRUPT			
GIMSK	0x3B(0x5B)	0x00	□ □ □ □ □ □ □ □
INT1			□ . . . . .
INT0			. □ . . . . .
PCIE			. . □ . . . .
EIFR	0x3A(0x5A)	0x00	□ □ □ □ □ □ □ □
INTF1			□ . . . . .
INTF0			. □ . . . . .
PCIF			. . □ . . . .
USART			
ANALOG_COMPARATOR			
PORTD			
EEPROM			
PORTA			
CPU			
USI			

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**I/O View**

TIMER\_COUNTER\_0

Name	Address	Value	Bits
PORTB			
TIMER_COUNTER_0			
TIMSK	0x39(0x59)	0x00	□ □ □ □ □ □ □ □
OCIE0B		0x00	. . . . . □ . .
TOIE0		0x00	. . . . . . □ .
OCIE0A		0x00	. . . . . . . □
TIFR	0x38(0x58)	0x00	□ □ □ □ □ □ □ □
OCF0B		0x00	. . . . . □ . .
TOV0		0x00	. . . . . . □ .
OCF0A		0x00	. . . . . . . □
OCR0B	0x3C(0x5C)	0x00	□ □ □ □ □ □ □ □
OCR0A	0x36(0x56)	0x00	□ □ □ □ □ □ □ □
TCCR0A	0x30(0x50)	0x00	□ □ □ □ □ □ □ □
COM0A1		0x00	□ . . . . .
COM0A0		0x00	. □ . . . . .
COM0B1		0x00	. . □ . . . .
COM0B0		0x00	. . . □ . . .
WGM01		0x00	. . . . . □ . .
WGM00		0x00	. . . . . . □ .
TCNT0	0x32(0x52)	0x00	□ □ □ □ □ □ □ □
TCCR0B	0x33(0x53)	0x00	□ □ □ □ □ □ □ □
FOC0A		0x00	□ . . . . .
FOC0B		0x00	. □ . . . . .
WGM02			. . . . . □ . .
CS02			. . . . . . □ .
CS01			. . . . . . . □
CS00			. . . . . . . □
TIMER_COUNTER_1			
WATCHDOG			
EXTERNAL_INTERRUPT			
USART			

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I/O View

TIMER\_COUNTER\_1

Name	Address	Value	Bits
PORTB			
TIMER_COUNTER_0			
TIMER_COUNTER_1			
TIMSK	0x39(0x59)	0x00	8 bits
TIFR	0x38(0x58)	0x00	8 bits
TCCR1A	0x2F(0x4F)	0x00	8 bits
TCCR1B	0x2E(0x4E)	0x00	8 bits
TCCR1C	0x22(0x42)	0x00	8 bits
TCNT1H	0x2D(0x4D)	0x00	8 bits
TCNT1L	0x2C(0x4C)	0x00	8 bits
OCR1AH	0x2B(0x4B)	0x00	8 bits
OCR1AL	0x2A(0x4A)	0x00	8 bits
OCR1BH	0x29(0x49)	0x00	8 bits
OCR1BL	0x28(0x48)	0x00	8 bits
ICR1H	0x25(0x45)	0x00	8 bits
ICR1L	0x24(0x44)	0x00	8 bits
WATCHDOG			
EXTERNAL_INTERRUPT			
USART			
ANALOG_COMPARATOR			
PORTD			
EEPROM			
PORTA			
CPU			
USI			

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I/O View

TIMER\_COUNTER\_1

Name	Address	Value	Bits
TIMSK	0x39(0x59)	0x00	8 bits
TOIE1			1 bit
OCIE1A			1 bit
OCIE1B			1 bit
ICIE1			1 bit
TIFR	0x38(0x58)	0x00	8 bits
TOV1			1 bit
OCF1A			1 bit
OCF1B			1 bit
ICF1			1 bit
TCCR1A	0x2F(0x4F)	0x00	8 bits
COM1A1			1 bit
COM1A0			1 bit
COM1B1			1 bit
COM1B0			1 bit
WGM11			1 bit
WGM10			1 bit
TCCR1B	0x2E(0x4E)	0x00	8 bits
ICNC1			1 bit
ICES1			1 bit
WGM13			1 bit
WGM12			1 bit
CS12			1 bit
CS11			1 bit
CS10			1 bit
TCCR1C	0x22(0x42)	0x00	8 bits
FOC1A			1 bit
FOC1B			1 bit
TCNT1H	0x2D(0x4D)	0x00	8 bits
TCNT1L	0x2C(0x4C)	0x00	8 bits

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I/O View

USI

Name	Address	Value	Bits
TIMER_COUNTER_0			
TIMER_COUNTER_1			
WATCHDOG			
EXTERNAL_INTERRUPT			
USART			
ANALOG_COMPARATOR			
PORTD			
EEPROM			
PORTA			
CPU			
USI			
USIDR	0x0F(0x2F)	0x00	
USISR	0x0E(0x2E)	0x00	
USISIF			
USIOIF			
USIPF			
USIDC			
USICNT3			
USICNT2			
USICNT1			
USICNT0			
USICR	0x0D(0x2D)	0x00	
USISIE			
USIOIE			
USIWM1			
USIWM0			
USICS1			
USICS0			
USICLK			
USITC			

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I/O View

USART

Name	Address	Value	Bits
USART			
UDR	0x0C(0x2C)	0x00	
UCSRA	0x0B(0x2B)	0x00	
RXC			
TXC		0x00	
UDRE		0x00	
FE			
DOR			
UPE			
U2X			
MPCM			
UCSRB	0x0A(0x2A)	0x00	
RXCIE			
TXCIE			
UDRIE			
RXEN		0x00	
TXEN		0x00	
UCSZ2			
RXB8		0x00	
TXB8			
UCSRC	0x03(0x23)	0x00	
UMSEL			
UPM1			
UPM0			
USBS			
UCSZ1		0x00	
UCSZ0		0x00	
UCPOL			
UBRRH	0x02(0x22)	0x00	
UBRRL	0x09(0x29)	0x00	

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I/O View

PORTB

Name	Address	Value	Bits
PORTB	0x18(0x38)	0x00	<input type="checkbox"/> <input type="checkbox"/> <input type="checkbox"/> <input type="checkbox"/> <input type="checkbox"/> <input type="checkbox"/> <input type="checkbox"/> <input type="checkbox"/>
DDRB	0x17(0x37)	0x00	<input type="checkbox"/> <input type="checkbox"/> <input type="checkbox"/> <input type="checkbox"/> <input type="checkbox"/> <input type="checkbox"/> <input type="checkbox"/> <input type="checkbox"/>
PINB	0x16(0x36)	0x00	<input type="checkbox"/> <input type="checkbox"/> <input type="checkbox"/> <input type="checkbox"/> <input type="checkbox"/> <input type="checkbox"/> <input type="checkbox"/> <input type="checkbox"/>
TIMER_COUNTER_0			
TIMER_COUNTER_1			
WATCHDOG			
EXTERNAL_INTERRUPT			
USART			
ANALOG_COMPARATOR			
PORTD			
PORTD	0x12(0x32)	0x00	<input type="checkbox"/> <input type="checkbox"/> <input type="checkbox"/> <input type="checkbox"/> <input type="checkbox"/> <input type="checkbox"/> <input type="checkbox"/> <input type="checkbox"/>
DDRD	0x11(0x31)	0x00	<input type="checkbox"/> <input type="checkbox"/> <input type="checkbox"/> <input type="checkbox"/> <input type="checkbox"/> <input type="checkbox"/> <input type="checkbox"/> <input type="checkbox"/>
PIND	0x10(0x30)	0x00	<input type="checkbox"/> <input type="checkbox"/> <input type="checkbox"/> <input type="checkbox"/> <input type="checkbox"/> <input type="checkbox"/> <input type="checkbox"/> <input type="checkbox"/>
EEPROM			
PORTA			
PORTA	0x1B(0x3B)	0x00	<input type="checkbox"/> <input type="checkbox"/> <input type="checkbox"/> <input type="checkbox"/> <input type="checkbox"/> <input type="checkbox"/> <input type="checkbox"/> <input type="checkbox"/>
DDRA	0x1A(0x3A)	0x00	<input type="checkbox"/> <input type="checkbox"/> <input type="checkbox"/> <input type="checkbox"/> <input type="checkbox"/> <input type="checkbox"/> <input type="checkbox"/> <input type="checkbox"/>
PINA	0x19(0x39)	0x00	<input type="checkbox"/> <input type="checkbox"/> <input type="checkbox"/> <input type="checkbox"/> <input type="checkbox"/> <input type="checkbox"/> <input type="checkbox"/> <input type="checkbox"/>
CPU			
USI			

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I/O View

ANALOG\_COMPARATOR

Name	Address	Value	Bits
PORTB			
TIMER_COUNTER_0			
TIMER_COUNTER_1			
WATCHDOG			
EXTERNAL_INTERRUPT			
USART			
ANALOG_COMPARATOR			
ACSR	0x08(0x28)	0x00	<input type="checkbox"/> <input type="checkbox"/> <input type="checkbox"/> <input type="checkbox"/> <input type="checkbox"/> <input type="checkbox"/> <input type="checkbox"/> <input type="checkbox"/>
ACD			<input type="checkbox"/> <input type="checkbox"/> <input type="checkbox"/> <input type="checkbox"/> <input type="checkbox"/> <input type="checkbox"/> <input type="checkbox"/> <input type="checkbox"/>
ACBG			<input type="checkbox"/> <input type="checkbox"/> <input type="checkbox"/> <input type="checkbox"/> <input type="checkbox"/> <input type="checkbox"/> <input type="checkbox"/> <input type="checkbox"/>
ACO			<input type="checkbox"/> <input type="checkbox"/> <input type="checkbox"/> <input type="checkbox"/> <input type="checkbox"/> <input type="checkbox"/> <input type="checkbox"/> <input type="checkbox"/>
ACI			<input type="checkbox"/> <input type="checkbox"/> <input type="checkbox"/> <input type="checkbox"/> <input type="checkbox"/> <input type="checkbox"/> <input type="checkbox"/> <input type="checkbox"/>
ACIE			<input type="checkbox"/> <input type="checkbox"/> <input type="checkbox"/> <input type="checkbox"/> <input type="checkbox"/> <input type="checkbox"/> <input type="checkbox"/> <input type="checkbox"/>
ACIC			<input type="checkbox"/> <input type="checkbox"/> <input type="checkbox"/> <input type="checkbox"/> <input type="checkbox"/> <input type="checkbox"/> <input type="checkbox"/> <input type="checkbox"/>
ACIS1			<input type="checkbox"/> <input type="checkbox"/> <input type="checkbox"/> <input type="checkbox"/> <input type="checkbox"/> <input type="checkbox"/> <input type="checkbox"/> <input type="checkbox"/>
ACIS0			<input type="checkbox"/> <input type="checkbox"/> <input type="checkbox"/> <input type="checkbox"/> <input type="checkbox"/> <input type="checkbox"/> <input type="checkbox"/> <input type="checkbox"/>
DIDR	0x01(0x21)	0x00	<input type="checkbox"/> <input type="checkbox"/> <input type="checkbox"/> <input type="checkbox"/> <input type="checkbox"/> <input type="checkbox"/> <input type="checkbox"/> <input type="checkbox"/>
PORTD			
EEPROM			
EEAR	0x1E(0x3E)	0x00	<input type="checkbox"/> <input type="checkbox"/> <input type="checkbox"/> <input type="checkbox"/> <input type="checkbox"/> <input type="checkbox"/> <input type="checkbox"/> <input type="checkbox"/>
EEDR	0x1D(0x3D)	0x00	<input type="checkbox"/> <input type="checkbox"/> <input type="checkbox"/> <input type="checkbox"/> <input type="checkbox"/> <input type="checkbox"/> <input type="checkbox"/> <input type="checkbox"/>
EEDR	0x1C(0x3C)	0x00	<input type="checkbox"/> <input type="checkbox"/> <input type="checkbox"/> <input type="checkbox"/> <input type="checkbox"/> <input type="checkbox"/> <input type="checkbox"/> <input type="checkbox"/>
EEPM1			<input type="checkbox"/> <input type="checkbox"/> <input type="checkbox"/> <input type="checkbox"/> <input type="checkbox"/> <input type="checkbox"/> <input type="checkbox"/> <input type="checkbox"/>
EEPM0			<input type="checkbox"/> <input type="checkbox"/> <input type="checkbox"/> <input type="checkbox"/> <input type="checkbox"/> <input type="checkbox"/> <input type="checkbox"/> <input type="checkbox"/>
EERIE			<input type="checkbox"/> <input type="checkbox"/> <input type="checkbox"/> <input type="checkbox"/> <input type="checkbox"/> <input type="checkbox"/> <input type="checkbox"/> <input type="checkbox"/>
EEMPE			<input type="checkbox"/> <input type="checkbox"/> <input type="checkbox"/> <input type="checkbox"/> <input type="checkbox"/> <input type="checkbox"/> <input type="checkbox"/> <input type="checkbox"/>
EEPE			<input type="checkbox"/> <input type="checkbox"/> <input type="checkbox"/> <input type="checkbox"/> <input type="checkbox"/> <input type="checkbox"/> <input type="checkbox"/> <input type="checkbox"/>
EERE			<input type="checkbox"/> <input type="checkbox"/> <input type="checkbox"/> <input type="checkbox"/> <input type="checkbox"/> <input type="checkbox"/> <input type="checkbox"/> <input type="checkbox"/>
PORTA			
CPU			
USI			

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