

The LNM Institute of Information Technology

DEPARTMENT OF ELECTRONICS & COMMUNICATION ENGINEERING

Analog Electronics Lab Time Table

Even Semester

Days	Monday	Tuesday	Wednesday	Thursday	Friday
Time					
Morning Slot		Pre Lab & Lab Record Evaluation (11:00AM – 01:00PM)	<u>Batch A1-A</u> 1. Dr. Gaurav Chatterjee 2. Dr. Nikhil Raj (9:00AM – 12:00PM)	<u>Batch A1-B</u> 1.Dr. Kusum Lata 2.Dr. Gaurav Chatterjee (9:30AM – 12:30PM)	
1:00PM-2:00 PM	Lunch Time				
Afternoon Slot					

Laboratory Superintendent: Mr. Pavan Kumar Sharma

Basic Electronics Lab Room No. - 1095