

The LNM Institute of Information Technology

DEPARTMENT OF ELECTRONICS & COMMUNICATION ENGINEERING
Basic Electronics Lab Time Table
Odd Semester

Days Time	Monday	Tuesday	Wednesday	Thursday	Friday
9:00AM - 12:00 PM		Pre Lab & Lab Record Evaluation	Batch B1	Batch B2	
1:00PM- 2:00 PM			Lunch Time		
2:00 PM- 5:00 PM		Pre Lab & Lab Record Evaluation	Batch A1	Batch A2	

Laboratory Superintendent: Mr. Pavan Kumar Sharma

Basic Electronics Lab Room No. - 1095