

Combined Slot Wise Class Time Table - ODD Semester 2018 - 19

		Monday		MONDAY								
	1st		08.00 - 09.00 - A	09.00 - 10.00 - B	10.00 - 11.00 - C	11.00 - 12.00 - D	12.00 - 01.00 - E					
			BE "A1" (IC ALL) (Deepak N, Gourav C) L1	CP "A1" IC ALL (Poonam G, Vibhor K, Sudheer S, Anukriti B) L1		CLP "A1" IC ALL (Pomita G, Amit N, G D Sharma, Subhayan B) L1	M - I "A1" IC ALL (Sunil G, Manish G, Dheerendra M, Trivedi H C) L1					
		BE "A2" IC ALL (Santosh S,Kanjalochoan J) L2	CP "A2" IC ALL (Poonam G, Vibhor K, Sudheer S, Anukriti B) L2		CLP "A2" IC ALL (Pomita G, Amit N, G D Sharma, Subhayan B) L2	M - I "A2" IC ALL (Sunil G, Manish G, Dheerendra M, Trivedi H C) L2						
	2nd	ECE										
		CCE	IDBMS CCE 1st 50% (Rajbir K, Nirmal S) L9	COA PC CCE 1st 50% (Preety S, Bharavi M) L9		AP PC CCE 1st 50% (Puneet J, Rimpny) L10						
	3rd	ECE										
		CCE					IDS PC CCE 1st 50% (Subrat D, Sakthi B) L10					
	4th	ECE	ITC PE-ECE (Joyeeta S, Divyang R) L4	MDC PE-ECE (R Gangopadhyay) L5	IIP PE ECE (Joyeeta S) L8	OPTI OE-UG ECE (Somnath M) L6	SBA OE ALL (Nabyendu D) L5					
						IBP OE ALL (Ashok G) L7	ECLSA OE ALL (Narendra K) L6					
		CCE			IIP PE CCE (Joyeeta S) L8	OPTI OE-UG CCE (Somnath M) L6	SBA OE ALL (Nabyendu D) L5					
						IBP OE ALL (Ashok G) L7	ECLSA OE ALL (Narendra K) L6					
		LAB		BE LAB B1-A (K L Jena) Room 1095								
Design lab -I B 51-100 (Gaurav C) Room 2095												
DSP LAB ECE 1-34 (Divyang R) Room 2081												
Digital Communication LAB CCE 69-102 (Purnendu K) Room 2094												
		1st	08.00 - 09.30 - F		09.30 - 11.00 - G		11:00 - 12:30 - H		12.30 - 01.00 FREE SLOT			
						TCE "A1" IC ALL (Manju D, Usha K, Payel P) L1						
					TCE "A2" IC ALL (Manju D, Usha K, Payel P) L2							
		2nd	ECE									
			CCE									
		3rd	ECE									
			CCE	SWE PC CCE 1st50% (Vikas B, R P Gorthi) L1								
		4th	ECE	RFE PE ECE+CCE (R Tomar) L3		BFSS OE ALL (A P Singh) L3						
				DVLSIC PE ECE+CCE (Kapil J) L4		SP OE- ALL (Sunil G) L4		MMTBD4 OE ALL (MMNF2) L6				
			CCE	CPSDS PE CCE (Rahul B) L6				TSAI OE ALL (Rajbala S) L3				
				DL PE CCE (Kshitiz V) L7				QCQI OE ALL (Rakesh T) L4				
			LAB				BE LAB B1 -B (Gaurav C) Room 1095					
					Design Lab C 1-50 (Deepak N) Room 2095							
Microprocessor and Interface Lab ECE 1-34 (Abhishek S)Room No 1073												
					DSP LAB CCE 1-34 (Divyang R) Room 2081							
DSP LAB ECE 69-102 (Navneet U) Room 2083												
Digital Communication LAB ECE 1-34 (Purnendu K) Room 2094												

Combined Slot Wise Class Time Table - ODD Semester 2018 - 19

WEDNESDAY	1st		08.00 - 09.00 - B	09.00 - 10.00 - C	10.00 - 11.00 - D	11.00 - 12.00 - E	12.00 - 01.00 - A	
			CP "A1" IC ALL (Poonam G, Vibhor K, Sudheer S, Anukriti B) L1					
		CP "A2" IC ALL (Poonam G, Vibhor K, Sudheer S, Anukriti B) L2		CLP "A2" IC ALL (Pomita G, Amit N, G D Sharma, Subhayan B) L2	M - I "A2" IC ALL (Sunil G, Manish G, Dheerendra M, Trivedi H C) L2	BE "A2" IC ALL (Santosh S,Kanjalochan J) L2		
	2nd	ECE						
		CCE	COA PC CCE 1st 50% (Preety S, Bharavi M) L9		AP PC CCE 1st 50% (Puneet J, RimpY) L10		IDBMS PC CCE 1st 50% (Rajbir K, Nirmal S) L9	
			AP PC CCE 2nd 50% (Puneet J, RimpY) L10	IDBMS PC CCE 2nd 50% (Rajbir K, Nirmal S) L9	COA PC CCE 2nd 50% (Preety S, Bharavi M) L9			
		ECE						
				AI PC CCE 1st 50% (Kshitiz V, Puneet J) L1		IDS PC CCE 1st 50% (Subrat D, Sakthi B) L10		
				IDS PC CCE 2nd 50% (Subrat D, Sakthi B) L2		AI PC CCE 2nd 50% (Kshitiz V, Puneet J) L9		
	4th	ECE	MDC PE-ECE (R Gangopadhyay, S Debnath) L5	IIP PE ECE (Joyeeta S) L8	OPTI OE-UG ALL (Somnath M) L6	SBA OE ALL (Nabyendu D) L5	ITC PE-ECE+CCE (Joyeeta S, Divyang R) L4	
			FNFT PE-ECE (R P Gorthi, Vikas B) L6		IBP OE ALL (Ashok G) L7	ECLSA OE ALL (Narendra K) L6		
					CIWW OE ALL (Payel P) L4			
		CCE		CC PE CCE (Hemant M) L6	OPTI OE-UG ALL (Somnath M) L6	SBA OE ALL (Nabyendu D) L5	DM PE-CCE (Subrat D) L3	
			MDC PE-CCE (R Gangopadhyay, S Debnath) L5	RTS PE CCE (Sunil K) L7	IBP OE ALL (Ashok G) L7	ECLSA OE ALL (Narendra K) L6	ITC PE-ECE+CCE (Joyeeta S, Divyang R) L4	
			FNFT PE-CCE (R P Gorthi, Vikas B) L6	IIP PE CCE (Joyeeta S) L8	CIWW OE ALL (Payel P) L4			
LAB			BE LAB B2-A(Santosh S) Room 1095					
			Design Lab C 101-150 (Deepak N) Room 2095					
			DSP LAB ECE 103-36 (Divyang R) Room 2081					
			Digital Communication LAB ECE 35-68 (Nikhil S) Room 2094					

THURSDAY	1st		08.00 - 09.30 - F		09.30 - 11.00 - G		11:00 - 12:30 - H		12.30 - 01.00 FREE SLOT		
					TCE "A1" IC ALL (Manju D, Usha K, Payel P) L1						
				TCE "A2" IC ALL (Manju D, Usha K, Payel P) L2							
	2nd										
		ECE									
			CCE	SWE PC CCE 1st50% (Vikas B, R P Gorthi) L1							
								SWE PC CCE 2nd 50% (Vikas B, R P Gorthi) L1			
	4th	ECE	RFE PE ECE+CCE (R Tomar) L3		BFSS OE ALL (A P Singh) L3		TSAI OE ALL (Rajbala S) L3				
			DVLSIC PE ECE+CCE (Kapil J) L4		ISP OE-UG ALL (Sunil G) L4		QCQI OE-UG ALL & PC-MSC PHY 3rd Sem (Rakesh T) L4				
		CCE	RFE PE ECE+CCE (R Tomar) L3		BFSS OE ALL (A P Singh) L3		TSAI OE ALL (Rajbala S) L3				
			CPSDS PE CCE (Rahul B) L6		ISP OE-UG ALL (Sunil G) L4		QCQI OE-UG ALL & PC-MSC PHY 3rd Sem (Rakesh T) L4				
			DL PE CCE (Kshitiz V) L7								
				BE LAB B2-B (Santosh S) Room 1095							
				DCS LAB ECE 1-35 (Kusum L) Room 2083							
DCS LAB ECE 36-50 & 246-260 (Kusum L) Room 2081											
Microprocessor and Interface Lab ECE 69-102 (Abhishek S) Room No 1073											
				Digital Communication LAB CCE 1-34 (Nikhil S) Room 2094							

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Friday	1st		08.00 - 09.00 - C	09.00 - 10.00 - D	10.00 - 11.00 - E	11.00 - 12.00 - A	12.00 - 01.00 - B	
				CLP "A1" IC ALL (Pomita G, Amit N, G D Sharma, Subhayan B) L1	M - I "A1" IC ALL (Sunil G, Manish G, Dheerendra M, Trivedi H C) L1	BE "A1" IC ALL (Deepak N, Gourav C) L1	CP "A1" IC ALL (Poonam G, Vibhor K, Sudheer S, Anukriti B) L1	
	2nd	ECE		CLP "A2" IC ALL (Pomita G, Amit N, G D Sharma, Subhayan B) L2	M - I "A2" IC ALL (Sunil G, Manish G, Dheerendra M, Trivedi H C) L2	BE "A2" IC ALL (Santosh S,Kanjalochoan J) L2	CP "A2" IC ALL (Poonam G, Vibhor K, Sudheer S, Anukriti B) L2	
		CCE				IDBMS PC CCE 1st 50% (Rajbir K, Nirmal S) L9	COA PC CCE 1st 50% (Preety S, Bharavi M) L9	
				IDBMS PC CCE 2nd 50% (Rajbir K, Nirmal S) L9	COA PC CCE 2nd 50% (Preety S, Bharavi M) L9			
	3rd	ECE						
		CCE		AI PC CCE 1st 50% (Kshitiz V, Puneet J) L1	AI PC CSE+CCE 2nd 50% (Kshitiz V, Puneet J) L9			
				IDS PC CCE 2nd 50% (Subrat D, Sakthi B) L2	IDS PC CSE+CCE 1st 50% (Subrat D, Sakthi B) L10			
	4th	ECE	IIP PE ECE & M. Tech. ECE (Joyeeta S) L8	CIWW OE ALL (Payel P) L4	SBA OE ALL (Nabyendu D) L5	ITC PE-ECE+CCE (Joyeeta S, Divyang R) L4	MDC PE-ECE+CCE (R Gangopadhyay) L5	
				OPTI OE-UG ALL & PC -MSC MTH 1st Sem (Somnath M) L6	ECLSA OE ALL (Narendra K) L6			
				IBP OE ALL (Ashok G) L7				
		CCE	IIP PE CCE (Joyeeta S) L8	CIWW OE ALL (Payel P) L4	SBA OE ALL (Nabyendu D) L5	ITC PE-ECE+CCE (Joyeeta S, Divyang R) L4	MDC PE-ECE+CCE (R Gangopadhyay) L5	
				OPTI OE-UG ALL & PC -MSC MTH 1st Sem (Somnath M) L6	ECLSA OE ALL (Narendra K) L6	DM PE- CCE (Subrat D) L3	FNFT PE CCE (R P Gorthi, Vikas B) L6	
			CC PE CCE (Hemant M) L6	IBP OE ALL (Ashok G) L7				
			RTS PE CCE (Sunil K) L7					
				DCS LAB A116-150 (Kusum L) Room No 2083				
				DCS LAB 151-180 (Kusum L) Room No 2081				

Combined Slot Wise Class Time Table - ODD Semester 2018 - 19

Monday	1st		01.00 - 02.00 - A	02.00 - 03.00 - B	03.00 - 04.00 - C	04.00 - 05.00 - D	05.00 - 06.00 - E	
			BE "A1" IC ALL (Deepak N, Gourav C) L1	CP "B1" IC ALL (Poonam G, Vibhor K, Sudheer S, Anukriti B) L1		CLP "B1" IC ALL (Pomita G, Amit N, G D Sharma, Subhayan B) L1	M - I "B1" IC ALL (Sunil G, Manish G, Dheerendra M, Trivedi H C) L1	
		BE "A2" IC ALL (Santosh S,Kanjalochan J) L2	CP "B2" IC ALL (Poonam G, Vibhor K, Sudheer S, Anukriti B) L2		CLP "B2" IC ALL (Pomita G, Amit N, G D Sharma, Subhayan B) L2	M - I "B2" IC ALL (Sunil G, Manish G, Dheerendra M, Trivedi H C) L2		
	2nd	ECE			SEMI PC ECE (K L Jena) L9	EEM PC ECE (R Tomar, Purnendu K) L9		
	CCE							
	3rd	ECE	DC PC ECE 1st 50%(S Debnath, Nikhil S) L9			MICROI PC ECE (Abhishek S) L9	DC PC ECE1st 50% (S Debnath, Nikhil S) L9	
			DSP PC ECE 2nd 50% (Divyang R, Navneet U) L10				DSP PC ECE 2nd 50% (Divyang R, Navneet U) L10	
		CCE	DC PC CCE 1st 50% (S Debnath, Nikhil S) L9				DSP PC CCE 1st 50% (Divyang R, Navneet U) L10	
DSP PC CCE 2nd 50% (Divyang R, Navneet U) L10						DC PC CCE 2nd 50% (S Debnath, Nikhil S) L9		
			BE LAB A1-A(Soumitra D) Room 1095					
			DSP LAB CCE 69-102(Joyeeta S) Room 2081					
			Digital Communication LAB CCE 35-68 (Santosh S) Room 2094					

Tuesday	1st		01.00 - 02.30 - F		02.30 - 04.00 - G		04:00 - 05:30 - H		05.30 - 06.00 FREE SLOT	06.00 - 07.00 EXTRA SLOT
					TCE "B1" IC ALL (Manju D, Usha K, Payel P) L1					
						TCE "B2" IC ALL (Manju D, Usha K, Payel P) L2				
	2nd	ECE	S&S PC ECE+CCE 1st 50% (Navneet U, Nikhil S) L1	M - III "B1" IC ECE (M K Kadalbajoo, MTHTBD 1, Pratibha G, MTHTBD 2) L9		S&S (PC ECE+CCE 1st 50%) (Navneet U, Nikhil S) L1				
			DCS PC ECE+CCE 2nd 50% (Santosh S, Kapil J) L2	M - III "B2" IC ECE (M K Kadalbajoo, MTHTBD 1, Pratibha G, MTHTBD 2) L10		DCS PC ECE+CCE 2nd 50% (Santosh S, Kapil J) L2				
		CCE		M - III "B1" IC CCE 1st 50% (M K Kadalbajoo, MTHTBD 1, Pratibha G, MTHTBD 2) L9						AP PC CCE 2nd 50%) (Mukesh J, Puneet J) L1
				M - III "B2" IC CCE 2nd 50% (M K Kadalbajoo, MTHTBD 1, Pratibha G, MTHTBD 2) L10						AP CCE 1st 50% (Mukesh J, Puneet J) L2
	3rd	ECE								
		CCE								
PTS IC ECE (Anu M) L10			EFE (CCE - Y16) (S S Nehra) L3							
4th	ECE									
	CCE									
LAB				BE LAB A1-B (Gourav C) Room 1095						
				Microprocessor and Interface Lab ECE103-136 (Abhishek S)						
				DSP LAB ECE 35-68 (Divyang R) Room						
				Digital Communication LAB ECE 69-102 (Soumitra D) Room 2094						

Combined Slot Wise Class Time Table - ODD Semester 2018 - 19

Wednesday	1st		01.00 - 02.00 - B	02.00 - 03.00 - C		03.00 - 04.00 - D	04.00 - 05.00 - E	05.00 - 06.00 - A		06.00 - 07.00 EXTRA SLOT		
			CP "B1" IC ALL (Poonam G, Vibhor K, Sudheer S, Anukriti B) L1	() L1		CLP "B1" IC ALL (Pomita G, Amit N, G D Sharma, Subhayan B) L1	M - I "B1" IC ALL (Sunil G, Manish G, Dheerendra M, Trivedi H C) L1	BE "A1" IC ALL (Deepak N, Gourav C) L1				
		CP "B2" IC ALL (Poonam G, Vibhor K, Sudheer S, Anukriti B) L2	() L2		CLP "B2" IC ALL (Pomita G, Amit N, G D Sharma, Subhayan B) L2	M - I "B2" IC ALL (Sunil G, Manish G, Dheerendra M, Trivedi H C) L2	BE "A2" IC ALL (Santosh S,Kanjalochoan J) L2					
	2nd	ECE		SEMI PC ECE (K L Jena) L9			EEM PC ECE (R Tomar, Purnendu K) L9					
		CCE								AP PC CCE 2nd 50% (Mukesh J, Puneet J) L1		
										AP PC CCE 1st 50% (Mukesh J, Puneet J) L2		
	3rd	ECE	DC PC ECE 1st 50% (S Debnath, Nikhil S) L9				MICROI PC ECE (Abhishek S) L10		DC PC ECE 1st 50% (S Debnath, Nikhil S) L9			
			DSP PC ECE2nd 50% (Divyang R, Navneet U) L10						DSP PC ECE 2nd 50% (Divyang R, Navneet U) L10			
		CCE	DC PC CCE 1st 50% (S Debnath, Nikhil S) L9						DSP PC CCE 1st 50% (Divyang R, Navneet U) L10			
			DSP PC CCE 2nd 50% (Divyang R, Navneet U) L10						DC PC CCE 2nd 50% (S Debnath, Nikhil S) L9			
	4th	ECE										
		CCE										
	LAB			BE LAB A2 A (Gourav C) Room 1095								
				DSP LAB CCE 35-68(Joyeeta S) Room 2081								

Thursday			01.00 - 02.30 - F		02.30 - 04.00 - G		04:00 - 05:30 - H		5.30 - 6.00 FREE SLOT		6.00 - 07.00 EXTRA SLOT		
	2nd				TCE "B1" IC ALL (Manju D, Usha K, Payel P) L1								
					TCE "B2" IC ALL (Manju D, Usha K, Payel P) L2								
		ECE	S&S PC ECE 1st 50% (Navneet U, Nikhil S) L1		M - III "B1" IC ECE 1st 50% (M K Kadalbajoo, MTHTBD 1, Pratibha G, MTHTBD 2) L9		S&S PC ECE 1st 50% (Navneet U, Nikhil S) L1						
			DCS PC ECE 2nd 50% (Santosh S, Kapil J) L2		M - III "B2" IC ECE 2nd 50% (M K Kadalbajoo, MTHTBD 1, Pratibha G, MTHTBD 2) L10		DCS PC ECE 2nd 50% (Santosh S, Kapil J) L2						
	CCE	S&S PC CCE 1st 50% (Navneet U, Nikhil S) L1		M - III "B1" IC CCE 1st 50% (M K Kadalbajoo, MTHTBD 1, Pratibha G, MTHTBD 2) L9		S&S PC CCE 1st 50% (Navneet U, Nikhil S) L1				AP PC CSE+CCE 2nd 50% (Mukesh J, Puneet J) L1			
		DCS PC CCE 2nd 50% (Santosh S, Kapil J) L2		M - III "B2" IC CCE 2nd 50% (M K Kadalbajoo, MTHTBD 1, Pratibha G, MTHTBD 2) L10		DCS PC CCE 2nd 50% (Santosh S, Kapil J) L2				AP PC CSE+CCE 1st 50% (Mukesh J, Puneet J) L2			
	3rd	ECE	PTS IC ECE (Anu M) L10		EFE (CCE - Y16) (S S Nehra) L3								
		CCE											
	4th	ECE											
		CCE											
	LAB			BE LAB A2-B(K L Jena) Room 1095									
Microprocessor and Interface Lab ECE 35-68 (Abhishek S) Room 1073													
Digital Communication LAB ECE 103-137 (Soumitra D) Room 2094													
					PG LAB -I (Divyang R) Room 2081								

Combined Slot Wise Class Time Table - ODD Semester 2018 - 19

FRIDAY			01.00 - 02.00 - C	02.00 - 03.00 - D	03.00 - 04.00 - E	04.00 - 05.00 - A	05.00 - 06.00 - B	
				CLP "B1" IC ALL (Pomita G, Amit N, G D Sharma, Subhayan B) L1	M - I "B1" IC ALL (Sunil G, Manish G, Dheerendra M, Trivedi H C) L1	BE "A1" IC ALL (Deepak N, Gourav C) L1	CP "B1" IC ALL (Poonam G, Vibhor K, Sudheer S, Anukriti B) L1	
				CLP "B2" IC ALL (Pomita G, Amit N, G D Sharma, Subhayan B) L2	M - I "B2" IC ALL (Sunil G, Manish G, Dheerendra M, Trivedi H C) L2	BE "A2" IC ALL (Santosh S,Kanjalochan J) L2	CP "B2" IC ALL (Poonam G, Vibhor K, Sudheer S, Anukriti B) L2	
	2nd	ECE	SEMI PC ECE (K L Jena) L9				EEM PC ECE (R Tomar, Purnendu K) L9	
		CCE						
	3rd	ECE		DSP PC ECE 1st 50% (Divyang R, Navneet U) L10	MICROI PC ECE (Abhishek S) L10		DC PC ECE 1st 50% (S Debnath, Nikhil S) L9	
				DC PC ECE 2nd 50% (S Debnath, Nikhil S) L9			DSP PC ECE 2nd 50% (Divyang R, Navneet U) L10	
		CCE		DSP PC CCE 1st 50% (Divyang R, Navneet U) L10			DC PC CCE 1st 50% (S Debnath, Nikhil S) L9	
				DC PC CCE 2nd 50% (S Debnath, Nikhil S) L9			DSP PC CCE 2nd 50% (Divyang R, Navneet U) L10	
	4th	ECE						
		CCE						
	LAB			DCS LAB 181-215 (Kusum Lata) Room 2083				
			DCS LAB 216-245(Kusum Lata) Room 2081					