Q8 Draw and explain the block diagram of 8251.

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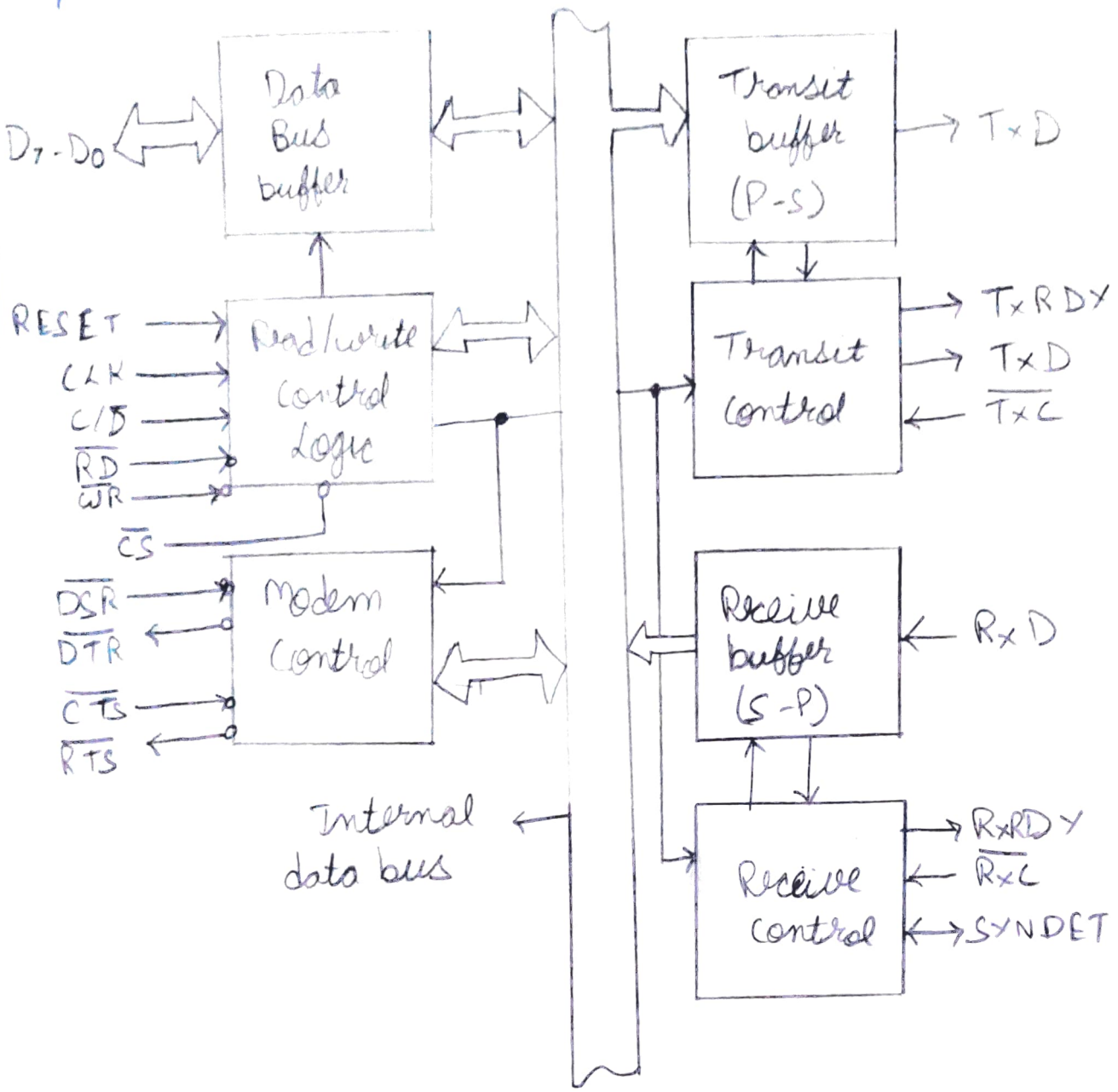

Block Diagram of 8251

It consists of the following blocks:-

1. **Data bus buffer** - This block helps in interfacing the internal data bus of 8251 to the system data bus. The data transmission is possible between 8251 and CPU by the data bus buffer block.

2. **Read/write control logic** - It is a control block for overall device. It controls the overall working by selecting the operation to be done. The operation selection depends upon input signals

as :-

$\overline{CS}$	C/$\overline{D}$	$\overline{RD}$	$\overline{WR}$	operation
1	x	x	x	Invalid
0	0	0	1	CPU ← data --- 8251
0	0	1	0	CPU --- data → 8251
0	1	0	1	CPU ← --- Status word 8251
0	1	1	0	CPU --- Control word → 8251

In this way, this unit selects one of the three registers - data buffer register, control register, status register.

3. **Modem control** – A device converts analog signals to digital signals and vice-versa and helps the computers to communicate over telephone lines or cable wires. The following are active-low pins of Modem.
   - DSR - Data Set Ready signal is an input signal.
   - DTR - Data terminal Ready is an output signal.
   - CTS - It is an input signal which controls the data transmit circuit.
   - RTS - It is an output signal which is used to set the status RTS.

4. **Transmit buffer** - This block is used for parallel to serial converter that receives a parallel byte for conversion into serial signal and further transmission onto the common channel.
   - TXD - It is an output signal, if its value is one, means transmitter will transmit the data.

5. **Transmit control** - This block is used to control the data transmission with the help of following pins:-
   - TXRDY - It means transmitter is ready to transmit data character.

- TxEMPTy - An output signal which indicates that TxEMPTy pin has transmitted all the data characters and transmitter is empty ~~now~~ now.
- TxC - An active-low input pin which controls the data transmission rate of transmitted data.

6. Receiver buffer - This block acts as a buffer for the received data.
   - RxD - An input signal which receives the data.

7. Receive control - This block controls the receiving data.
   - RxRDy - An input signal indicates that it is ready to receive the data.
   - RxC - An active-low input signal which controls the data transmission rate of received data.
   - SYNDET/BD - An input or output terminal. External synchronous mode - input terminal and asynchronous mode - output terminal.