Aim: To write a program that implements the target code generation

Code:

```
while (fscanf(fp1, "%s", op) != EOF) {
    i++;

    if (check_label(i)) {
        fprintf(fp2, "\nLABEL#%d:\n", i);
    }

    if (strcmp(op, "print") == 0) {
        fscanf(fp1, "%s", result);
        fprintf(fp2, "\toUT %s\n", result);
    }

    else if (strcmp(op, "goto") == 0) {
        fscanf(fp1, "%s %s", operand1, operand2);
        fprintf(fp2, "\toMP %s, LABEL#%s\n", operand1, operand2);
        label[no++] = atoi(operand2);
    }

    else if (strcmp(op, "[]=") == 0) {
        fscanf(fp1, "%s %s %s", operand1, operand2, result);
        fprintf(fp2, "\toTORE %s[%s], %s\n", operand1, operand2, result);
    }

    else if (strcmp(op, "uminus") == 0) {
        fscanf(fp1, "%s %s", operand1, result);
        fprintf(fp2, "\toTORE R1, %s\n", operand1);
        fprintf(fp2, "\toTORE R1, %s\n", result);
}
```

```
switch (op[0]) {
    case "":
        fscanf(fp1, "%s %s %s", operand1, operand2, result);
        fprintf(fp2, "\tLOAD %s, Ro\n", operand1);
        fprintf(fp2, "\tLOAD %s, Ro\n");
        fprintf(fp2, "\tLOAD %s, Ro\n");
        fprintf(fp2, "\tLOAD %s, Ro\n");
        fprintf(fp2, "\tLOAD %s, Ro\n"), operand2);
        fprintf(fp2, "\tLOAD %s, Ro\n"), operand1;
        fprintf(fp2, "\tLOAD %s, Ro\n"), operand2);
        fprintf(fp2, "\tLOAD %s, Ro\n"), operand2);
        fprintf(fp2, "\tLOAD %s, Ro\n", result);
        break;
    case '.':
        fscanf(fp1, "%s %s %s", operand1, operand2, result);
        fprintf(fp2, "\tLOAD %s, Ro\n", operand1);
        fprintf(fp2, "\tLOAD %s, Ro\n");
        fprintf(fp2, "\tLOAD %s, Ro\n");
        fprintf(fp2, "\tLOAD %s, Ro\n", operand2);
        fprintf(fp2, "\tLOAD %s, Ro\n", result);
        break;
    case '/':
        fscanf(fp1, "%s %s %s", operand1, operand2, result);
        fprintf(fp2, "\tLOAD %s, Ro\n", operand1);
        fprintf(fp2, "\tLOAD %s, Ro\n", operand2);
        fprintf(fp2, "\tLOAD %s, Ro\n"), operand2);
        fprintf(fp2, "\tLOAD %s, Ro\n"), operand2);
        fprintf(fp2, "\tLOAD %s, Ro\n"), operand2, result);
        break;
    case '%':
        fscanf(fp1, "%s %s %s", operand1, operand2, result);
        fprintf(fp2, "\tLOAD %s, Ro\n"), operand2);
        fprintf(fp2, "\tLOAD %s, Ro\n"), operand1);
        fprintf(fp2, "\tLOAD %s, Ro\n"), operand2);
        fprintf(fp2, "\tLOAD %s, Ro\n");
        forand fpar definition for
```

Input.txt

```
1 = a t1

2 = b t2

3 + t1 t2 t3

4 = t3 c

5 print c

6
```

Output:

```
Enter filename of the intermediate code: input.txt

Generated Target Code:

STORE a, t1
STORE b, t2
LOAD t1, R0
LOAD t2, R1
ADD R1, R0
STORE R0, t3
STORE t3, c
OUT c
```

Result: Thus, the program to implement target code generation has been successfully executed