

# Report Generated by Test Manager

**Title:** Test

**Author:**

**Date:** 29-Jun-2023 18:25:03

## Test Environment

Platform: PCWIN64

MATLAB: (R2022a)

## Summary

Name

Outcome

Duration  
(Seconds)

 [Master Model Test](#)



174.415

## Master\_Model\_Test


### Test Result Information

Result Type: Test Case Result  
Parent: None  
Start Time: 29-Jun-2023 18:33:39  
End Time: 29-Jun-2023 18:36:34  
Outcome: Passed

### Test Case Information

Name: Master\_Model\_Test  
Type: Simulation Test

### Aggregated Coverage Results

Analyzed Model	Sim Mode	Complexity	Decision	Condition	MCDC
 <a href="#">controllers/master</a>	Normal	29	82%	100%	100%

### Simulation

#### System Under Test Information

Model: controllers  
Harness: Master\_Model\_Test  
Harness Owner: controllers/master  
Release: Current  
Simulation Mode: normal  
Override SIL or PIL: 0  
Mode:  
Configuration Set: QuickStart\_50021\_1\_22\_18\_5\_17469  
Start Time: 0  
Stop Time: 300  
Checksum: 3269004829 3319928792 3092486435 3529799186  
Simulink Version: 10.5  
Model Version: 1.11  
Model Author: dmenr  
Date: Mon Jun 26 19:58:22 2023  
User ID: dmenr

Model Path:	C:\Users\dmenr\Downloads\matlab_hw\emb_final \mine\design\controllers.slx
Machine Name:	DESKTOP-T2B7E2C
Solver Name:	FixedStepDiscrete
Solver Type:	Fixed-Step
Fixed Step Size:	1
Simulation Start Time:	2023-06-29 17:33:42
Simulation Stop Time:	2023-06-29 17:36:16
Platform:	PCWIN64

#### Simulation Logs:

No data is logged for the model 'Master\_Model\_Test'.

'log\_type' is defined, but is never used in the Test Sequence block. [Delete this object.](#)

'log\_data' is defined, but is never used in the Test Sequence block. [Delete this object.](#)

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