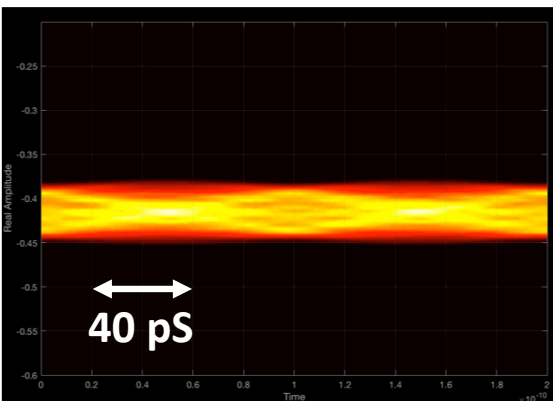
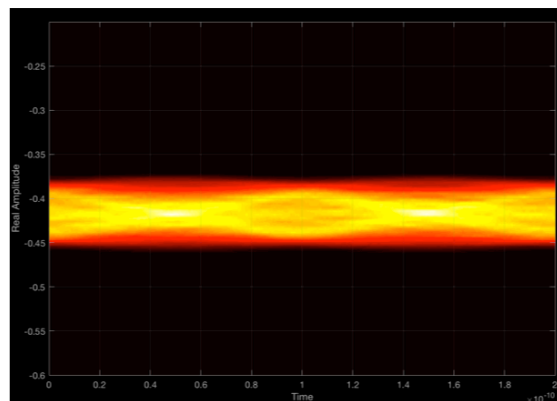


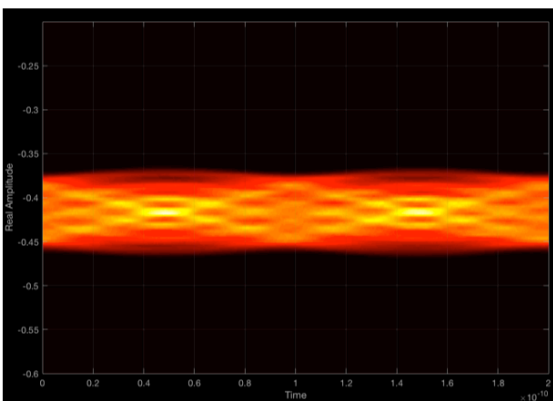
Output port 1



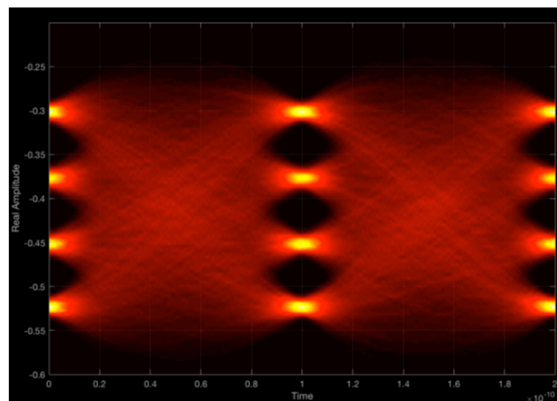
Output port 2



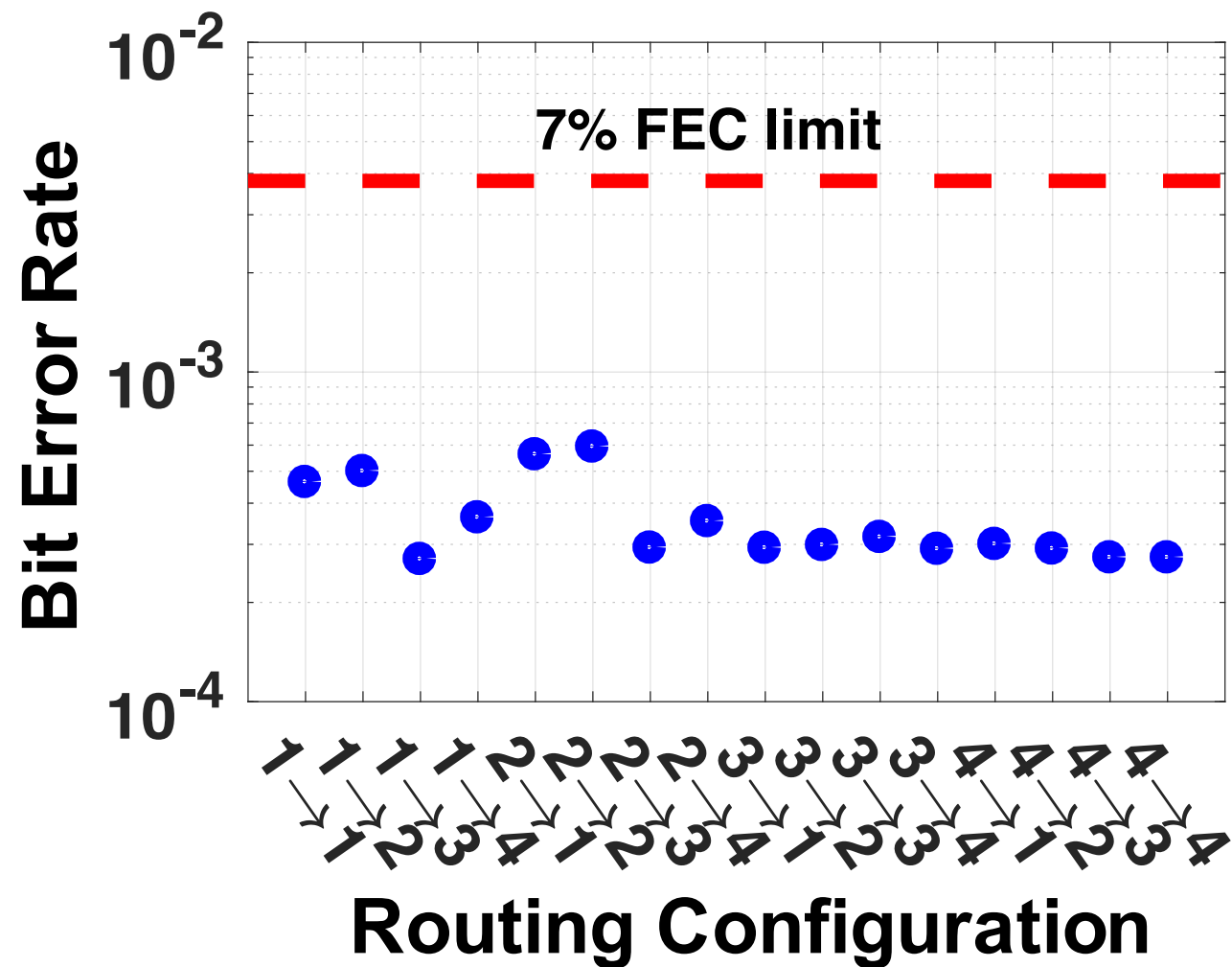
Output port 3



Output port 4



(a)



(b)