Module1

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2)

Value				
	0x080002EE 4002	DCW	0x4002	
0x08000379	Reset_Handler:			
0x20000224	0x080002F0 4806	LDR	r0,[pc,#24]	; @0x0800030C
0x00000000	0x080002F2 4780	BLX	r0	
0x08000DDD	0x080002F4 4806	LDR	r0,[pc,#24]	; @0x08000310
0x08000F30	0x080002F6 4700	BX	r0	
0x08000F30	NMI_Handler:			
0x00000000	0x080002F8 E7FE	B	NMI_Handler (0x080002F8)	
0x00000000	HardFault_Handler:			
0x00000000	0x080002FA E7FE	B	HardFault_Handler (0x080002FA)	
0x00000000	MemManage_Handler:			
0x00000000	0x080002FC E7FE	B	MemManage_Handler (0x080002FC)	
0x00000000	BusFault_Handler:			

3) 143 KB gpio code from hw 5

4)

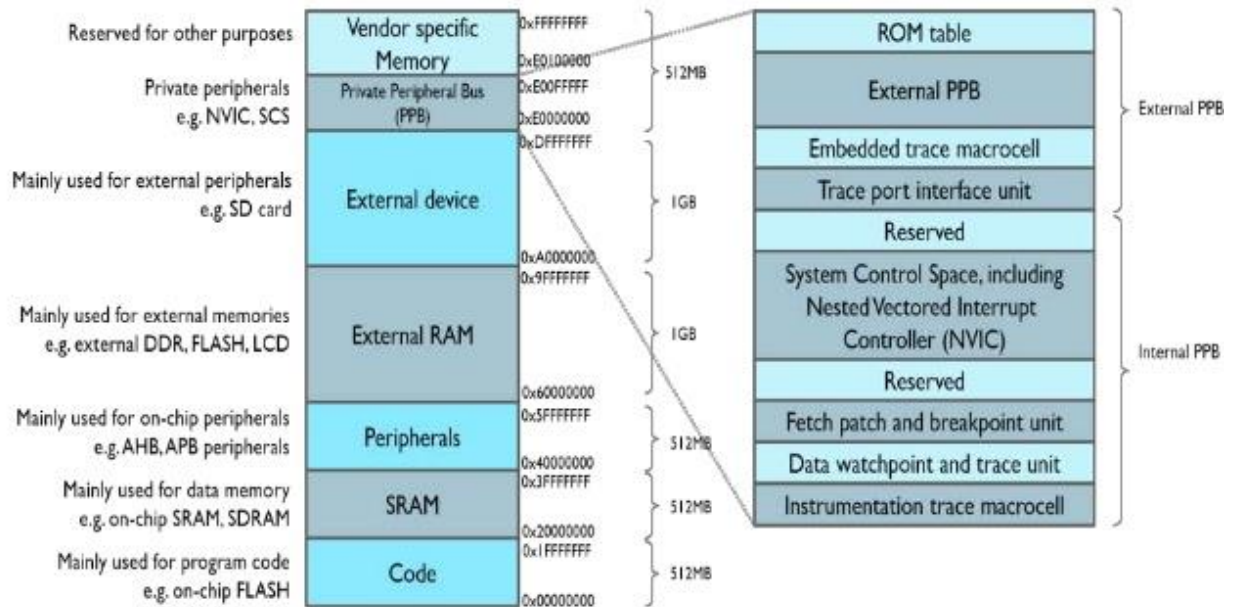

```
                          Hello World !
RTC example
Time as a basic string = Sat Mar  9 19:43:49 2019
 program running for 1 seconds.
Time as a basic string = Sat Mar  9 19:43:50 2019
 program running for 2 seconds.
Time as a basic string = Sat Mar  9 19:43:51 2019
 program running for 3 seconds.
Time as a basic string = Sat Mar  9 19:43:52 2019
 program running for 4 seconds.
Time as a basic string = Sat Mar  9 19:43:53 2019
 program running for 5 seconds.
```


Memory Usage

Type	Size	Max
Code (Flash)	28.4 kB	512.0 kB
Code Data	3.2 kB	n/a
RO Data (Flash)	3.2 kB	512.0 kB
RW Data (RAM)	0.2 kB	96.0 kB
ZI Data (RAM)	1.6 kB	96.0 kB
Debug	9.1 kB	n/a
ROM	31.8 kB	n/a
Flash	31.6 kB	512.0 kB
RAM	1.8 kB	96.0 kB

5)

The memory model for the Cortex M4 is similar to the M0 with the program code starting at 0X00000000 and the data code (SRAM) starting at 0x20000000. The interrupts are handled in the private peripheral bus which starts at 0xE0000000.

6)

```
Dhrystone time for 20511744 passes = 60.001 sek
This machine benchmarks at 341859 dhrystones/second
```

We obtain VAX DMIPS by dividing Dhrystones per second by 1757.

341859/1757 =194.57 DMIPS