

# G SATHISH

Quality Engineer – Device & Server Testing | Windows/Linux | Python Automation

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## PROFESSIONAL SUMMARY

Quality Engineer with experience in device testing, server validation, hardware enablement, and OS certification across Windows and Linux platforms. Skilled in functional & regression testing, compatibility analysis, defect lifecycle management, and Python-based automation. Strong foundation in BIOS, firmware, storage, RAID, networking components, and system-level validation.

## CORE SKILLS

- |                  |                      |                      |                     |               |
|------------------|----------------------|----------------------|---------------------|---------------|
| • Device Testing | • Functional Testing | • Regression Testing | • System Validation | • UAT         |
| • Windows Server | • GPU                | • Linux              | • BIOS              | • Firmware    |
| • RAID           | • Storage            | • Networking         | • Python Automation | • Basics of C |

## PROFESSIONAL EXPERIENCE

### Quality Engineer – Server & Device Certification | LTIMindtree | June 2024 – Present

- Performed device-level, functional, and regression testing on Windows & Linux servers.
- Validated BIOS, firmware, RAID controllers, NICs, GPUs, and storage components.
- Conducted system-level validation, interoperability checks, and UAT.
- Identified, documented, and tracked defects with structured RCA.
- Collaborated with development teams for quick defect resolution.
- Automated OS certification using Python, reducing manual effort.

## INTERNSHIP

### Data Engineering Intern – LTIMindtree | April 2023 – June 2023

- Developed Python-based ETL pipelines for data ingestion, cleaning, and transformation.
- Used SQL for complex data validation and manipulation.
- Performed data-quality checks ensuring accuracy and consistency.

## PROJECTS

### Design of Non-Volatile Full Adder using MTJ | Dec 2022 – Apr 2023

- Designed CMOS + MTJ-based full adder for near-zero leakage power.
- Compared MTJ-based designs with high-power CMOS circuits.
- Used LTspice for simulation and measurement of power and delay.

### Smart Power Management System | June 2022 – Dec 2022

- Built automatic ON/OFF smart power adapter based on battery percentage.
- Used Arduino, TRIAC, SMPS circuits, Arduino IDE & Python.

## EDUCATION

### Bachelor of Technology – Electronics & Communication Engineering

Madanapalle Institute of Technology and Science | CGPA: 8.87