

D:/Projects/Raspberrypi  
\_pico/pico\_freertos\_final  
/freertos\_pico2/pico\_freertos  
/FreeRTOS-Kernel/portable/ThirdParty  
/GCC/RP2350\_RISC-V/include/portContext.h

```
graph TD; A["D:/Projects/Raspberrypi_pico/pico_freertos_final/freertos_pico2/pico_freertos/FreeRTOS-Kernel/portable/ThirdParty/GCC/RP2350_RISC-V/include/portContext.h"] --> B[pico.h]; A --> C[hardware/regs/rvcsr.h]; A --> D[hardware/regs/intctrl.h]; A --> E[freertos_risc_v_chip_specific_extensions.h];
```

pico.h

hardware/regs/rvcsr.h

hardware/regs/intctrl.h

freertos\_risc\_v\_chip  
\_specific\_extensions.h