Programming Language (10) Making a compiler

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2 Implementing a minimum compiler for a C-like language

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Compilation Basics

2 Implementing a minimum compiler for a C-like language

From high-level programming languages to machine code

- there are *no structured control flows* (for, while, if, etc.); everything must be done by (conditional) jump instructions (≈ "goto" statement)
- an instruction can perform only a single operation, so nested expressions (e.g., a * x + b * y + c * z) must be broken down into a series of instructions
- a register \approx a variable, but
 - ▶ you have *only a fixed number of them*, so some values may have to be spilled on memory (esp. at function calls)
 - ▶ function parameters and return values are on predetermined registers (calling convention or Application Binary Interface)

Code generation by hand — introspecting "human compiler"

• ex: how to convert the following (which finds \sqrt{c} by the Newton method) into machine language

```
double sq(double c, long n) {
   double x = c;
   for (long i = 0; i < n; i++) {
      x = x / 2 + c / (x + x);
   }
   return x;
}</pre>
```

Step 1 — make all controls "goto"s

```
double sq(double c, long n) {
  double x = c;
  for (long i = 0; i < n; i++) {
    x = x / 2 + c / (x + x);
  }
  return x;
}</pre>
```

```
double sq(double c, long n) {
   double x = c;
   long i = 0;
   if (i >= n) goto Lend;
   Lstart:
        x = x / 2 + c / (2 * x);
   i++;
   if (i < n) goto Lstart;
   Lend:
   return x;
}</pre>
```

Step 2 — flatten all nested expressions to "C = A op B"

```
double sq(double c, long n) {
   double x = c;
   long i = 0;
   if (i >= n) goto Lend;
   Lstart:
    x = x / 2 + c / (2 * x);
   i++;
   if (i < n) goto Lstart;
   Lend:
   return x;
}</pre>
```

```
double sq3(double c, long n) {
      double x = c;
      long i = 0;
      if (!(i < n)) goto Lend;
   Lstart:
      double t0 = 2:
      double t1 = x / t0;
     double t2 = t0 * x;
     double t3 = c / t2:
10
     x = t1 + t3;
      i = i + 1:
11
      if (i < n) goto Lstart;
12
     Lend:
13
      return x;
14
15
```

Step 3 —assign "machine variables" (registers or memory) to variables

• note: cannot write floating point constants in instructions

```
/* c : xmm0, n : rdi */
   double sq3(double c, long n) {
     double x = c; /* x : xmm1 */
3
     long i = 0; /* i : rsi */
     if (!(i < n)) goto Lend;
    Lstart:
     double t0 = 2; /* t0 : xmm2 */
     double t1 = x / t0; /* t1 : xmm3 */
8
     double t2 = t0 * x; /* t2 : xmm4 */
     double t3 = c / t2; /* t3 : xmm5 */
10
     x = t1 + t3:
11
     i = i + 1:
12
     if (i < n) goto Lstart;
13
    Lend:
14
1.5
     return x;
16
```

Step 4 — convert them to machine instructions

```
/* c : xmm0, n : rdi */
    double sq3(double c, long n) {
   # double x = c; /*x:xmm1*/
   movasd %xmm0.%xmm1
    # long i = 0; /*i:rsi*/
    movq $0,%rsi
    .Lstart:
    # if (!(i < n)) goto Lend;
    cmpq %rdi, %rsi # n - i
    jle .Lend
10
11
    # double t0 = 2; /*t0:xmm2*/
    movasd .L2(%rip), %xmm2
12
    # double t1 = x / t0; /*t1:xmm3*/
13
    movasd %xmm1, %xmm3
14
15
    divq %xmm2,%xmm3
    # double t2 = t0 * x; /*t2:xmm4*/
16
17
    movasd %xmm0, %xmm4
    mulsd xmm2, %xmm4
18
```

```
# double t3 = c/t2: /*t3:xmm5*/
    movasd %xmm0, %xmm5
    divsd %xmm4,%xmm5
    # x = t1 + t3;
    movasd %xmm3, %xmm1
   addsd %xmm5.%xmm1
   # i = i + 1;
    addq $1,%rsi
    # if (i < n) goto Lstart;</pre>
10 cmpq %rdi, %rsi # n - i
   jl .Lstart
11
12
    .Lend:
    # return x;
13
14
    movq %xmm1,%xmm0
1.5
    ret
16
```

Things are more complex in general . . .

 \bullet we've liberally assigned registers to intermediate results, but

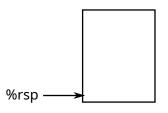
```
double x = c;    /* x : xmm1 */
Lstart:
if (!(i < n)) goto Lend;
double t0 = 2;    /* t0 : xmm2 */
double t1 = x / t0; /* t1 : xmm3 */
double t2 = t0 * x; /* t2 : xmm4 */
double t3 = c / t2; /* t3 : xmm5 */</pre>
```

- registers are finite (may run out)
- some registers are destroyed (i.e., values on them are lost) across a function call
- some instructions demand operands to be on specific registers (e.g., dividend of integer division must be on rax and rdx = rax and rdx are destroyed across an integer division)
- ullet you must use memory ("stack" region) as well

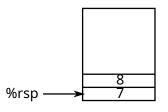
Register usage conventions (ABI)

- the first six integer/pointers arguments: rdi, rsi, rdx, rcx, r8, r9
- floating point number arguments: xmm0, xmm1, ...
- an integer/pointer return value : rax
- a floating point number return value : xmm0
- rsp: points the end of the stack upon function entry, which holds the return address
- callee-save registers: rbx, rbp, r12, r13, r14, r15, rsp (preserved across function calls → a function must save them before using (setting a value to) them)
- other registers are *caller-save* (a function must assume they are destroyed across function calls)
- see "general-purpose" registers in https://wiki.cdot.senecacollege.ca/wiki/X86_64_ Register_and_Instruction_Quick_Start

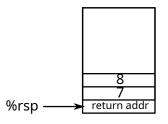
- long f() $\{ \ldots g(1,2,3,4,5,6,7,8); \ldots \}$
- during **f**



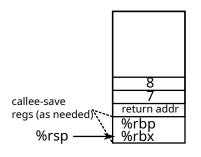
- long f() $\{ \ldots g(1,2,3,4,5,6,7,8); \ldots \}$
- right before "call g" rdi=1, rsi=2, rdx=3, rcx=4, r8=5, r9=6



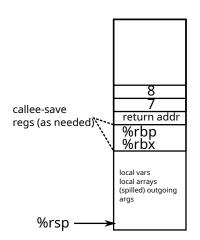
- long f() { ... g(1,2,3,4,5,6,7,8); ... }
- right after "call g" (when g started)



- long f() { ... g(1,2,3,4,5,6,7,8); ... }
- save callee-save registers g uses

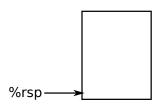


- long f() $\{ \ldots g(1,2,3,4,5,6,7,8); \ldots \}$
- ullet extend stack as needed to execute ${\tt g}$



A simplest general strategy for code generation

- in general,
 - there may be too many intermediate results to hold on registers
 - values used after a function call must be saved on memory (or callee-save registers)
 - ⇒ "always" using memory (stack) is the simplest strategy
- a register is used only "temporarily" to apply an instruction



A code generation based on the simple strategy

```
double integ(long n) {
   double x = 0;
   double dx = 1 / (double)n;
   double s = 0;
   for (long i = 0; i < n; i++) {
      s += f(x);
      x += dx;
   }
   return s * dx;
}</pre>
```

converting to "goto"s and "C = A op B"s

```
double integ(long n) {
1
      double x = 0;
      double t0 = 1:
3
      double t1 = (double)n;
4
      double dx = t0 / t1;
.5
      double s = 0:
6
      long i = 0;
      if (!(i < n)) goto Lend;
8
     Lstart:
9
10
      double t2 = f(x):
11
      s += t2:
12
      x += dx;
      i += 1;
13
      if (i < n) goto Lstart;
14
     Lend:
1.5
16
      double t3 = s * dx:
      return t3;
17
18
```

allocate memory slot for intermediate values

```
double integ(long n) { /* n : 0(%rsp) */
1
     double x = 0;
                          /* x : 8(%rsp) */
     double t0 = 1; /* t0 : 16(\% rsp) */
3
     double t1 = (double)n; /* t1 : 24(%rsp) */
4
     double dx = t0 / t1; /* dx : 32(%rsp) */
.5
     double s = 0; /* s : 40(\%rsp) */
6
                            /* i : 48(%rsp) */
     long i = 0;
     if (!(i < n)) goto Lend;
8
    Lstart:
10
     double t2 = f(x); /* t2 : \frac{56(\%rsp)}{} */
     s += t2:
11
12
     x += dx;
     i += 1;
13
     if (i < n) goto Lstart;
14
    Lend:
1.5
16
     double t3 = s * dx; /* t3 : 64(\% rsp) */
     return t3;
17
18
```

機械語 / Machine code

```
double integ(long n) {
                                                  movsd .LO(%rip), %xmm0
1
      subq $72,%rsp
                                                  movsd %xmm0,40(%rsp)
      /* n : 0(%rsp) */
                                                  # long i = 0;
      movq %rdi,0(%rsp)
                                                  /* i : 48(%rsp) */
      # double x = 0:
                                                  movg $0,48(%rsp)
      /* x : 8(%rsp)*/
                                                  # if (!(i < n)) goto Lend;
      movsd .LO(%rip), %xmmO
                                                  movq 0(%rsp),%rdi
      movsd %xmm0,8(%rsp)
                                                  cmpq 48(%rsp),%rdi # n - i
      # double t0 = 1:
                                                  jle .Lend
      /* t0 : 16(%rsp)*/
                                                .Lstart:
10
                                            10
      movsd .L1(%rip), %xmm0
                                                  # double t2 = f(x):
11
                                           11
      movsd %xmm0,16(%rsp)
                                                  /* t2 : 56(%rsp) */
12
                                           12
13
      # double t1 = (double)n:
                                           13
                                                  movq 8(%rsp), %rdi
      /* t1 : 24(%rsp)*/
                                                  call f
                                           14
1.5
      cvtsi2sdq 0(%rsp),%xmm0
                                           1.5
                                                  movg %rax,56(%rsp)
      movsd %xmm0,24(%rsp)
                                                  # s += t2:
16
                                           16
17
      # double dx = t0 / t1:
                                           17
                                                  movq 40(%rsp),%xmm0
      /* dx : 32(%rsp) */
                                                  addsd 56(%rsp),%xmm0
18
                                           18
      movsd 16(%rsp),%xmm0
                                                  movq %xmm0,40(%rsp)
19
                                           19
      divsd 24(%rsp),%xmm0
                                                  \# x += dx:
20
                                           20
      movsd %xmm0,32(%rsp)
                                                  movsd 8(%rsp),%xmm0
                                           21
      # double s = 0:
                                                  addsd 32(%rsp),%xmm0
                                           22
      /* s : 40(%rsp) */
                                                  movsd %xmm0,8(%rsp)
23
                                           23
                                                                               17/43
```

機械語 / Machine code

```
# i += 1:
1
      movq 48(%rsp),%rdi
      addq $1,%rdi
3
      movq %rdi,48(%rsp)
      # if (i < n) goto Lstart;</pre>
5
      movq 0(%rsp),%rdi
6
      cmpq 48(%rsp),%rdi # n - i
\gamma
      ig .Lstart
    .Lend:
9
      movsd 40(%rsp),%xmm0
10
      addsd 32(%rsp),%xmm0
11
      addsd %xmm0,64(%rsp)
12
      # return t3;
13
      addsd 64(%rsp),%xmm0
14
15
      ret
16
```

Contents

Compilation Basics

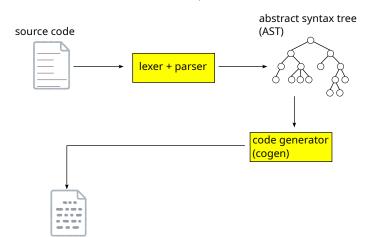
2 Implementing a minimum compiler for a C-like language

MinC ("Minimum C") spec overview

- this will be your final report if you choose option A
- all expressions have type long (8 byte integers)
 - no typedefs
 - ▶ no ints, floating point numbers, or pointers
 - everything is long, so type checks are unnecessary
- no global variables \Rightarrow
 - ► a program = list of function definitions
- function calls with C conventions, so you can call or be called by C functions compiled by ordinary compilers (e.g., gcc)
- supported complex statements are if, while and compound statement ({ ... }) only

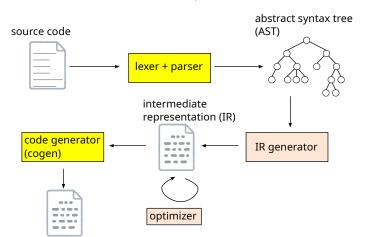
Structure of compilers

- Abstract Syntax Tree (AST) : data structure representing the program
- Intermediate Representatin (IR): common representation portable across multiple source/target languages



Structure of compilers

- Abstract Syntax Tree (AST) : data structure representing the program
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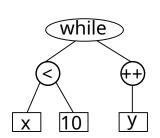
Structure of the program

- parser/
 - ▶ minc_grammar.y grammar definition
 - ▶ minc_to_xml.py minC → XML converter
- {ml,jl,go,rs}/minc/
 - ▶ minc_ast.?? abstract syntax tree (AST) definition
 - ▶ minc_parse.?? $XML \rightarrow AST$
 - ▶ $minc_cogen.$?? AST → assembly
 - ▶ main.?? or minc.?? main driver
- the exact location depends on the language
- your work will be mostly done in minc_cogen.??
- other parts are given

Abstract Syntax Tree (AST)

- naturally represent a program
 - ▶ the whole program
 - function definition
 - statement
 - expression
 - etc.
- also called *parse tree*
- see minc_ast.??

```
while (x < 10)
y++;</pre>
```



Lexer and parser

 \bullet lexer (lexical analyzer, tokenizer) : string \to sequence of "tokens" (words)

while (x < 10) y++;
$$\rightarrow$$
 while (|x| < 10) |y| ++ ;

• parser : sequence of tokens \Rightarrow AST



Implementing lexer and parser

- first write a *grammar*, typically in the Backus-Naur form (BNF)
- e.g., (part of C grammar)
 - ightharpoonup statement | if-statement | ...
 - ▶ while-statement = 'while' '(' expr')' statement
 - $expr = number \mid expr$ '+' $expr \mid \dots$
 - ightharpoonup number = digit + | digit + |
 - digit = [0-9]
 - **.** . . .
- based on the grammar, either
 - write them by hand, or
 - ▶ use a lexser/parser generators

Lexer/parser generators

- *lexer generator* generates a lexer from a syntax of *tokens* (variables, numbers, . . .)
- parser generator generates a parser from a syntax of higher-level constructs (expressions, statements, ...)
- some grammar frameworks (PEG) specify them in a single framework

Lexer/parser generators

- many programming languages have lexer/parser generators for them
 - ▶ lex/yacc (flex/bison) : C/C++
 - ► ANTLR: C, C++, Java, Python, JavaScript, Go, ...
 - ▶ ocamllex/menhir : OCaml
 - ▶ tatsu : Python
 - ▶ etc.
- this exercise uses tatsu, to generate a Python program that converts C source into XML
 - ► the grammar is in minc_grammar.y
- each language reads the XML by the respective XML library you have used before

Intermediate Representation (IR)

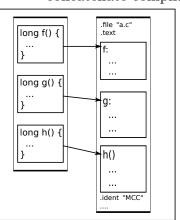
- a common representation of programs internally used by a compiler
 - ▶ hopefully independent from the source language (C, C++, Rust, Go, Julia, etc.)
 - ▶ hopefully independent from the target language (x86, ARM, PowerPC, etc.)
- it generally looks like "an assembly with infinite registers (variables)"
- a compiler performs optimizations as IR \rightarrow IR transformations
- note: in the exercise you could design your IR, but it is not necessary (it is possible to directly go from AST \rightarrow asm)

Code generation (minc_cogen) — basic structure

- takes an AST and returns machine code (a list of instructions)
- generate machine code for an AST \approx generate machine code of its components and properly arrange them
- the program (program) → function definition (definition)
 → statement (stmt) → expression (expr)
- code generator has lots of
 - case analysis based on the type of the tree; use
 - **★** pattern matching (OCaml match and Rust match) or
 - ⋆ polymorphism (OCaml objects, Julia function, Go interface, Rust trait)
 - recursive calls to child trees

Compiling an entire file

 $\bullet \approx$ concatenate compilation of individual function definitions



In OCaml, it will look like ...

Compiling a function definition

• \approx compile the body (statement); put prologue (grow the stack, etc.) and epilogue (shrink the stack, ret, etc.)

```
long f() {

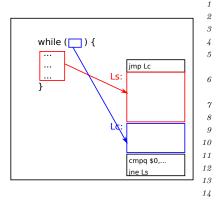
...
grow stack
save args
...
}

...
shrink stack
ret
```

```
let ast_to_insns_def def ... =
match def with

DefFun(f, params, ret_type, body) ->
(gen_prologue def)
0 (ast_to_insns_stmt body ...)
0 (gen_epilogue def)
```

Compiling a statement (e.g., while statement)



```
let rec ast_to_insns_stmt stmt ...
  match stmt with
  | StmtWhile(cond, body) ->
    let cond_op,cond_insns =
         ast_to_insns_expr cond ... in
    let body_insns = ast_to_insns_stmt body
         ... in
    let ... in
    [ jmp Lc;
      Ls 1
    @ bodv insns
    [Lc]
    @ cond insns @
    [ cmpq $0,cond_op;
      ine Ls ]
```

Compiling an expression (arithmetic)

 $\bullet \approx$ compile the arguments; an arithmetic instruction

```
let rec ast_to_insns_expr expr ... =
                    match expr with
                     | ExprOp("+", [e0; e1]) ->
                       let insns1,op1 = ast_insns_expr e1 ... in
                       let insns0,op0 = ast_insns_expr e0 ... in
movq ...,XX(%rsp)
                       let m = a \ slot \ on \ the \ stack \ in
                       ((insns1
                       @ [ movq op1,m ]
              10
                       @ insns0
                       0 [addq m,op0]), (*op0 = op0 + m *)
addg XX(%rsp),...
              11
                       (0go
              12
              13
```

- Remark: movq XX(%rsp),... saves the first operand, ensuring it won't be destroyed during the evaluation of the second
- remember we are following the simplest strategy = "save all intermediate results on the stack"

Compiling an expression (comparison)

- A < B is an expression that evaluates to
 - ▶ 1 if *A* < *B*
 - ▶ 0 if A >= B
- no single instruction exactly does this
- note that they can appear anywhere expression can
 - z = x < y, (x < y) + z, and f(x < 1) are allowed (they do not necessarily appear in condition expression of if or while)
- how to do it in assembly code?
 - conditional branch
 - 2 conditional set instruction. e.g.,

```
movq $0,%rax
cmpq %rdi,%rsi
setle %al
```

will set %al (the lowest 8 bits of %rax) to 1 when %rsi - %rdi ≤ 0 (less-than-or-equal)

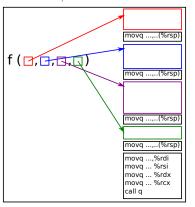
Compiling an expression (comparison)

 $\bullet \approx$ compile the arguments; compare; conditional set

```
let rec ast_to_insns_expr expr ... =
                        match expr with
                        | ExprOp("<", [e0; e1]) ->
                          let insns1,op1 = ast_to_insns_expr e1 ...
                                 in
                          let insns0,op0 = ast_to_insns_expr e0 ...
                 6
                                 in
                          let mO = a \ slot \ on \ the \ stack \ in
movq ...,XX(%rsp)
                          let m1 = a \ slot \ on \ the \ stack in
                          ((insns1
                10
                             @ [ movq op1,m1 ]
                11
mova ....YY(%rsp)
                             @ insns0
                12
movg $0,%rax
mova YY(%rsp)....
                13
                             @ [ movq op0,m0;
cmpa XX(%rsp)....
                                 movq $0, %rax;
                14
setl %al
                1.5
                                 movq m0,op0;
                                 cmpq m1,op0;
                16
                                 setl rax 1
                17
                18
                          (0go
                19
```

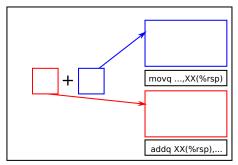
Compiling an expression (function call)

 • compile all arguments; put them to positions specified by ABI; a call instruction



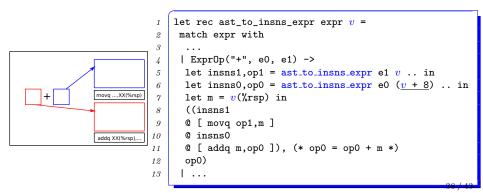
Details we have been leaving out

- how to determine locations to save values of *subexpressions* and *variables*
- that is, how to determine XX below



Determining where to save subexpressions

- ast_to_insns_expr receives a value (v) pointing to the lowest end of free space ast_to_insns_expr E v . . . generates instructions that evaluate E using (destroying) only addresses above v (%rsp)
- \rightarrow when evaluating A + B, save B at v(%rsp)
- let A use v + 8 and higher addresses



Locations to hold variables

• ex:

```
1 if (...) {
2 long a, b, c;
3 ...
4 }
```

- we need to hold a, b, c on the stack
- the problem is almost identical to saving values of subexpressions
- $\bullet \to \texttt{ast_to_insns_stmt}$ also takes v pointing to the beginning of the free space
 - spec: $ast_to_insns_stmt \ S \ v \dots$ generates instructions to execute S; they use (destroy) only addresses above v(%rsp)
- \rightarrow e.g., hold a $\mapsto v(\% rsp)$, b $\mapsto v + 8(\% rsp)$, c $\mapsto v + 16(\% rsp)$

Environment: records where variables are held

- when a variable occurs in an expression, we need to get the location that holds the variable
 - ex: to compile x + 1, we need to know where x is held
- make a data structure that holds a mapping "variable → location" (environment) and pass it to ast_to_insns_stmt and ast_to_insns_expr
- when new variables are declared at the beginning of a compound statement $(\{ \dots \})$, add new mappings to it

ast_to_insns_expr receives an environment

```
let rec ast_to_insns_expr expr env v =
match expr with
...
| ExprId(x) ->
let loc = env_lookup x env in
([ movq loc,... ], ...)
| ...
```

• $env_lookup \ x \ env$ searches environment env for x and returns its location

ast_to_insns_stmt receives an environment too

```
let rec ast_to_insns_stmt expr env v =
match expr with
...
| StmtCompound(decls, stmts) ->
let env',v' = env_extend decls env v in
cogen_stmts stmts env' v' ...
| ...
```

- env extend decls env v
 - ▶ assign locations (v, v + 8, v + 16, ...) to variables declared in decls
 - ightharpoonup register them in env
 - \triangleright return the new environment env' and the new free space v'

Implementing environment

- an environment is a list of (variable name, location)'s
- loc = env_lookup x env
 returns the location paired with x in environment env
- $env' = env_add \ x \ loc \ env$ returns a new environment env' which has a new mapping $x \mapsto loc$ in addition to env((x, loc)::env)
- an environment can be easily implemented with a list of (variable name, location)'s and is left for your exercise