

Constrained Long-Horizon Direct Model Predictive Control for Grid-Connected Converters with *LCL* Filters

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«Model predictive control», «Grid-connected converter», «Optimal control», «Multi-objective optimization», «Control methods for electrical systems», «LCL», «Converter control».

Abstract

This paper presents a direct model predictive control algorithm for a three-level neutral point clamped converter connected to the grid via an *LCL* filter. The proposed controller simultaneously controls the grid and converter currents as well as the filter capacitor voltage, while meeting the relevant grid standards. Moreover, output constraints are included to ensure operation of the system within its safe operating limits. This is achieved by formulating the direct MPC problem as a constrained integer least-squares optimization problem, wherein the output constraints are mapped into input constraints. The presented results verify the effectiveness of the proposed method.

Introduction

Medium-voltage (MV) grid-tied converters are used for the integration of renewable energy sources, scalable loads, and high-performance variable speed drives into the electrical grid. To ensure smooth operation, grid standards—e.g., the IEEE 519 [1] and the IEC 61000-2-4 [2] standards—are imposed to the point of common coupling (PCC). Such standards set tight limits on the current and voltage harmonics injected by the power electronic systems into the grid. To mitigate the current harmonics, *LCL* filters are commonly placed between the converter and the PCC. The addition of such a filter, however, gives rise to a third-order system, implying that its control is a nontrivial task. More specifically, not only the grid current needs to be controlled—as is the case with all grid-tied systems—but also the converter current and the capacitor voltage. Moreover, adequate damping of the filter resonance is required. Finally, large overshoots during transients that may harm the hardware components due to the correlated dynamics of the system need to be avoided to ensure safe system operation.

Meeting the above-mentioned tasks with conventional control techniques—that rely on linear control principles—is challenging, while the controller design can become complicated [3]. Furthermore, the most favorable dynamic operation is not achieved due to the cascaded control loops that tend to limit the bandwidth of the controller, especially when operation at low switching frequency is required [4]. As an alternative to linear control techniques, model predictive control (MPC) can be employed. Thanks to its multiple-input multiple-output (MIMO) nature as well as its ability to handle explicit constraints and provide active damping, superior steady-state and dynamic performance of the grid-connected power electronic system can be achieved [5, 6].

The most popular MPC-based method in academia is direct MPC, i.e., a control strategy that directly computes and applies the switching signals. Direct MPC with output reference tracking, also referred to as finite control set MPC (FCS-MPC), fully exploits the advantages that MPC can offer, but, alas, it comes with pronounced computational load [5, 7]. To address this—at least to some extent—the direct

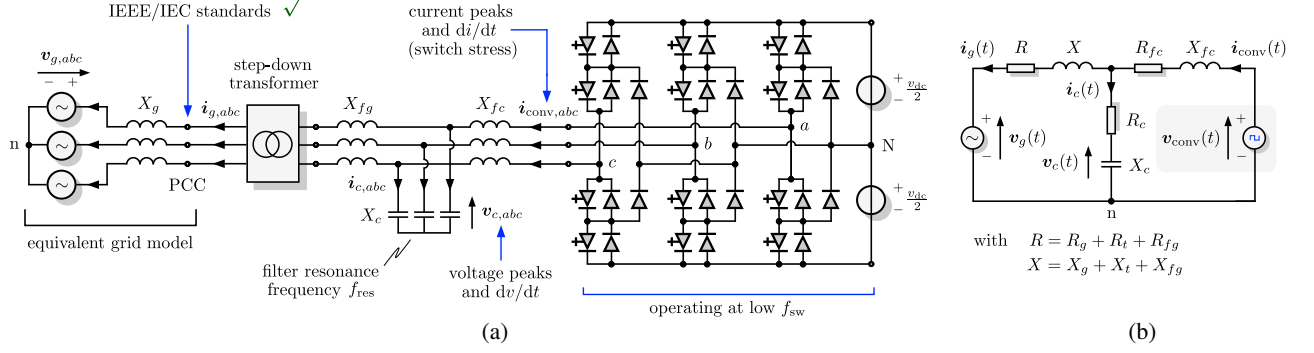


Fig. 1: (a) MV grid-tied three-phase 3L-NPC converter with an LCL -filter placed between the converter and the step-down transformer. (b) Equivalent circuit in the $\alpha\beta$ -plane.

MPC problem can be designed as an integer least-squares (ILS) one and solved with a sphere decoding algorithm [8]. Such an approach, however, cannot easily handle constraints that will ensure operation of the system within its safe operating area. Thus, to avoid overcurrents and/or overvoltages that may damage the hardware of the converter and/or the filter, the feasible set of the optimization problem underlying direct MPC needs to be carefully redesigned [9].

Given the above, this paper proposes a constrained direct MPC for an MV three-level neutral point clamped (3L-NPC) converter connected to the grid via an LCL filter. The optimization problem, formulated as an ILS one, accounts for constraints on all the controlled variables to increase the system reliability. To this aim, the output constraints are mapped into constraints on the control input. In doing so, candidate solutions that may lead to large overshoots are already excluded in a preprocessing stage. Moreover, a long horizon is employed, which combined with full-state information, provides damping of the filter resonance without requiring a dedicated active damping loop. As a result, the MV converter can be operated at a very low switching frequency (and close to the resonance), while still meeting the grid standards. The effectiveness of the adopted method is demonstrated with the presented results.

Formulation of the Constrained Direct MPC Optimization Problem

Consider the MV grid-tied 3L-NPC converter with an LCL -filter shown in Fig. 1(a). To keep the demonstration of the proposed control method simple, the dc-link voltage V_{dc} is assumed to be constant¹ and the neutral point potential N fixed. All variables given in the abc -plane $\xi_{abc} = [\xi_a \ \xi_b \ \xi_c]^T$ are mapped into two-dimensional vectors $\xi_{\alpha\beta} = [\xi_\alpha \ \xi_\beta]^T$ via the reduced Clarke transformation matrix \mathbf{K} , i.e., $\xi_{\alpha\beta} = \mathbf{K} \xi_{abc}$, see [5].

Controller Model

The equivalent circuit of the system under consideration in the $\alpha\beta$ -reference frame² is shown in Fig. 1(b). This paper considers the same MV conversion system as the case study described in [10], from which the system parameters are taken (see Table I in [10]). A strong grid is assumed, and the (dominant) LCL -filter resonance frequency is $f_{res} = 304$ Hz. The passive components have very small (series) resistive parts, i.e., they essentially do not provide any passive damping. All SI variables are normalized based on the rated values of the step-down transformer (secondary side).

Depending on the *single-phase* switch position $u_z \in \mathcal{U} = \{-1, 0, 1\}$, with $z \in \{a, b, c\}$, the single-phase output voltage of a 3L-NPC converter can assume three possible discrete voltage levels, i.e., $-\frac{V_{dc}}{2}$, 0 , $\frac{V_{dc}}{2}$, respectively. By introducing the *three-phase* switch position³ $\mathbf{u}_{abc} = [u_a \ u_b \ u_c]^T \in \mathcal{U} = \mathcal{U}^3$, the converter output voltage \mathbf{v}_{conv} is given by

$$\mathbf{v}_{conv}(t) = \frac{V_{dc}}{2} \mathbf{K} \mathbf{u}_{abc}(t) = \frac{V_{dc}}{2} \mathbf{u}(t). \quad (1)$$

¹The outer layer which regulates the dc-link voltage and generates the power references is out of the scope of this paper.

²Hereafter, to simplify the notation, variables in the $\alpha\beta$ -frame are not indicated by the corresponding subscript, unless otherwise stated.

³Note that, given a 3L-NPC topology, the integer input set \mathcal{U} comprises $3^3 = 27$ combinations of \mathbf{u}_{abc} .

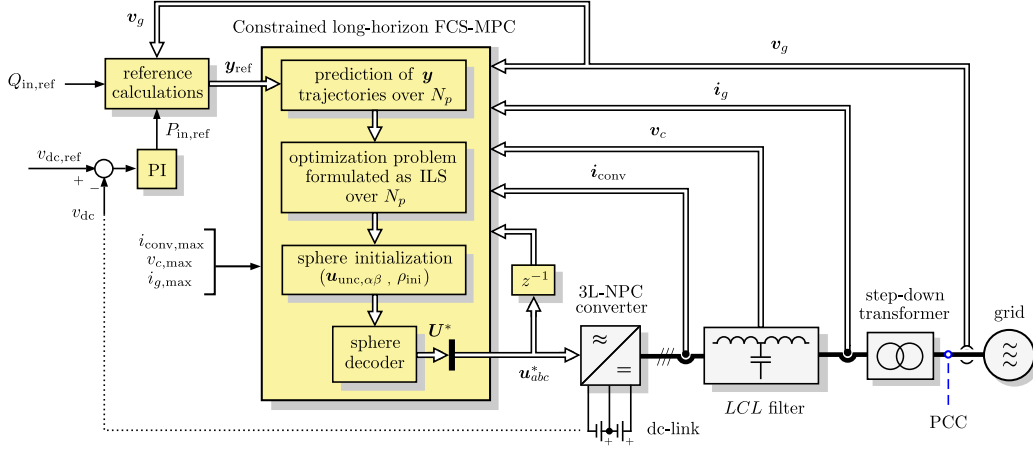


Fig. 2: Structure of the direct MPC (formulated as an ILS problem) to control the system shown in Fig. 1. The preprocessing procedure to compute ρ_{ini} is executed before calling the sphere decoder, which computes then u_{abc}^* .

By choosing $\mathbf{x} = [i_{conv}^T \ v_c^T \ i_g^T \ v_g^T]^T \in \mathbb{R}^8$ and $\mathbf{y} = [i_{conv}^T \ v_c^T \ i_g^T]^T \in \mathbb{R}^6$ as the state and output vectors, respectively, and $\mathbf{u} \equiv \mathbf{u}_{abc}$ as the control input, the continuous-time state-space representation of the system in Fig. 1(a) is

$$\frac{d\mathbf{x}(t)}{dt} = \mathbf{F}\mathbf{x}(t) + \mathbf{G}\mathbf{u}(t) \quad (2a)$$

$$\mathbf{y}(t) = \mathbf{C}\mathbf{x}(t), \quad (2b)$$

where the voltage v_g models the grid source, i_g is the grid current, i_{conv} is the converter current, while v_c is the capacitor voltage. The reader is referred to [10] for the full derivation of the continuous-time differential equations which describe the system dynamics depicted in Fig. 1(b) as well as the definition of the matrices $\mathbf{F} \in \mathbb{R}^{8 \times 8}$, $\mathbf{G} \in \mathbb{R}^{8 \times 3}$, and $\mathbf{C} \in \mathbb{R}^{6 \times 8}$, respectively. Note that since an ideal grid is assumed (i.e., its angular frequency ω_g is constant), \mathbf{F} is a time-invariant matrix. Likewise, due to the assumption of a constant dc-link voltage, \mathbf{G} is also time invariant.

The discretized version of the continuous-time system (2) is derived by using exact discretization with the sampling interval T_s , i.e.,

$$\mathbf{x}(k+1) = \mathbf{A}\mathbf{x}(k) + \mathbf{B}\mathbf{u}(k) \quad (3a)$$

$$\mathbf{y}(k) = \mathbf{C}\mathbf{x}(k), \quad (3b)$$

with $\mathbf{A} = e^{\mathbf{F}T_s}$ and $\mathbf{B} = \int_0^{T_s} e^{\mathbf{F}\tau} d\tau \mathbf{G} = -\mathbf{F}^{-1}(\mathbf{I}_8 - \mathbf{A})\mathbf{G}$, while e is the matrix exponential, and $k \in \mathbb{N}$ denotes the discrete time step. Due to the absence of a modulator, T_s directly affects the switching frequency (and the *granularity of switching*), see [11].

Reformulation of the Constrained Direct MPC Problem in the Equivalent ILS Form

The block diagram of the presented MPC strategy is shown in Fig. 2. As a grid-connected power converter is considered in this work, the total demand distortion (TDD) of $i_{g,abc}$ and $v_{pcc,abc}$ as well as the amplitude of the associated harmonics should meet the IEEE 519 [1] and IEC 61000-2-4 [2] grid standards. Given this, the main control objective is to regulate i_g , i_{conv} , and v_c along their sinusoidal references by directly manipulating the converter switches, i.e., without the use of a modulator. This needs to be achieved while operating the MV converter at a low switching frequency so that the switching power losses are kept low. The output references \mathbf{y}_{ref} , i.e. $\mathbf{y}_{ref} = [i_{conv,ref}^T \ v_{c,ref}^T \ i_{g,ref}^T]^T$, are computed based on the real $P_{in,ref}$ and reactive $Q_{in,ref}$ power requirements at the transformer secondary side, with $Q_{in,ref} = 0$ at steady-state operation to achieve unity power factor $pf = 1$. Given a prediction horizon of N_p time steps, these control objectives are mapped into a scalar by the quadratic objective function

$$J(k) = \sum_{\ell=k}^{k+N_p-1} \|\mathbf{y}_{ref}(\ell+1) - \mathbf{y}(\ell+1)\|_Q^2 + \lambda_u \|\Delta \mathbf{u}(\ell)\|_2^2. \quad (4)$$

The first term in (4) denotes the deviation of the output variables \mathbf{y} from their reference values \mathbf{y}_{ref} . This error is weighted with the 6×6 positive semidefinite matrix $\mathbf{Q} \succeq \mathbf{0}$, the diagonal entries of which prioritize the tracking accuracy among the different controlled variables. The second term in (4) penalizes the switching effort, i.e., $\Delta \mathbf{u}(\ell) = \mathbf{u}(\ell) - \mathbf{u}(\ell - 1)$, which directly relates to the definition of the average device switching frequency f_{sw} [11]. To set the trade-off between the tracking accuracy and the resulting f_{sw} at each time step $\ell = k, \dots, k + N_p - 1$, the weighting factor $\lambda_u \in \mathbb{R}^{++}$ is introduced.

The aforementioned control goals have to be met while protecting the switching devices and *LCL* filter components from overcurrents and overvoltages, respectively. Therefore, during power transients, the amplitudes of i_{conv} , v_c , and i_g must be kept within given bounds. To this end, soft constraints are typically introduced in the optimization problem to include such physical limitations. Nevertheless, hard constraints are implemented in this work to facilitate the reformulation of the optimization problem, as shown in later sections. Hence, the constraints on the system output are designed in the $\alpha\beta$ -plane and imposed at each time step $\ell + 1$, i.e.,

$$\|i_{\text{conv}}(\ell + 1)\|_2 \leq i_{\text{conv,max}}, \quad \|v_c(\ell + 1)\|_2 \leq v_{c,\text{max}}, \quad \|i_g(\ell + 1)\|_2 \leq i_{g,\text{max}}, \quad (5)$$

where the positive scalars $i_{\text{conv,max}}, v_{c,\text{max}}, i_{g,\text{max}} \in \mathbb{R}^+$ define the upper boundary values of the converter current, filter capacitor voltage, and grid current, respectively. As an example, considering i_{conv} bounded by $i_{\text{conv,max}}$, it follows that

$$i_{\text{conv},\alpha}^2(\ell + 1) + i_{\text{conv},\beta}^2(\ell + 1) \leq i_{\text{conv,max}}^2, \quad (6)$$

which defines a circle in the $\alpha\beta$ -plane of radius $i_{\text{conv,max}}$. The constraints on v_c and i_g are imposed in a similar manner by referring to $v_{c,\text{max}}$ and $i_{g,\text{max}}$, respectively.

Given the above, to compute the optimal switching sequence over the N_p prediction steps⁴, i.e., $\mathbf{U}^*(k) = [\mathbf{u}^{*T}(k) \mathbf{u}^{*T}(k + 1) \dots \mathbf{u}^{*T}(k + N_p - 1)]^T \in \mathbb{U} = \mathcal{U} \times \dots \times \mathcal{U} = \mathcal{U}^{N_p} \subset \mathbb{Z}^{3N_p}$, that results in the best system performance—as quantified by (4)—while respecting the system dynamics and constraints—as expressed by (3) and (5), respectively—the constrained optimization problem underlying MPC needs to be solved. As shown in [8], to mitigate the computational burden of long-horizon direct MPC, while still guaranteeing optimality, function (4) can be written such that the associated optimization problem is a truncated ILS one. This problem can be subsequently solved in a computationally efficient manner with a dedicated branch-and-bound strategy named *sphere decoder* [8], [12].

By considering the unconstrained solution \mathbf{U}_{unc} , i.e., the solution that minimizes (4) when relaxing the feasible set from \mathbb{U} to \mathbb{R}^{3N_p} , the following equivalent constrained ILS problem can be defined

$$\mathbf{U}^*(k) = \arg \min_{\mathbf{U}(k) \in \mathbb{U}} \|\mathbf{V}\mathbf{U}(k) - \bar{\mathbf{U}}_{\text{unc}}(k)\|_2^2 \quad (7a)$$

$$\text{subject to } \mathbf{U}(k) \in \mathbb{U} = \mathcal{U}^{N_p}, \quad \|\Delta \mathbf{u}(\ell)\|_\infty \leq 1, \quad (7b)$$

$$\|i_{\text{conv}}(\ell + 1)\|_2 \leq i_{\text{conv,max}}, \quad \|v_c(\ell + 1)\|_2 \leq v_{c,\text{max}}, \quad (7c)$$

$$\|i_g(\ell + 1)\|_2 \leq i_{g,\text{max}}, \quad \forall \ell = k, \dots, k + N_p - 1, \quad (7d)$$

where $\bar{\mathbf{U}}_{\text{unc}}(k) = \mathbf{V}\mathbf{U}_{\text{unc}}(k) \in \mathbb{R}^{3N_p}$, and $\mathbf{V} \in \mathbb{R}^{3N_p \times 3N_p}$ is a nonsingular, upper triangular matrix, known as the *lattice generator* matrix. The latter generates the $3N_p$ -dimensional discrete space (lattice), one point of which is the solution to problem (7), i.e., the lattice point with the shortest Euclidean distance to $\bar{\mathbf{U}}_{\text{unc}}$. Note that, a further (hard) input constraint, i.e., $\|\Delta \mathbf{u}(\ell)\|_\infty \leq 1$, with $\Delta \mathbf{u}(\ell) = \mathbf{u}(\ell) - \mathbf{u}(\ell - 1)$, is imposed by (7b) to avoid a shoot-through in the converter due to the 3L-NPC topology [5]. Out of \mathbf{U}^* , only the first element \mathbf{u}^* is applied to the converter whereas the rest are discarded in line with the receding horizon (RH) control principle [13]. Following, the optimization procedure is repeated at $k + 1$ based on a new $\mathbf{x}(k)$ and a shifted prediction horizon N_p .

Note that the initial radius of the sphere ρ_{ini} affects the effectiveness of the search process as it defines the first upper bound of the branch-and-bound mechanism. Therefore, ρ_{ini} should be as small as possible

⁴Note that, the *feasible set* \mathbb{U} is defined by the N_p -times Cartesian product of \mathcal{U}

to remove a priori as many candidate solutions as possible, while still ensuring feasibility of the optimization process, e.g., containing at least one lattice point. In [14], the initial radius ρ_{ini} is computed as

$$\rho_{\text{ini}}(k) = \min \{ \rho_1(k), \rho_2(k) \} , \quad (8)$$

where the relative options are

$$\rho_1(k) = \|\bar{\mathbf{U}}_{\text{unc}}(k) - \mathbf{V}\mathbf{U}_{\text{bab}}(k)\|_2 \quad \text{and} \quad \rho_2(k) = \|\bar{\mathbf{U}}_{\text{unc}}(k) - \mathbf{V}\mathbf{U}_{\text{ed}}(k)\|_2 . \quad (9)$$

Radius ρ_1 in (9) depends on the Babai estimate \mathbf{U}_{bab} , which is the rounded unconstrained solution, i.e., $\mathbf{U}_{\text{bab}}(k) = \lfloor \mathbf{U}_{\text{unc}}(k) \rfloor$. On the other hand, radius ρ_2 in (9) depends on an educated guess \mathbf{U}_{ed} , which is the previously applied solution $\mathbf{U}^*(k-1)$ shifted by one time step according to the RH policy, see [9].

Solving the Equivalent Constrained Integer Least-Squares Problem

The presence of constraint (7) affects the aforementioned computation of ρ_{ini} . Indeed, by adopting (8), there is a possibility that \mathbf{U}_{unc} and/or some of the candidate solutions included in the sphere violate (7). To avoid this, the computation of the initial radius needs to be revised. This is done by mapping the output constraints (7) into (hard) input constraints, thus limiting the feasible set \mathbb{U} . The approach presented in this paper is based on [9] and extended to handle multiple constraints by simultaneously considering bounds on \mathbf{i}_{conv} , \mathbf{v}_c , and \mathbf{i}_g .

By utilizing (3a) and (3b), the output dynamics at step $k+1$ are given by

$$\mathbf{y}(k+1) = \mathbf{C}\mathbf{A}\mathbf{x}(k) + \mathbf{C}\tilde{\mathbf{B}}\mathbf{K}\mathbf{u}(k) \Rightarrow \mathbf{i}_{\text{conv}}(k+1) = [\mathbf{I}_2 \quad \mathbf{0}_{2 \times 6}] \mathbf{A}\mathbf{x}(k) + \gamma_{\text{conv}} \mathbf{I}_2 \mathbf{u}_{\alpha\beta}(k) \quad (10a)$$

$$\mathbf{v}_c(k+1) = [\mathbf{0}_{2 \times 2} \quad \mathbf{I}_2 \quad \mathbf{0}_{2 \times 4}] \mathbf{A}\mathbf{x}(k) + \gamma_c \mathbf{I}_2 \mathbf{u}_{\alpha\beta}(k) \quad (10b)$$

$$\mathbf{i}_g(k+1) = [\mathbf{0}_{2 \times 4} \quad \mathbf{I}_2 \quad \mathbf{0}_{2 \times 2}] \mathbf{A}\mathbf{x}(k) + \gamma_g \mathbf{I}_2 \mathbf{u}_{\alpha\beta}(k) , \quad (10c)$$

where $\tilde{\mathbf{B}} = -\mathbf{F}^{-1}(\mathbf{I}_8 - \mathbf{A})\tilde{\mathbf{G}}$, with $\tilde{\mathbf{G}} = (V_{\text{dc}}/2X_{fc})[\mathbf{I}_2 \quad \mathbf{0}_{2 \times 6}]^T$. Moreover, $\mathbf{u}_{\alpha\beta}(k) = \mathbf{K}\mathbf{u}(k)$, while $\mathbf{C}\tilde{\mathbf{B}}\mathbf{K} = \mathbf{C}\mathbf{B}$ holds. In more detail, given $\mathbf{C} \in \mathbb{R}^{6 \times 8}$ and $\tilde{\mathbf{B}} \in \mathbb{R}^{8 \times 2}$, (10a), (10b), and (10c), are derived by noticing the structure of the product $\mathbf{C}\tilde{\mathbf{B}} \in \mathbb{R}^{6 \times 2}$, i.e.,

$$\mathbf{C}\tilde{\mathbf{B}} = \begin{bmatrix} \gamma_{\text{conv}} & 0 & \gamma_c & 0 & \gamma_g & 0 \\ 0 & \gamma_{\text{conv}} & 0 & \gamma_c & 0 & \gamma_g \end{bmatrix}^T = [\gamma_{\text{conv}}\mathbf{I}_2 \quad \gamma_c\mathbf{I}_2 \quad \gamma_g\mathbf{I}_2]^T . \quad (11)$$

As can be seen in (11), three 2×2 diagonal matrices appear, i.e., $\text{diag}(\gamma_{\text{conv}}) \succ 0 \in \mathbb{R}^{2 \times 2}$, $\text{diag}(\gamma_c) \succ 0 \in \mathbb{R}^{2 \times 2}$, and $\text{diag}(\gamma_g) \succ 0 \in \mathbb{R}^{2 \times 2}$, with $\gamma_{\text{conv}} \neq \gamma_c \neq \gamma_g$. In particular, $\gamma_{\text{conv}}\mathbf{I}_2 = [\mathbf{I}_2 \quad \mathbf{0}_{2 \times 6}]\tilde{\mathbf{B}}$, $\gamma_c\mathbf{I}_2 = [\mathbf{0}_{2 \times 2} \quad \mathbf{I}_2 \quad \mathbf{0}_{2 \times 4}]\tilde{\mathbf{B}}$ and $\gamma_g\mathbf{I}_2 = [\mathbf{0}_{2 \times 4} \quad \mathbf{I}_2 \quad \mathbf{0}_{2 \times 2}]\tilde{\mathbf{B}}$ can be easily computed. Given (10a), (10b), and (10c), the three one-dimensional constraints in (5) can be rewritten as

$$\|\mathbf{i}_{\text{conv}}(\ell+1)\|_2 \leq i_{\text{conv},\text{max}} \Rightarrow \left\| \frac{[\mathbf{I}_2 \quad \mathbf{0}_{2 \times 6}] \mathbf{A}\mathbf{x}(k)}{\gamma_{\text{conv}}} + \mathbf{K}\mathbf{u}(k) \right\|_2 \leq \frac{i_{\text{conv},\text{max}}}{\gamma_{\text{conv}}} \quad (12a)$$

$$\|\mathbf{v}_c(\ell+1)\|_2 \leq v_{c,\text{max}} \Rightarrow \left\| \frac{[\mathbf{0}_{2 \times 2} \quad \mathbf{I}_2 \quad \mathbf{0}_{2 \times 4}] \mathbf{A}\mathbf{x}(k)}{\gamma_c} + \mathbf{K}\mathbf{u}(k) \right\|_2 \leq \frac{v_{c,\text{max}}}{\gamma_c} \quad (12b)$$

$$\|\mathbf{i}_g(\ell+1)\|_2 \leq i_{g,\text{max}} \Rightarrow \left\| \frac{[\mathbf{0}_{2 \times 4} \quad \mathbf{I}_2 \quad \mathbf{0}_{2 \times 2}] \mathbf{A}\mathbf{x}(k)}{\gamma_g} + \mathbf{K}\mathbf{u}(k) \right\|_2 \leq \frac{i_{g,\text{max}}}{\gamma_g} . \quad (12c)$$

To simplify the derivation of the constrained ILS problem, let us first consider a generic one-dimensional constraint of the form

$$\left\| \frac{\tilde{\mathbf{C}}\mathbf{A}\mathbf{x}(k)}{\gamma} + \mathbf{K}\mathbf{u}(k) \right\|_2 \leq \frac{y_{\text{max}}}{\gamma} \iff \|\mathbf{u}_{\text{constr}}(k) + \mathbf{K}\mathbf{u}(k)\|_2 \leq \rho_{\text{constr}} , \quad (13)$$

where the generic $\tilde{\mathbf{C}}$, γ , and y_{max} are changed according to which constraint among (12a), (12b), and (12c) is taken into consideration. Note that (13) describes a feasible set in the form of a circle \mathcal{C} in the $\alpha\beta$ -plane centered at $\mathbf{u}_{\text{constr}}(k) = -\frac{\tilde{\mathbf{C}}\mathbf{A}\mathbf{x}(k)}{\gamma}$ with radius $\rho_{\text{constr}} = \frac{y_{\text{max}}}{\gamma}$.

With (13), the constraints in (12) define $\mathcal{C}_{\text{conv}}$, \mathcal{C}_c , and \mathcal{C}_g related to \mathbf{i}_{conv} , \mathbf{v}_c , and \mathbf{i}_g , respectively,

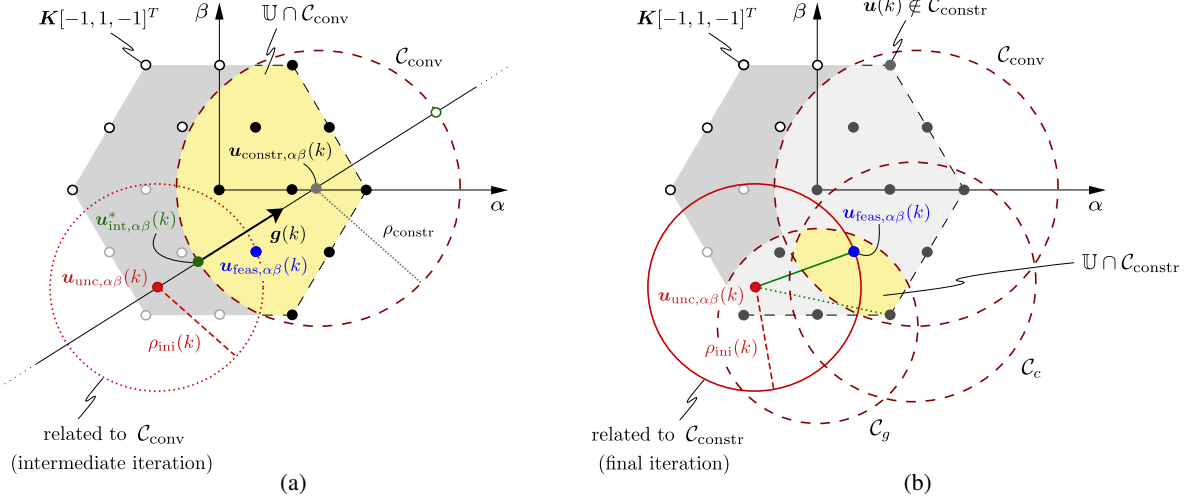


Fig. 3: Computation of the input constrained set $\mathcal{C}_{\text{constr}}$ and choice of the initial radius in the $\alpha\beta$ -plane. (a) Computation of $\rho_{\text{ini}}(k)$ for a one-dimensional constraint (set $\mathcal{C}_{\text{conv}}$). (b) Computation of $\mathcal{C}_{\text{constr}}$ and $\rho_{\text{ini}}(k)$ based on all three constraints. Focusing on (a), the gradient, as depicted by the (scaled-down) vector $\mathbf{g}(k)$, shows the relative position of $\mathbf{u}_{\text{unc}}(k)$ with respect to the center of $\mathcal{C}_{\text{conv}}$, i.e., $\mathbf{u}_{\text{constr}}(k)$. Subsequently, the intersection point between $\mathbf{g}(k)$ and $\mathcal{C}_{\text{conv}}$, i.e., $\mathbf{u}_{\text{int}}^*(k)$ (indicated as green circle), that is closer to $\mathbf{u}_{\text{unc}}(k)$ (shown as red circle) is rounded up. Based on $\mathbf{u}_{\text{feas}}(k)$ (shown as blue circle) the radius $\rho_{\text{ini}}(k)$ (red dash-dotted line) of the hypersphere (shown as a red circle) is determined. The infeasible points enclosed in the hypersphere are indicated with light gray circles, whereas the black solid circles are feasible, but suboptimal, points.

which individually restrict the feasible set \mathbb{U} . This implies that to meet the output constraint (7), the optimal switching sequence \mathbf{U}^* should lie within the intersection of all the constrained sets $\mathcal{C}_{\text{constr}} = \mathcal{C}_{\text{conv}} \cap \mathcal{C}_c \cap \mathcal{C}_g$ in the $\alpha\beta$ -plane. Based on this, the *feasible* set of the integer-valued input vector \mathbf{U} is defined as $\mathbb{U}_{\text{constr}} = \mathbb{U} \cap \mathcal{C}_{\text{constr}} = \mathcal{U}_{\text{constr},1} \times \dots \times \mathcal{U}_{\text{constr},N_p}$, with

$$\mathcal{U}_{\text{constr},i} = \{\mathbf{u}(\ell) \mid \mathbf{K}\mathbf{u}(\ell) \in \mathcal{C}_{\text{constr}}, \mathbf{u}(\ell) \in \mathcal{U}\} \quad \forall \ell = k, \dots, k + N_p - 1, \quad (14)$$

for $i \in \{1, \dots, N_p\}$. A graphical example is depicted in Fig. 3(a).

Given (14), the initial radius ρ_{ini} of the hypersphere needs to be revised according to the restricted input feasible set $\mathbb{U}_{\text{constr}}$. While on the one hand the radius should meet the same objective as in the unconstrained ILS problem case, i.e., to be as small as possible, an additional goal is that the new hypersphere should include at least one *feasible* point. To satisfy both criteria, (8) is substituted by

$$\rho_{\text{ini}}(k) = \min \{ \hat{\rho}_1(k), \hat{\rho}_2(k) \}. \quad (15)$$

To compute ρ_{ini} in presence of the multiple output constraints, the following procedure is adopted.

Step 1: Radius $\hat{\rho}_1$ is defined as

$$\hat{\rho}_1(k) = \|\bar{\mathbf{U}}_{\text{unc}}(k) - \mathbf{V}\mathbf{U}_{\text{rnd}}(k)\|_2, \quad (16)$$

where the new initial guess $\mathbf{U}_{\text{rnd}}(k) = [\mathbf{u}_{\text{rnd}}^T(k) \dots \mathbf{u}_{\text{rnd}}^T(k + N_p - 1)]^T$ in the $\alpha\beta$ -plane is equal to the Babai estimate \mathbf{U}_{bab} from time step $k + 1$ up to $k + N_p - 1$, whereas the estimate at k results by guessing the feasible candidate solution $\mathbf{u}_{\text{feas}}(k) = \mathbf{K}\mathbf{u}_{\text{feas},abc}(k)$, with $\mathbf{u}_{\text{feas},abc} \in \mathcal{U}_{\text{constr},1}$, closest to \mathbf{u}_{unc} [9].

Step 2: Considering a single (one-dimensional) output constraint, the vector $\mathbf{g}(k) = \mathbf{u}_{\text{constr}}(k) - \mathbf{u}_{\text{unc}}(k)$ that spans the line passing through \mathbf{u}_{unc} and the center of \mathcal{C} , i.e., $\mathbf{u}_{\text{constr}}$, is computed.

Step 3: The intersection points, $\mathbf{u}_{\text{int},1}$ and $\mathbf{u}_{\text{int},2}$, of the aforementioned line and \mathcal{C} are calculated. The one closer to \mathbf{u}_{unc} is computed with

$$\mathbf{u}_{\text{int}}^*(k) = \arg \minimize \|\mathbf{u}_{\text{int},i}(k) - \mathbf{u}_{\text{unc}}(k)\|_2 \quad \text{for } i = 1, 2.$$

Step 4: The feasible input closer to $\mathbf{u}_{\text{int}}^*$ is found. This is done by examining the sign of the elements of $\mathbf{g}(k)$, i.e., the direction of the gradient in each dimension of the space. Following, $\mathbf{u}_{\text{int}}^*$ is *rounded* up or down (i.e., ceiled or floored, respectively) depending on the direction of the gradient, i.e.,

$$\mathbf{u}_{\text{feas}}(k) = \begin{cases} \lceil \mathbf{u}_{\text{int}}^*(k) \rceil & \text{if } \mathbf{g}(k) \geq 0 \\ \lfloor \mathbf{u}_{\text{int}}^*(k) \rfloor & \text{if } \mathbf{g}(k) < 0. \end{cases} \quad (17)$$

Step 5: The candidate solution \mathbf{u}_{feas} —mapped onto the three-phase switch position by $\mathbf{u}_{\text{feas},abc}(k) = \mathbf{K}^{-1}\mathbf{u}_{\text{feas}}(k)$ —is the initial guess at time-step k . As for radius $\hat{\rho}_2$, this depends on the educated guess \mathbf{U}_{ed} . However, it is finite only if \mathbf{U}_{ed} belongs to the constrained feasible set, i.e.,

$$\hat{\rho}_2(k) = \begin{cases} \rho_2(k) & \text{if } \mathbf{U}_{\text{ed}}(k) \in \mathbb{U}_{\text{constr}} \\ \infty & \text{if } \mathbf{U}_{\text{ed}}(k) \notin \mathbb{U}_{\text{constr}}. \end{cases} \quad (18)$$

Then, the initial radius is found by solving $\rho_{\text{ini}}(k) = \min \{ \hat{\rho}_1(k), \hat{\rho}_2(k) \}$.

At this point it should be mentioned that by adopting steps 1–5 for each one-dimensional constraint, three values of ρ_{ini} —related to three different values of \mathbf{u}_{feas} —result. Hence, the new hypersphere should contain at least one point/solution within $\mathcal{C}_{\text{constr}} = \mathcal{C}_{\text{conv}} \cap \mathcal{C}_c \cap \mathcal{C}_g$. To achieve this, the following step is introduced.

Step 6: For each one-dimensional constraint $j \in \{1, 2, 3\}$, the outcome of steps 1–5 is $\mathbf{u}_{\text{feas},j}$ (see (17)) that relates to $\rho_{\text{ini},j}$. Thus, $\mathbf{u}_{\text{feas},j}$ which lies in $\mathcal{C}_{\text{constr}}$ is selected and the related $\rho_{\text{ini},j}$ is used to build the sphere. If multiple $\mathbf{u}_{\text{feas},j}$ lie in $\mathcal{C}_{\text{constr}}$, the one with the minimal distance from \mathbf{u}_{unc} is selected⁵.

To elucidate the above-mentioned procedure, the example in Fig. 3(b) is provided. Therein, the three output constraints are visualized, with $\mathbf{u}_{\text{feas},1} \leftarrow \mathcal{C}_{\text{conv}}$, $\mathbf{u}_{\text{feas},2} \leftarrow \mathcal{C}_c$, and $\mathbf{u}_{\text{feas},3} \leftarrow \mathcal{C}_g$, while only one candidate solution is assumed to lie in $\mathcal{C}_{\text{constr}}$ at time step k to simplify the demonstration. After computing the feasible input set and the refined initial radius, the sphere decoder is called with the difference that now the $3N_p$ -dimensional candidate solution $\mathbf{U}^*(k)$ enclosed in the hypersphere belongs to set $\mathbb{U}_{\text{constr}} = \mathbb{U} \cap \mathcal{C}_{\text{constr}}$. Nevertheless, in the worst-case scenario, if none of the resulting $\mathbf{u}_{\text{feas},j}$ computed from steps 1–6 belongs to $\mathcal{C}_{\text{constr}}$, the set is first relaxed to $\mathcal{C}_{\text{constr}} = \mathcal{C}_{\text{conv}} \cap \mathcal{C}_c$, and then to $\mathcal{C}_{\text{constr}} = \mathcal{C}_{\text{conv}}$ in order to find the that best suboptimal solution that violates the smallest number of constraints.

Performance Assessment

The performance of the proposed direct MPC scheme is evaluated through MATLAB simulations. Considering $f_{\text{res}} = 304 \text{ Hz}$, the goal is to choose λ_u such that the switching frequency f_{sw} is close to f_{res} . To this aim, λ_u is chosen such that $f_{\text{sw}} \approx 400 \text{ Hz}$ results, i.e., $\lambda_u = 0.45$. A sampling interval $T_s = 150 \mu\text{s}$ is chosen such that a high granularity of switching is achieved, as this improves the system performance [11]. Moreover, given that for a favorable steady-state and transient performance a long prediction interval in time is recommended [15], a ten-step ($N_p = 10$) horizon is implemented. Regarding the matrix \mathbf{Q} , the tracking of $\mathbf{i}_{g,abc}$ is prioritized over the reference tracking of the other variables to reduce the grid current TDD, $I_{g,\text{TDD}}$. Then, the converter current tracking is prioritized with respect to the voltage capacitor to (indirectly) avoid a deterioration in the tracking performance of $\mathbf{i}_{g,abc}$. This yields $\mathbf{Q} = \text{diag}(10, 10, 1, 1, 100, 100)$. The upper bounds $i_{\text{conv,max}} = 1.3$ per unit (p.u.), $v_{c,\text{max}} = 1.25$ p.u., and $i_{g,\text{max}} = 1.25$ p.u. are considered to limit overshoots during transients. Note that, given the chosen limits, the hard constraints do not affect the steady-state operation. All results in the sequel are in p.u.

Both steady-state and transient performances are depicted in Fig. 4 over two fundamental periods. More specifically, Fig. 4(a) shows the three-phase output waveforms produced by a direct MPC formulation without output bounds, i.e., when neglecting (7) and computing ρ_{ini} as in (8). On the other hand, Fig. 4(b) depicts the system response when the constrained MPC is taken into account, i.e., when considering (7) and the modified radius is computed according to (15).

⁵Note that, this procedure can be also adopted to evaluate a larger number of constraints n_c , i.e. $j = 1, \dots, n_c$

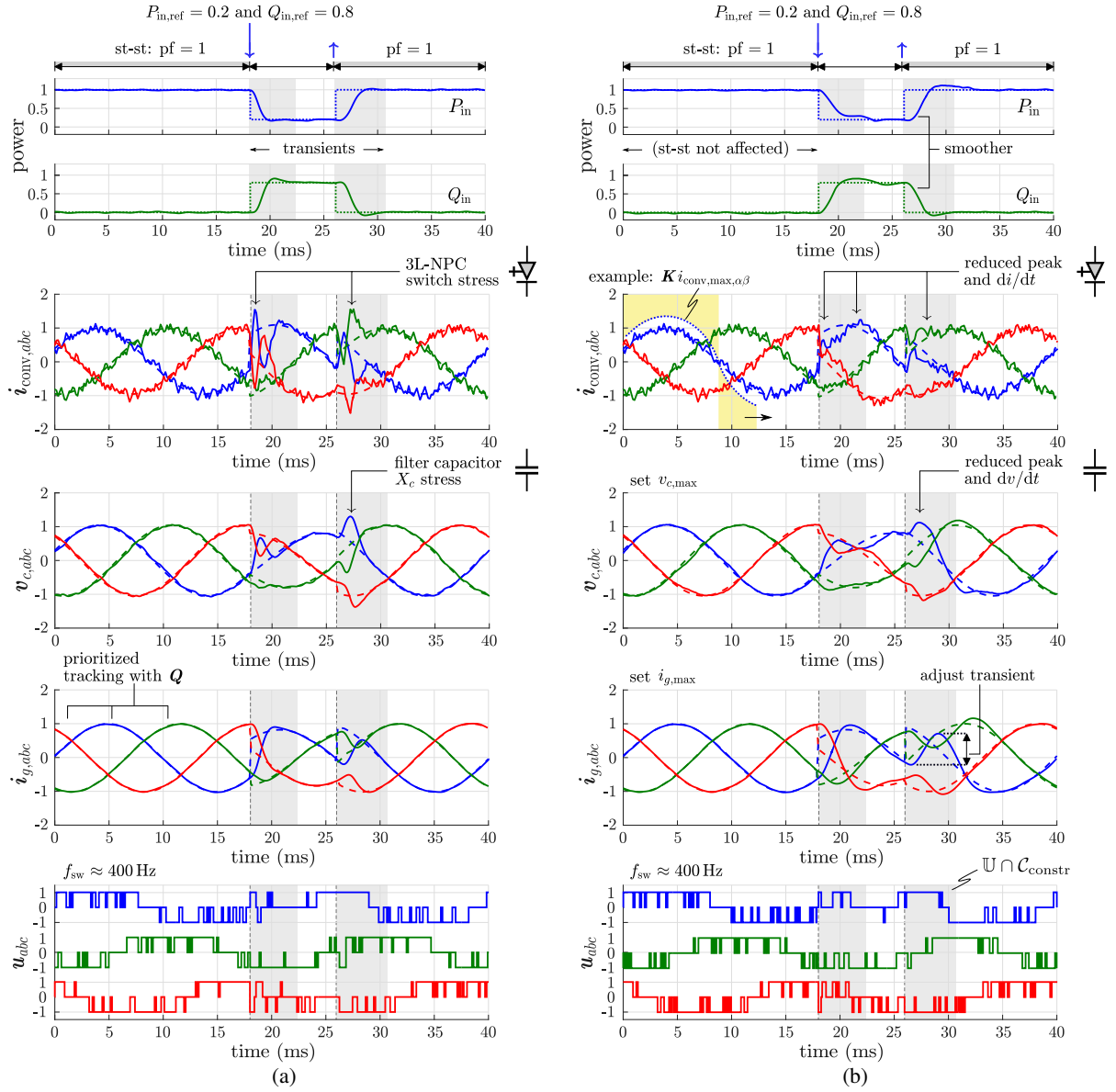


Fig. 4: Simulated waveforms produced by direct MPC when the output constraints are (a) not included, and (b) included. The results are shown over two fundamental periods $2T_g$, with $T_g = 20$ ms. From top to bottom: real P_{in} (blue line) and reactive power Q_{in} (green line) and their references (dashed lines); three-phase converter currents $i_{conv,abc}$ (with a , b and c shown with blue, red, and green lines, respectively) and the related references; three-phase capacitor voltage $v_{c,abc}$; three-phase grid currents $i_{g,abc}$; three-phase switch position u_{abc} .

Large step-wise changes in the input power references are applied. At $t = 18$ ms, $P_{in,ref}$ is changed from 1 to 0.2 p.u. and back to 1 p.u. at $t = 26$ ms. Likewise, $Q_{in,ref}$ is changed from 0 to 0.8 p.u. and back to 0 p.u. at the same time instants. The power references are translated into the corresponding $y_{ref,abc}$ which are accurately followed by y_{abc} . As shown in Fig. 4(a), when the output constraints are not considered, the variables $i_{conv,abc}$ and $v_{c,abc}$ exhibit significant overshoots during transients, exceeding the associated trip levels defined by the translation of $i_{conv,max}$ and $v_{c,max}$ into the abc -plane via K . To provide more insight into this point, and since the constraints are given in the $\alpha\beta$ -plane, the dynamics in Fig. 4 are translated accordingly and shown in Fig. 5. In particular, given the time window from $t = 15$ ms to $t = 35$ ms, Fig. 5(a) shows and quantifies the violation of y_{abc} . For example, as can be seen, $i_{conv,\alpha\beta}$ presents a peak of 1.70 p.u. which is 40% above $i_{conv,max} = 1.3$ p.u.

On the other hand, the effectiveness of the constrained MPC algorithm can be appreciated in Figs. 4(b) and 5(b), where y_{abc} (and $y_{\alpha\beta}$) always remain within the imposed bounds. It is worth mentioning, however, that the constrained MPC is less aggressive since the output bounds restrict the input set to $\mathcal{U}_{constr} \subset \mathcal{U}$, i.e., the candidate solutions that could lead to faster settling times are limited as these can lead to violation of the constraints. This implies that the decisions the MPC algorithm makes when the

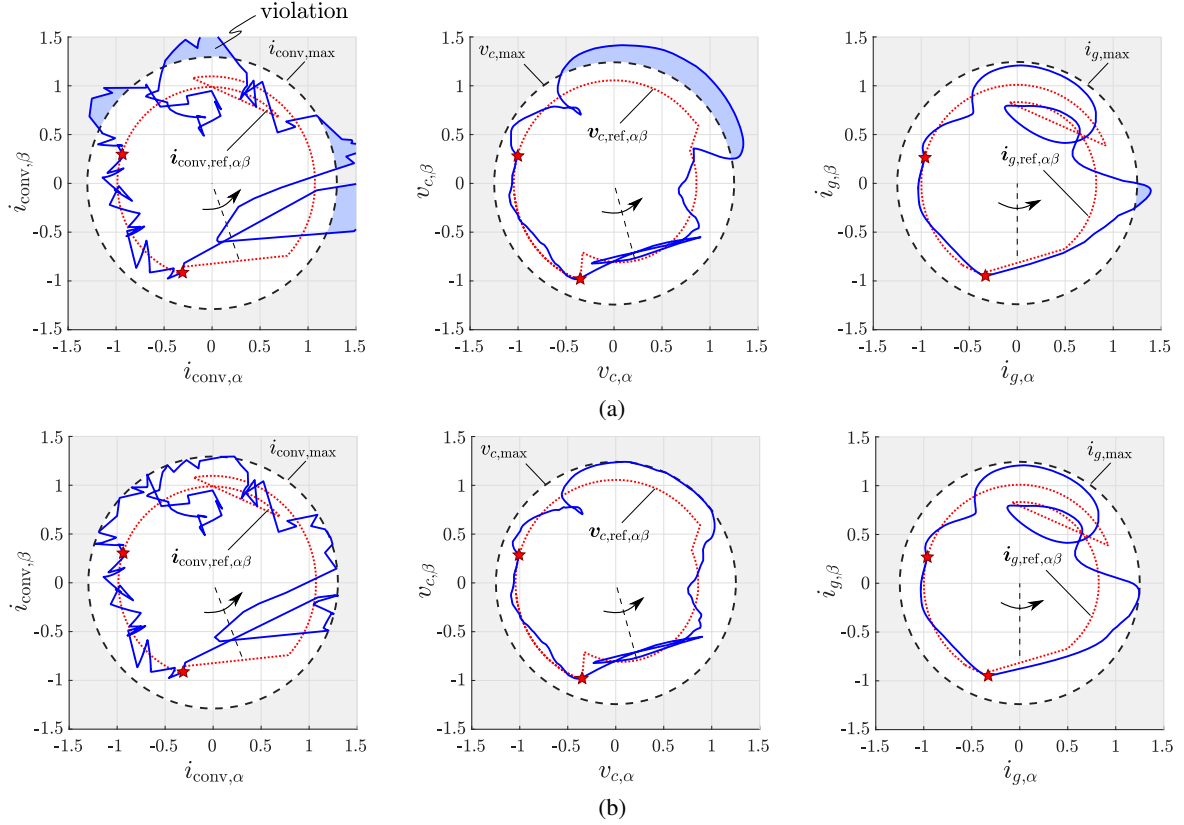


Fig. 5: Simulated waveforms of $i_{\text{conv},\alpha\beta}$ (left), $v_{c,\alpha\beta}$ (center), and $i_{g,\alpha\beta}$ (right) produced by the proposed direct MPC algorithm when output constraints are (a) not included, and (b) included. The references and bounds are shown as (red) dotted and (black) dashed circles, respectively. The $\alpha\beta$ -plots cover a time window from 15 to 35 ms. All variables in Fig. 4 are mapped from abc to $\alpha\beta$, i.e. $\xi_{\alpha\beta} = K\xi_{abc}$.

output constraints are active would correspond to suboptimal solutions compared to the case where the constraints are not considered. As a result, the settling times of the power transients in Figs. 4(b) are slightly longer compared to Fig. 4(a).

Finally, it worth mentioning that when operating at steady-state—i.e., $P_{\text{in,ref}} = 1$ and $Q_{\text{in,ref}} = 0$ with $\text{pf} = 1$ —the output constraints are not activated, hence both MPC algorithms perform the same.⁶ In both cases, all output variables y_{abc} accurately track their sinusoidal reference values, despite operation at a switching frequency that is very close to the resonance one. The current and PCC voltage harmonic spectra produced by the proposed direct MPC are shown in Fig. 6. As can be seen, even though the harmonic energy for both $i_{g,abc}$ and $v_{\text{pcc},abc}$ is spread over a wide range of frequencies due to the variable switching frequency, the grid codes, such as the IEEE 519 and IEC 61000-2-4 standards, are met with $I_{g,\text{TDD}} \approx 1.25\%$ and $V_{\text{pcc},\text{TDD}} \approx 3.03\%$. For comparison purposes the spectra of conventional space vector modulation (SVM) are shown in the same figure, while considering the same f_{sw} . This is like having a simple closed-loop linear controller with a very low bandwidth [16]. As can be seen, the TDD values of both current and PCC voltage produced by SVM are higher than those obtained with MPC.

Conclusions

This paper presented a long-horizon direct MPC algorithm for an MV 3L-NPC converter connected to the grid via an LCL filter. The control problem formulation is augmented with output constraints that relate to physical limitations of the system components. The underlying optimization problem is formulated as an ILS problem where the output constraints are translated into input constraints that define a new feasible set. Subsequently, the employed sphere decoder finds the feasible point within the refined hypersphere in a computationally efficient manner. In doing so, operation within the safety operating region of the system is guaranteed without deteriorating its performance. As shown, the proposed approach

⁶Note that the constraint $\|\Delta u(\ell)\|_{\infty} \leq 1$ is activated and fully respected in both MPC formulations.

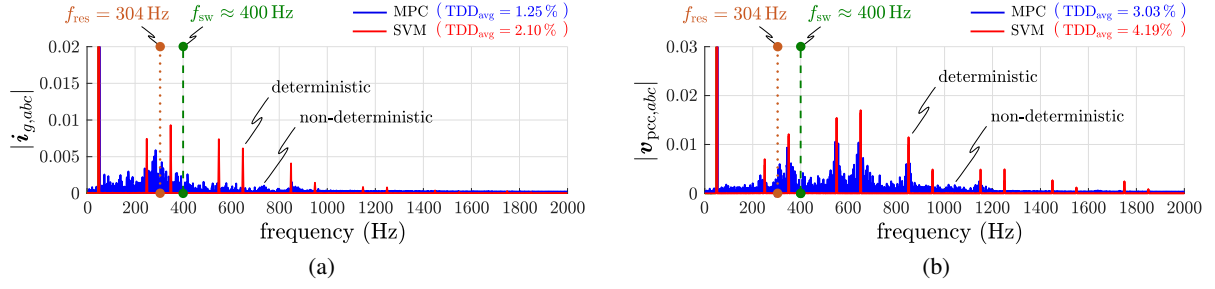


Fig. 6: Harmonic spectra of (a) $i_{g,abc}$, and (b) $v_{pcc,abc}$ in p.u. For both cases, the harmonics do not violate their respective limits (in terms of TDD and amplitude) imposed by the IEEE 519 and IEC 61000-2-4 standards.

achieves excellent steady-state and transient performance, while meeting the relevant grid codes despite the low switching frequency which is very close to the resonance one. Finally, thanks to the adopted long prediction horizon and the full-state information, an additional active damping loop is unnecessary, resulting in a simpler control architecture compared to conventional control strategies.

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