January 23, 2019

Hiring Manager

Micron

Dear Hiring Manager,

I am Sattanaathan Thayumanan and I am currently pursuing my Master’s in Computer Engineering at the University of Florida class of 2020. I have a keen interest in firmware development and system engineering. I believe that I will be a great fit for the role.

The University of Florida MS in ECE was a logical next step for me to grow in my career. My current graduate level coursework includes Advanced systems programing, Principles of computer system design, Virtual Computers, Reconfigurable computing and Computer Architecture using Python, VHDL, System C, and C.

One of the more interesting projects that I am currently working on is in Hardware Software co-validation – under the guidance of Professor Dr. Sandip Ray. I’m currently working on Hardware Software Co-validation, targeting firmware validation with JasperGold formal verification.

In my junior year of college, I did an internship with the Alstom Transport, Bangalore. I learned about different subsystems/system level integration and validation of Low Voltage Logics. I designed and developed a simulation which is used in system level validation with ControlBuild and NI LabVIEW.

My prior experience was in Software design and development at ALSTOM with JAVA and C++. My role primarily involved developing new features for the existing System and subsystem level software. I strongly believe I would be able to leverage on my programming knowledge in the role offered at our company. The role offered by our company is something that I feel is a perfect fit for me as I believe my prior experience is quite relevant.

I would be privileged to work amidst the top qualified professionals and advance together to achieve the mission and vision of the company alongside personal career development.

Sincerely,

Sattanaathan Thayumanan