



Documentation

IP Location

Show disabled ports

Component Name

axi\_dma\_0

☐ Enable Asynchronous Clocks (Auto)
 ☐ Enable Scatter Gather Engine
 ☐ Enable Micro DMA
 ☐ Enable Multi Channel Support
 ☐ Enable Control / Status Stream

Width of Buffer Length Register (8-26)

24

bits

Address Width (32-64)

32

bits

☒ Enable Read Channel
 

Number of Channels

1

Memory Map Data Width

32

Stream Data Width

8

Max Burst Size

256

☒ Allow Unaligned Transfers

☐ Enable Write Channel
 

Number of Channels

1

Memory Map Data Width

32

Stream Data Width

32

Max Burst Size

16

☐ Allow Unaligned Transfers
 ☐ Use Rlength In Status Stream