

Computer Architecture (COL 216)
II Semester 2019-2020

Assignment 8: Simulating a Pipelined Processor
Deadline : 1st April, 2020

In this assignment you will extend the simulator you wrote in Assignment 7 by introducing **PIPELINING** into the processor.

1. Use the pipelining structure and ideas for the MIPS processor we studied in class.
2. Stall the processor when any type of hazard is detected.
3. In your output, display the instruction that is active in each stage of the pipeline (in addition to the information you displayed in Assignment 7).

Other details of the simulation, input program, output display, etc., are as stated in Assignment 7.