

Laboratory 9

Title of the Laboratory Exercise: Inline assembly language programs for code optimisation

1. Introduction and Purpose of Experiment

Students will create C programs with inline assembly code for code optimisation

2. Aim and Objectives

Aim

To develop inline assembly language program for code optimisation

Objectives

At the end of this lab, the student will be able to

- Identify inline assembly language calls
- Explain optimization of program by exploiting architectural features in target computer
- Create C programs with inline assembly code

3. Experimental Procedure

1. Write algorithm to solve the given problem
2. Translate the algorithm to assembly language code
3. Run the assembly code in GNU assembler
4. Create a laboratory report documenting the work

4. Questions:

Develop a C program without inline assembly instructions and find out the code size and memory used for the program. Develop the C program with inline assembly instructions and find out the code size and memory used for the program. Compare the results.

5. Calculations/Computations/Algorithms

```
1 #include <stdio.h>
2 #define di(i) printf("\nDEBUG--##i " : %d#\n", i);
3
4 /* a = b + (c * d) */
5 int a, b = 1, c = 2, d = 3;
6
7 int main() {
8     di(b); di(c); di(d);
9     printf("\nBefore Operation:\n");
10    di(a);
11    __asm__( "movl c, %eax\n\t"
12            "movl d, %edx\n\t"
13            "mull %edx\n\t"
14            "add b, %eax\n\t"
15            "movl %eax, a\n\t");
16    printf("\nAfter Operation:\n");
17    di(a);
18    return 0;
19 }
```

Figure 0-1 Inline Assembly C Code

```
1 #include <stdio.h>
2 #define di(i) printf("\nDEBUG--##i " : %d#\n", i);
3
4 /* a = b + (c * d) */
5 int a, b = 1, c = 2, d = 3;
6
7 int main() {
8     di(b); di(c); di(d);
9     printf("\nBefore Operation:\n");
10    di(a);
11    a = b + (c * d);
12    printf("\nAfter Operation:\n");
13    di(a);
14    return 0;
15 }
```

Figure 0-2 Native C Code

```

1  .file    "assem.c"
2  .comm    a,4,4
3  .globl   b
4  .data
5  .align   4
6  .type    b, @object
7  .size    b, 4
8 b:
9  .long    1
10 .globl   c
11 .align   4
12 .type    c, @object
13 .size    c, 4
14 c:
15 .long    2
16 .globl   d
17 .align   4
18 .type    d, @object
19 .size    d, 4
20 d:
21 .long    3
22 .section .rodata
23 .LC0:
24 .string  "\nDEBUG--#b : %d#\n"
25 .LC1:
26 .string  "\nDEBUG--#c : %d#\n"
27 .LC2:
28 .string  "\nDEBUG--#d : %d#\n"
29 .LC3:
30 .string  "\nBefore Operation:"
31 .LC4:
32 .string  "\nDEBUG--#a : %d#\n"
33 .LC5:
34 .string  "\nAfter Operation:"
35 .text
36 .globl   main
37 .type    main, @function
38 main:
39 .LFB0:
40 .cfi_startproc
41 pushq    %rbp
42 .cfi_def_cfa_offset 16
43 .cfi_offset 6, -16
44 movq     %rsp, %rbp
45 .cfi_def_cfa_register 6
46 movl     b(%rip), %eax
47 movl     %eax, %esi
48 movl     $.LC0, %edi
49 movl     $0, %eax
50 call     printf
51 movl     c(%rip), %eax
52 movl     %eax, %esi
53 movl     $.LC1, %edi
54 movl     $0, %eax
55 call     printf
56 movl     d(%rip), %eax
57 movl     %eax, %esi
58 movl     $.LC2, %edi
59 movl     $0, %eax
60 call     printf
61 movl     $.LC3, %edi
62 call     puts
63 movl     a(%rip), %eax
64 movl     %eax, %esi
65 movl     $.LC4, %edi
66 movl     $0, %eax
67 call     printf
68 #APP
69 # 11 "assem.c" 1
70 movl     c, %eax
71 movl     d, %edx
72 mull     %edx
73 add     b, %eax
74 movl     %eax, a
75
76 # 0 "" 2
77 #NO_APP
78 movl     $.LC5, %edi
79 call     puts
80 movl     a(%rip), %eax
81 movl     %eax, %esi
82 movl     $.LC4, %edi
83 movl     $0, %eax
84 call     printf
85 movl     $0, %eax
86 popq     %rbp
87 .cfi_def_cfa 7, 8
88 ret
89 .cfi_endproc
90 .LFE0:
91 .size    main, .-main
92 .ident   "GCC: (Ubuntu 5.4.0-6ubuntu1~16.04.11) 5.4.0 20160609"
93 .section .note.GNU-stack,"",@progbits
94

```

Figure 0-3 Inline Assembly assembly code

```

1  .file   "cprog.c"
2  .comm   a,4,4
3  .globl  b
4  .data
5  .align  4
6  .type   b, @object
7  .size   b, 4
8  b:
9  .long   1
10 .globl  c
11 .align  4
12 .type   c, @object
13 .size   c, 4
14 c:
15 .long   2
16 .globl  d
17 .align  4
18 .type   d, @object
19 .size   d, 4
20 d:
21 .long   3
22 .section .rodata
23 .LC0:
24 .string "\nDEBUG--#b : %d#\n"
25 .LC1:
26 .string "\nDEBUG--#c : %d#\n"
27 .LC2:
28 .string "\nDEBUG--#d : %d#\n"
29 .LC3:
30 .string "\nBefore Operation:"
31 .LC4:
32 .string "\nDEBUG--#a : %d#\n"
33 .LC5:
34 .string "\nAfter Operation:"
35 .text
36 .globl  main
37 .type   main, @function
38 main:
39 .LFB0:
40 .cfi_startproc
41 pushq   %rbp
42 .cfi_def_cfa_offset 16
43 .cfi_offset 6, -16
44 movq    %rsp, %rbp
45 .cfi_def_cfa_register 6
46 movl    b(%rip), %eax
47 movl    %eax, %esi
48 movl    $.LC0, %edi
49 movl    $0, %eax
50 call    printf
51 movl    c(%rip), %eax
52 movl    %eax, %esi
53 movl    $.LC1, %edi
54 movl    $0, %eax
55 call    printf
56 movl    d(%rip), %eax
57 movl    %eax, %esi
58 movl    $.LC2, %edi
59 movl    $0, %eax
60 call    printf
61 movl    $.LC3, %edi
62 call    puts
63 movl    a(%rip), %eax
64 movl    %eax, %esi
65 movl    $.LC4, %edi
66 movl    $0, %eax
67 call    printf
68 movl    c(%rip), %edx
69 movl    d(%rip), %eax
70 imull   %eax, %edx
71 movl    b(%rip), %eax
72 addl    %edx, %eax
73 movl    %eax, a(%rip)
74 movl    $.LC5, %edi
75 call    puts
76 movl    a(%rip), %eax
77 movl    %eax, %esi
78 movl    $.LC4, %edi
79 movl    $0, %eax
80 call    printf
81 movl    $0, %eax
82 popq    %rbp
83 .cfi_def_cfa 7, 8
84 ret
85 .cfi_endproc
86 .LFE0:
87 .size   main, .-main
88 .ident  "GCC: (Ubuntu 5.4.0-6ubuntu1~16.04.11) 5.4.0 20160609"
89 .section .note.GNU-stack,"",@progbits
90

```

Figure 0-4 Native C, assembly code

6. Presentation of Results

```
shadowLeaf@SHADOWLEAF-ROG > /mnt/d/University-Work/01-MPLab/lab-09
./cprog

DEBUG--#b : 1#
DEBUG--#c : 2#
DEBUG--#d : 3#

Before Operation:
DEBUG--#a : 0#

After Operation:
DEBUG--#a : 7#
shadowLeaf@SHADOWLEAF-ROG > /mnt/d/University-Work/01-MPLab/lab-09
```

Figure 0-5 Output of Native C Code

```
shadowLeaf@SHADOWLEAF-ROG > /mnt/d/University-Work/01-MPLab/lab-09
./assem

DEBUG--#b : 1#
DEBUG--#c : 2#
DEBUG--#d : 3#

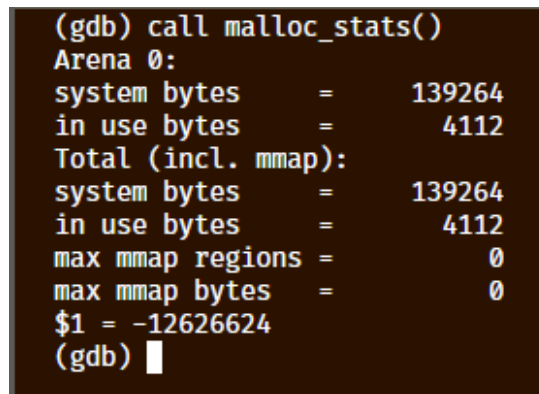
Before Operation:
DEBUG--#a : 0#

After Operation:
DEBUG--#a : 7#
shadowLeaf@SHADOWLEAF-ROG > /mnt/d/University-Work/01-MPLab/lab-09
```

Figure 0-6 Output of Inline Assembly

```
(gdb) call malloc_stats()
Arena 0:
system bytes      =    139264
in use bytes      =     4112
Total (incl. mmap):
system bytes      =    139264
in use bytes      =     4112
max mmap regions  =         0
max mmap bytes    =         0
$2 = -12626624
(gdb)
```

Figure 0-7 Native C Code Memory Usage



```
(gdb) call malloc_stats()
Arena 0:
system bytes      =      139264
in use bytes      =         4112
Total (incl. mmap):
system bytes      =      139264
in use bytes      =         4112
max mmap regions  =          0
max mmap bytes    =          0
$1 = -12626624
(gdb) 
```

Figure 0-8 Inline Assembly Memory Usage

7. Analysis and Discussions

The format of basic inline assembly is very much straight forward. Its basic form is

```
asm("assembly code");
```

Example.

```
asm("movl %ecx %eax"); /* moves the contents of ecx to eax */

__asm__("movb %bh (%eax)"); /*moves the byte from bh to the memory pointed by eax
*/
```

We can use `__asm__` if the keyword `asm` conflicts with something in our program. If we have more than one instructions, we write one per line in double quotes, and also suffix a `'\n'` and `'\t'` to the instruction. This is because gcc sends each instruction as a string to `as(GAS)` and by using the newline/tab we send correctly formatted lines to the assembler.

If in our code we touch (ie, change the contents) some registers and return from `asm` without fixing those changes, something bad is going to happen. This is because GCC have no idea about the changes in the register contents and this leads us to trouble, especially when compiler makes some optimizations. It will suppose that some register contains the value of some variable that we might have changed without informing GCC, and it continues like nothing happened. What we can do is either use those instructions having no side effects or fix things when we quit or wait for something to crash. This is where we want some extended functionality. Extended `asm` provides us with that functionality.

8. Conclusions

Inline assembly can be used in C programs to write in low level language and have direct access to the CPU registers, this gives more control over the register's memory.

If we compare the code size of the trans-piled assembly code from the c source code, we find that the inline assembly code takes a greater number of lines in assembly than the native c code trans-piled to assembly code.

The memory usage of both the programs are similar with little to no differences in memory usage.

9. Comments

1. Limitations of Experiments

The Experiment is limited to a very simple C program, hence concluding the results in difficult for the same.

2. Limitations of Results

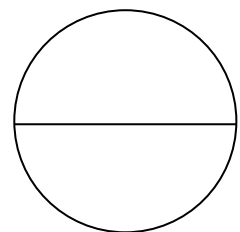
Since the operations performed were very simple, there is little to no differences in the two codes, the inline assembly and native c code, although for complex codes, the results might differ.

3. Learning happened

We learnt how to disassemble a C code into its assembly code using `gcc -S filename.c` command.

4. Recommendations

To have a better comparison between the two types of codes, take a larger and complex c code and trans-pile it to its assembly code.



Signature and date

Marks