# Laboratory 3

**Gate Level Minimization using Karnaugh Maps**

1. Introduction and Purpose of Experiment

Students will learn to minimize Boolean Expressions using K-Maps and then simulate and implement them.

1. Aim and Objectives

**Aim:** To apply K-Maps to minimize Boolean expressions

**Objectives:** At the end of this lab, the student will be able to

* Apply K-Maps to simplify three- and four-variable Boolean Expressions
* Implement minimized expressions using basic and universal gates

1. Experimental Procedure
   1. Minimize the following expressions using K-Maps.
   2. Draw truth tables and circuit diagrams for the minimized expressions in 3(a) considering:

* 3(a) 1 and 3(a) 2: Use basic gates
* 3(a) 3 and 3(a) 4: Use NAND gates
* 3(a) 5: Use basic gates
* 3(a) 6 and 3(a) 7: Use NOR gates
  1. Use Logisim to generate truth tables and circuit diagrams for the expressions in 3(a).
  2. Implement the minimized expressions of 3(a)3 to 3(a)6. Show the output to the course leader.
  3. With an example, show why incorrect grouping in K-Maps may result in a non-minimized expression

Your document should include:

* Handwritten truth tables and circuit diagrams for the expressions
* Logisim screenshots
* Answer to 3(e)

3.a.1.

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| --- | --- | --- |
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3.a.2.

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| --- | --- | --- |
|  |  |  |

3.a.3.

|  |  |  |
| --- | --- | --- |
|  |  |  |

3.a.4.

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|  |

3.a.5.

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| --- | --- | --- |
|  |  |  |

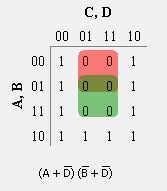
3.a.6.

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3.a.7.

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3.e Let’s take the example of 3.a.4, where the K-Map is :



Since it is a POS form the are grouped, forming two groups of zeros, and the minimized expression is

If instead of that, the zeros were grouped as groups of zeros each, we would have 3 terms in our expression, which would then be , which is not minimized, doing further Boolean simplification the actual minimized expression is obtained and it reduces to .

Hence it can be concluded that incorrect grouping in K-Map can lead to unnecessary terms in the expression, that have to be then further manually simplified.