

#	Subject	Score	Bonus	Issue
HW#01	Register File , Arithmetic Logic Unit (ALU), Multiplier	35	3	3.14, 2023
HW#02	Memory model, RISC-V Instruction, Decoder	35	2	3.21, 2023
HW#03	Program Counter, Branch Instructions	35	2	3.28, 2023
HW#04	Load, Store Instructions, RISC-V Core	30	0	4.04, 2023
HW#05	AMBA Bus	40	3	4.11, 2023
HW#06	IO, LCD Drive, Image processing application	30	2	4.18, 2023
HW#07	SOC System Integration	30	0	4.25, 2023
HW#08	CNN for Super-resolution, Reference SW	30	2	5.02, 2023
HW#09	MAC Kernel, Convolutional kernels	35	2	5.09, 2023
HW#10	Sliding window, CNN Accelerator top file	30	2	5.16, 2023
HW#11	CNN Accelerator, buffers	30	2	5.23, 2023
HW#12	CNN Accelerator and DMA	40	0	5.30, 2023
Project	CNN Accelerator Design and Optimization	200	0	5.30, 2023
Exam	Final exam	200	0	6.20, 2023

Policy for a late submission:

1. Total **three days** without penalty
2. After three late days are used, the score of an assignment is decided as follows:
 - a. One-day late submission: 80%
 - b. Two-day late submission: 50%
 - c. After that: 0
3. The policy is applied for both weekly assignments and the final project.

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Due
3.20, 2023
3.27, 2023
4.03, 2023
4.10, 2023
4.17, 2023
4.24, 2023
5.01, 2023
5.08, 2023
5.15, 2023
5.22, 2023
5.29, 2023
6.05, 2023
6.22, 2023
6.20, 2023