

# Saumya Shah

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## Education

<b>The University of Texas at Dallas</b>	<b>Dallas, Texas</b>	<b>January 2023 – December 2024</b>
<ul style="list-style-type: none"><li>• Master of Science in Electrical Engineering <b>GPA: 3.78/4.00</b></li><li>• <b>Relevant Coursework:</b> Microprocessor and Embedded Systems, VLSI Design, Computer Architecture, Advanced Digital Logic, Functional Verification, Testing and Testable Design, ASIC Design, Reconfigurable Systems</li></ul>		
<b>Nirma University</b>	<b>Ahmedabad, India</b>	<b>July 2018 – June 2022</b>
<ul style="list-style-type: none"><li>• Bachelor of Engineering in Electronics and Communication Engineering.</li></ul>		

## Technical Skills

- **Languages:** C++, C, Python
- **Tools & Technologies:** STM32CubeIDE, PlatformIO, KiCAD, Arduino IDE, MQTT(Mosquitto), Git
- **Concepts:** Embedded Firmware Development, ARM Cortex-M Microcontrollers, Event-Driven Firmware Architecture, OTA Updates, Real-Time Data Acquisition
- **Protocols:** I2C, SPI, UART, MQTT, HTTP, 1-Wire, Wi-Fi, Bluetooth

## Work Experience

<b>Embedded Engineer</b>	<b>Winwinlabs</b>	<b>April 2025 – Present</b>
<ul style="list-style-type: none"><li>• Developed a multi-function firmware platform on ESP32-S3 for real-time acquisition and streaming of DS18B20 and DHT22 sensor data via MQTT and WebSockets to a responsive browser-based interface.</li><li>• Integrated OTA firmware updates, web UI delivery from LittleFS, and persistent sensor configuration storage using NVS for seamless remote management.</li><li>• Architected an event-driven firmware framework enabling concurrent sensor polling, network communication, and user interaction with minimal latency and high reliability across varied IoT deployments.</li></ul>		
<b>Research and Development Intern</b>	<b>Thruster, India</b>	<b>May 2021 – July 2021</b>
<ul style="list-style-type: none"><li>• Devised a circuit using an Arm Cortex M4 based STM32F4 controller to deliver signals to the motor driver and collect coil current and voltage data for future analysis.</li><li>• Revised the SRM motor driver assessment process by designing and deploying tailored algorithms that measured performance attributes, leading to a 30% reduction in evaluation time and improved decision-making capabilities.</li></ul>		

## Projects

<b>Sobel Edge-Detection for Reconfigurable Computing</b>	<b>September 2024 – December 2024</b>
<ul style="list-style-type: none"><li>• Designed and implemented a Sobel filter on FPGA using HLS for hardware and ARM programming for software, enabling efficient image processing.</li><li>• Integrated the FPGA hardware accelerator with an ARM-based Hard Processor System (HPS) to achieve optimal system performance, reducing processing latency and improved detection efficiency by 14%.</li></ul>	
<b>Analysis of Branch Predictors and Cache Associativity</b>	<b>September 2023 – December 2023</b>
<ul style="list-style-type: none"><li>• Conducted a comprehensive analysis of diverse cache design strategies, assessing the influence on x86 CPU performance. Subsequently, derived an optimal configuration yielding the lowest Cycle Per Instruction (CPI) while maintaining cost efficiency.</li><li>• Evaluated the impact of changing variables on branch predictors through experimentation using advanced features from GEM5.</li><li>• Generated python scripts to improve the benchmarks' execution speed by 30%.</li></ul>	
<b>Sensor Data Acquisition and Cloud Integration</b>	<b>October 2021 – December 2021</b>
<ul style="list-style-type: none"><li>• Developed a concurrent data acquisition system on STM32F7 running FreeRTOS, efficiently collecting data from multiple sensors.</li><li>• Implemented data communication between STM32F7 and ESP8266 using USART to transfer sensor data.</li><li>• Optimized system resource management, ensuring smooth data collection and transmission in a multitasking environment.</li></ul>	
<b>4-Wheel Holonomic System</b>	<b>July 2019 – May 2020</b>
<ul style="list-style-type: none"><li>• Built a four-wheeled holonomic robot on STM32F4 and Arduino, developing SPI, I<sup>2</sup>C, and USART drivers to interface motor controllers, sensors, and a PS4 wireless controller.</li><li>• Designed fuzzy logic algorithms for precise maneuverability and semi-autonomous operation, achieving 90% accuracy.</li><li>• Conducted JTAG-based debugging and register-level analysis to validate data flow, optimize memory usage, and ensure stable real-time performance.</li></ul>	
<b>Custom PCB Design for Embedded and Robotic Applications</b>	<b>October 2018 – December 2021</b>
<ul style="list-style-type: none"><li>• Designed development boards featuring the LPC17xx microcontroller for embedded system development.</li><li>• Created custom PCB solutions integrating the STM32F4 Discovery board for advanced robotic applications.</li></ul>	