

ROUTER

INPUT PORTS

Data_0



Flow_cntrl_0



Data_1



Flow_cntrl_1



Data_M

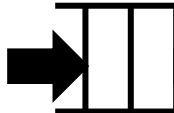


Flow_cntrl_M

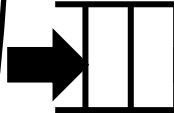
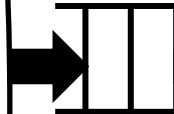


Routing

Flit
Buffers



Allocation



Switch



OUTPUT PORTS

Data_0



Flow_cntrl_0



Data_1



Flow_cntrl_1



Data_N



Flow_cntrl_N

