

CSE-420 Project-2_Report

1) Problem 1

a. Question-1

- i. Statistics for the benchmarks are enlisted in **filename: q1_stats_table**
- ii. Non constant statistics between two simulation modes are as following :
Number of ticks simulated
Number of cpu cycles simulated
Number of busy cycles
These are highlighted red in filename: q1_stats_table.

b. Question-2

From the stats file we can observe that Number of ticks simulated, Number of cpu cycles simulated, Number of busy cycles of timing model are approx. 100 times that of atomic model. Hence, we can say that atomic model has higher speed compared to a timing model.

2) Problem 2

a. Question-1

- i. CPU model selected : Timing

Explanation: Even though atomic model is faster than timing but timing model is more detailed CPU modelling. So the accuracy of timing model is more compared to atomic. In case of atomic model, Memory accesses are atomic while in case of timing, CPU waits until memory access returns, i.e., they use timing path. By choosing timing model we can observe and analyse the accurate program statistics more effectively than atomic model.

- ii. Working binary and assembly files for optimization with -O0 and -O3.
Folder name: final q2-1

- iii. Comparison:
Folder name: final q2-1
Filename: O0-O3_stats_comparison_optimization

- Number of instructions to be simulated and number of cpu cycles are less for -O3 optimization.
- Number of integer registers read/write is lesser in -O3 optimization.
- Number of load/store instructions are reduced.

Hence, Better optimization is achieved by -O3 compared to -O0 in terms of speed and time.

Original vs -O3>

12. main:	13. main:
13. .frame \$15,32,\$26,0	14. .frame \$30,48,\$26,0
14. .mask 0x4008000,-32	15. .mask 0x4003e00,-48
15. ldah \$29,0(\$27) !gpdisp!1	16. ldah \$29,0(\$27) !gpdisp!1
16. lda \$29,0(\$29) !gpdisp!1	17. lda \$29,0(\$29) !gpdisp!1
17. \$main.ng:	18. \$main.ng:
18. lda \$30,-32(\$30)	19. lda \$30,-48(\$30)
19. stq \$26,0(\$30)	20. mov \$31,\$4
20. stq \$15,8(\$30)	21. lda \$28,A(\$29) !literal
21. mov \$30,\$15	22. stq \$26,0(\$30)
22. .prologue 1	23. stq \$9,8(\$30)
23. stl \$31,16(\$15)	24. stq \$10,16(\$30)
24. \$L2:	25. stq \$11,24(\$30)
25. ldl \$1,16(\$15)	26. stq \$12,32(\$30)
26. cmple \$1,9,\$1	27. stq \$13,40(\$30)
27. beq \$1,\$13	28. .prologue 1
28. stl \$31,20(\$15)	
29. \$L5:	
30. ldl \$1,20(\$15)	
31. cmple \$1,9,\$1	
32. beq \$1,\$14	
33. ldq \$4,A(\$29) !literal	
34. ldl \$2,16(\$15)	
35. ldl \$3,20(\$15)	
36. mov \$2,\$1	
37. s4addq \$1,0,\$1	
38. addq \$1,\$2,\$1	
39. addq \$1,\$1,\$1	
40. addq \$1,\$3,\$1	
41. s4addq \$1,0,\$1	
42. addq \$1,\$4,\$3	
43. ldl \$2,16(\$15)	
44. ldl \$1,20(\$15)	
45. addl \$2,\$1,\$1	
46. stl \$1,0(\$3)	
47. ldl \$1,20(\$15)	
48. addl \$1,\$1,\$1	

The label L20 has redundant code which is repeated and hence removed completely after optimizations which reduces code length and improves characteristics of model.

Original vs -O0>

11. .ent main	12. .ent main
12. main:	13. main:
13. .frame \$15,32,\$26,0	14. .frame \$30,48,\$26,0
14. .mask 0x4008000,-32	15. .mask 0x4003e00,-48
15. ldah \$29,0(\$27) !gpdisp!1	16. ldah \$29,0(\$27) !gpdisp!1
16. lda \$29,0(\$29) !gpdisp!1	17. lda \$29,0(\$29) !gpdisp!1
17. \$main.ng:	18. \$main.ng:
18. lda \$30,-32(\$30)	19. lda \$30,-48(\$30)
19. stq \$26,0(\$30)	20. mov \$31,\$4
20. stq \$15,8(\$30)	21. ldq \$28,A(\$29) !literal
21. mov \$30,\$15	22. stq \$26,0(\$30)
22. .prologue 1	23. stq \$9,8(\$30)
23. stl \$31,16(\$15)	24. stq \$10,16(\$30)
24. \$L2:	25. stq \$11,24(\$30)
25. ldl \$1,16(\$15)	26. stq \$12,32(\$30)
26. cmple \$1,9,\$1	27. stq \$13,40(\$30)
27. beq \$1,\$13	28. .prologue 1
28. stl \$31,20(\$15)	
29. \$L5:	
30. ldl \$1,20(\$15)	
31. cmple \$1,9,\$1	
32. beq \$1,\$14	
33. ldq \$4,A(\$29) !literal	
34. ldl \$2,16(\$15)	
35. ldl \$3,20(\$15)	
36. mov \$2,\$1	
37. s4addq \$1,0,\$1	
38. addq \$1,\$2,\$1	
39. addq \$1,\$1,\$1	
40. addq \$1,\$3,\$1	
41. s4addq \$1,0,\$1	
42. addq \$1,\$4,\$3	
43. ldl \$2,16(\$15)	
44. ldl \$1,20(\$15)	
45. addl \$2,\$1,\$1	
46. stl \$1,0(\$3)	
47. ldl \$1,20(\$15)	
48. addl \$1,\$1,\$1	

The label L20 has redundant code which is repeated and hence removed partially (1/2 of the iterations) after optimizations which reduces code length and improves characteristics of model.

Explanation:

No. of lines of code in assembly of original= 410

No. of lines of code with O0 optimization in assembly= 217

No. of lines of code with O3 optimization in assembly = 131

-o = The compiler tries to reduce the code size and execution time without performing any optimization that take a great deal of compilation time.

-O0= Reduced compilation time and make debugging produced the expected results.

-O3= Optimize even more.

Hence, we observe that with optimization the length of the assembly code decreases.

b. Question-2

i. Working unrolled files

Folder name: final_q2-2

ii. Comparison:

Folder name: final_q2-2

Filename: unrolling_stats_comparison

We observed that number of conditional control instructions decreases as the level of unrolling increases. Hence, conditional branch instruction is executed lesser times. As we unroll the k-loop, the length of the code increases and the execution time goes on increasing with more unrolling.

Original vs unrolling 2-times>

75. \$L27: 76. ldl \$11,0(\$4)	77. \$L27: 78. ldl \$18,0(\$6) 79. ldl \$19,0(\$7) 80. ldl \$16,4(\$6)
77. bis \$31,\$31,\$31 78. addl \$6,1,\$6 79. ldl \$12,0(\$5) 80. cmple \$6,9,\$9 81. lda \$4,4(\$4) 82. mull \$11,\$12,\$10 83. lda \$5,40(\$5) 84. addq \$7,\$10,\$1	81. bis \$31,\$31,\$31 82. addl \$8,2,\$8 83. ldl \$17,40(\$7) 84. mull \$18,\$19,\$12 85. cmple \$8,9,\$9 86. lda \$6,8(\$6) 87. lda \$7,80(\$7) 88. mull \$16,\$17,\$11 89. addq \$22,\$12,\$10 90. addq \$10,\$11,\$1
85. cpvys \$f31,\$f31,\$f31 86. bis \$31,\$1,\$7 87. bne \$9,\$L27 88. addl \$22,1,\$22 89. stl \$1,0(\$8) 90. cmple \$22,9,\$16 91. cpvys \$f31,\$f31,\$f31 92. lda \$8,4(\$8) 93. bne \$16,\$L28 94. addl \$24,1,\$24 95. cmple \$24,9,\$17 96. bne \$17,\$L29	91. cpvys \$f31,\$f31,\$f31 92. bis \$31,\$1,\$22 93. bne \$9,\$L27 94. addl \$24,1,\$24 95. stl \$1,0(\$23) 96. cmple \$24,9,\$20 97. cpvys \$f31,\$f31,\$f31 98. lda \$23,4(\$23) 99. bne \$20,\$L28 100. addl \$28,1,\$28 101. cmple \$28,9,\$22 102. bne \$22,\$L29
97. mov \$31,\$12 98. ldah \$11,\$LC0(\$29) !gprelhigh	103. mov \$31,\$12 104. ldah \$11,\$LC0(\$29) !gprelhigh

Original vs unrolling 5-times>

75. \$L27:		76. \$L27:	
76. ldl \$11,0(\$4)		77. ldl \$23,0(\$24)	
77. bis \$31,\$31,\$31		78. ldl \$10,0(\$25)	
78. addl \$6,1,\$6		79. ldl \$9,40(\$25)	
79. ldl \$12,0(\$5)		80. ldl \$8,4(\$24)	
80. cmple \$6,9,\$9		81. mull \$23,\$10,\$7	
81. lda \$4,4(\$4)		82. ldl \$5,8(\$24)	
82. mull \$11,\$12,\$10		83. ldl \$3,80(\$25)	
83. lda \$5,40(\$5)		84. ldl \$2,12(\$24)	
84. addo \$7,\$10,\$1		85. ldl \$6,120(\$25)	
85. cpys \$f31,\$f31,\$f31		86. ldl \$26,16(\$24)	
86. bis \$31,\$1,\$7		87. addl \$0,5,\$0	
87. bne \$9,\$L27		88. ldl \$27,160(\$25)	
88. addl \$22,1,\$22		89. cmple \$0,9,\$23	
89. stl \$1,0(\$8)		90. lda \$24,20(\$24)	
90. cmple \$22,9,\$16		91. lda \$25,200(\$25)	
		92. mull \$8,\$9,\$4	
		93. addq \$28,\$7,\$1	
		94. mull \$5,\$3,\$28	
		95. addq \$1,\$4,\$22	
		96. mull \$2,\$6,\$12	
		97. addq \$22,\$28,\$11	
		98. mull \$26,\$27,\$10	
		99. addq \$11,\$12,\$9	
		100. addq \$9,\$10,\$1	
		101. bis \$31,\$1,\$28	
		102. bne \$23,\$L27	
		103. addl \$20,1,\$20	
		104. stl \$1,0(\$21)	
		105. cmple \$20,9,\$24	
91. cpys \$f31,\$f31,\$f31		106. cpys \$f31,\$f31,\$f31	
92. lda \$8,4(\$8)		107. lda \$21,4(\$21)	
93. bne \$16,\$L28		108. bne \$24,\$L28	
94. addl \$24,1,\$24		109. addl \$18,1,\$18	
95. cmple \$24,9,\$17		110. cmple \$18,9,\$19	
96. bne \$17,\$L29		111. bne \$19,\$L29	
97. mov \$31,\$12		112. mov \$31,\$12	
98. ldah \$11,\$LC0(\$29)	!gprelhigh	113. ldah \$11,\$LC0(\$29)	!gprelhigh

The length of the L27 loop is increased as no of instructions in branch loop is increased so branch is executing lesser times.

Explanation:

Therefore, we can say that more we unroll the loop lesser will be cycles per instruction, i.e. CPI. Because this rescheduled and unrolled loop program will hit branch instruction lesser times compared to original one. More the unrolling lesser times it will come to execute branch instruction.

When we unroll the k-loop 2-times it will reduce overall stalls for that loop. The overall stalls can further be reduced using more unrolling so is the case with unrolling 5-times. But again it will be limited by no. of registers we have.