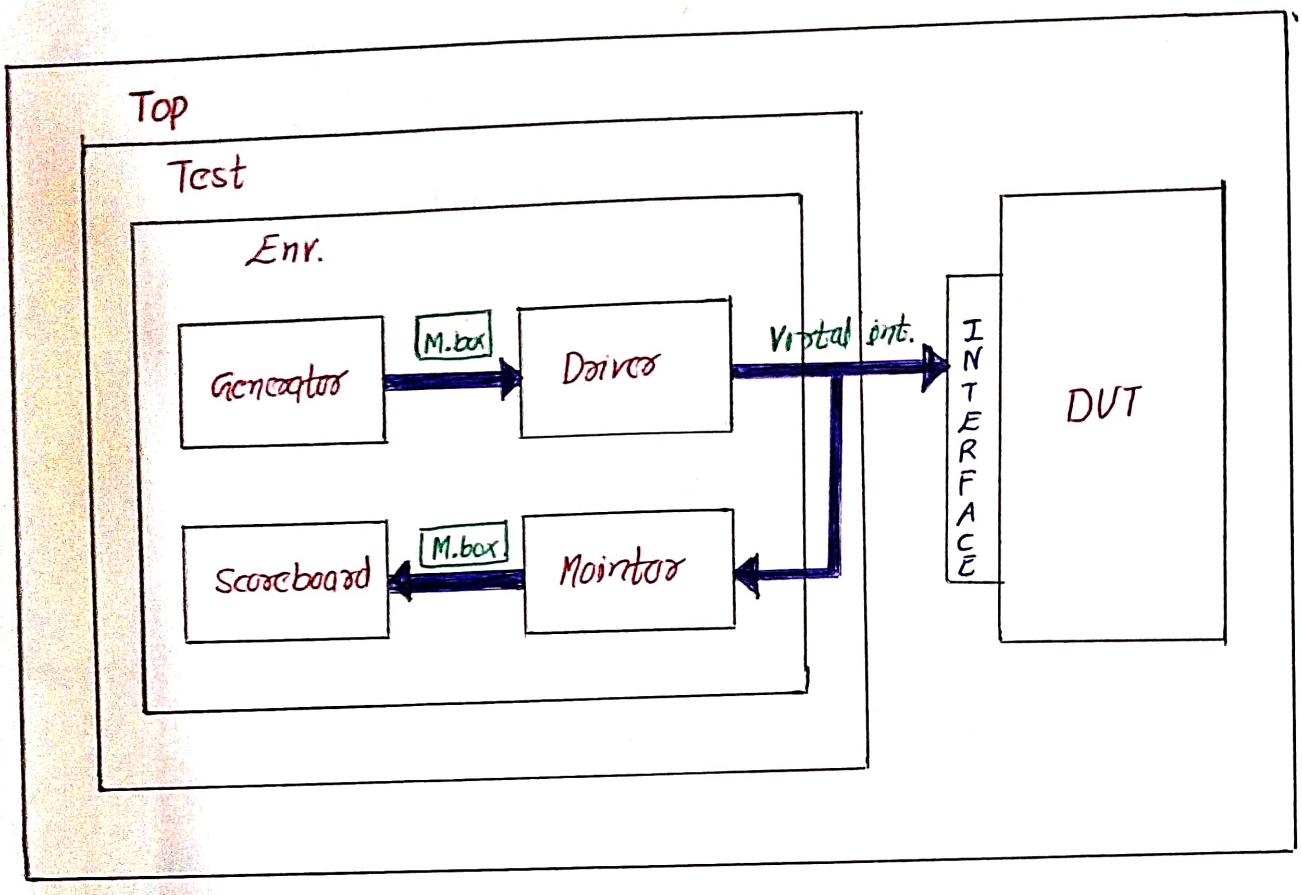

⇒ We are going to send information from test to DUT and DUT will form perform output, according to output we can decide design is correct or not.

⇒ Let's taking example of Half adder, which have two input (A, B) and output (sum, Carray).

⇒ All the input should be declare as rand keyword.

⇒ Transcation contain inuput as "rand".

Generator :

⇒ Generator will generate randomise stimulus coming for transcation.

⇒ After randomisation generator will put information in "Mailbox".

Driver :

⇒ Driver will take information from "Mailbox".

⇒ It will convert packet-level data into pin-level data.

⇒ Now, driver will sends this information via virtual interface.

DUT :

⇒ DUT will take this information and generate output.

⇒ After that output send to monitor

Monitor :

⇒ Monitor will convert pin-level data into packet-level data.

⇒ It will sample output and send sampled to mailbox.

Scoreboard :

⇒ Scoreboard will compare generated output with refrence model, if output matches the design output then our design correct otherwise bug.