

Saurabh Kumar

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About

Dedicated B.Tech student in Electronics and Communication Engineering with a focus on avionics. Interests span hardware prototyping, FPGA design, IoT development, and embedded systems, with experience in C++, Verilog, and MATLAB. Passionate about applying technology to create impactful solutions in space and embedded systems.

Education

B.Tech in ECE	Indian Institute of Space Science and Technology, Thiruvananthapuram	Nov 2022 – May 2026
	• CGPA: 8.04/10	
Senior Sec. (Class XII, CBSE)	Gyansthali, Gaya	2019 - 2021
	• Percentage: 93.2%	
Secondary (Class X, CBSE)	Elegant Public School, Gaya	2015 - 2019
	• Percentage: 98.4%	

Experience

SAC, ISRO , Intern	Ahmedabad
• Electrical testing and characterization of SAMRH707 Microcontroller	Jun 2025 – Jul 2025
• Resource allocation, pin mappings and IG-XL test program development for Device Interface Board (DIB) to interface the DUT with the Automated Testing Equipment (ATE)	
• Custom schematic design of DIB for interfacing of DUT with the ATE in CADSTAR	
Robotics Club, IIST , Vice President	IIST, TVM
	Feb 2024 – Oct 2024
College Fests , Volunteer	IIST, TVM
• Organizer of technical events like Maze Solver, Robosoccer, C-Cubed	Oct 2023, Oct 2024
• Conducted robotics workshop for schools in TVM for Techfest, Cultural fest and IEEE	

Projects

Quiz Buzzer	
• Design of quiz buzzer using 555-timer as part of Analog Electronics course	
Mini Bots Development	
• Development of robotics projects including WiFi-controlled Car, Maze Solver bot and Line Follower	
8-bit CPU	GitHub link
• Development of 8-bit processor in Verilog and its implementation on Xilinx PYNQ FPGA	
Sanjog	App Hardware App-Source
• Development of a hardware-based matchmaking device using ESP32 and its mobile app	
Handwriting Generator	WebApp Model
• Development of a GAN model for the generation of custom handwriting	

Skills

Languages: C++, Python

Technologies: MATLAB, Verilog, Arduino, 8085, 8086, React